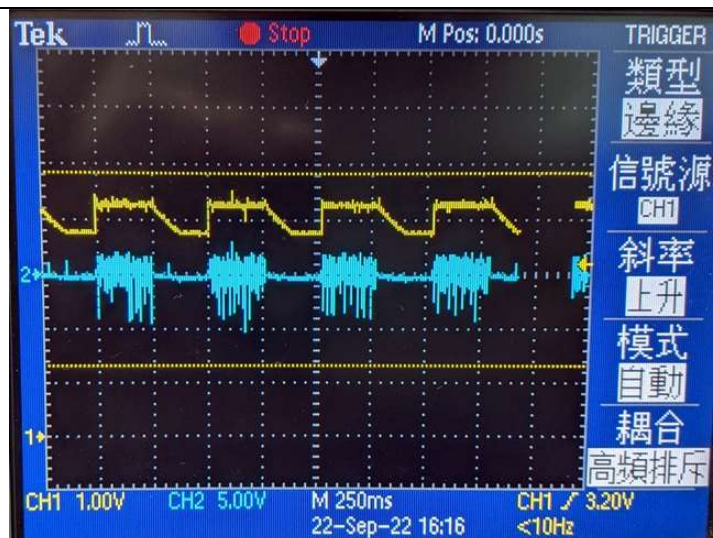
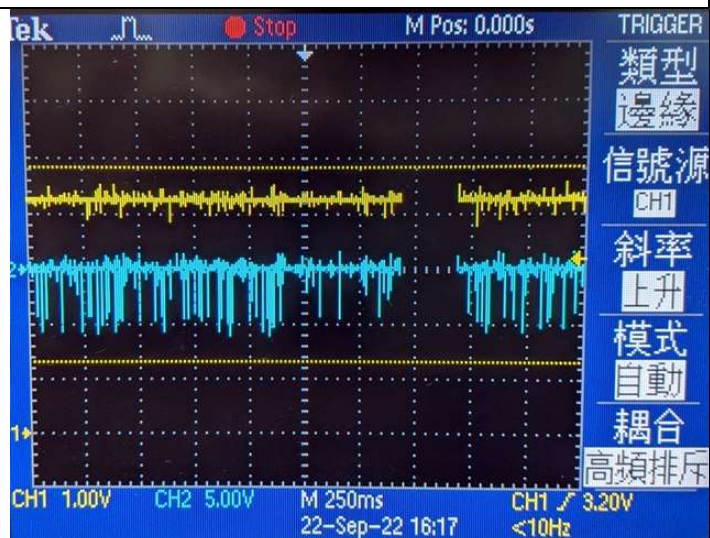


When the output voltage is unstable (no load and light load)

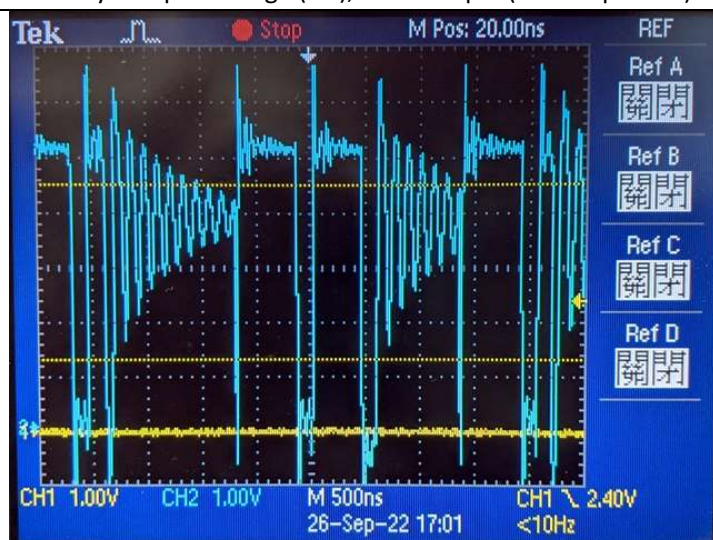


CH1: Vsys output voltage (DC), CH2: SW pin (AC component)

When the output voltage is stable (load current $\geq 1.15A$)



CH1: Vsys output voltage (DC), CH2: SW pin (AC component)



SW pin waveform is unstable (DC)



SW pin waveform is stable, edge ringing is obvious (DC)

Register settings

	lithium battery	Adapter	Adapter + lithium battery
REG00	0x1f	0x17	0x17
REG01	0x18	0x18	0x18
REG02	0xa2	0xa2	0xa2
REG03	0x22	0x22	0x22
REG04	0x58	0x58	0x58
REG05	0x8f	0x8f	0x8f
REG06	0x66	0x66	0x66
REG07	0x4c	0x4c	0x4c
REG08	0x0	0x74	0x74
REG09	0x0	0x0	0x0
REG0A	0x0	0xc0	0xc0
REG0B	0x11	0x11	0x11

DC IN & Battery Charger

