# bq25892 : Set BATFET_DIS bit during /QON low

**Fig.1**

**full system reset normal operation**
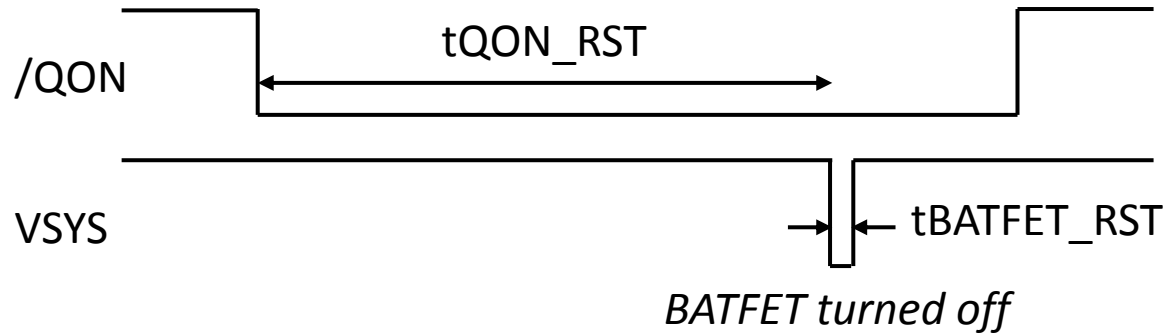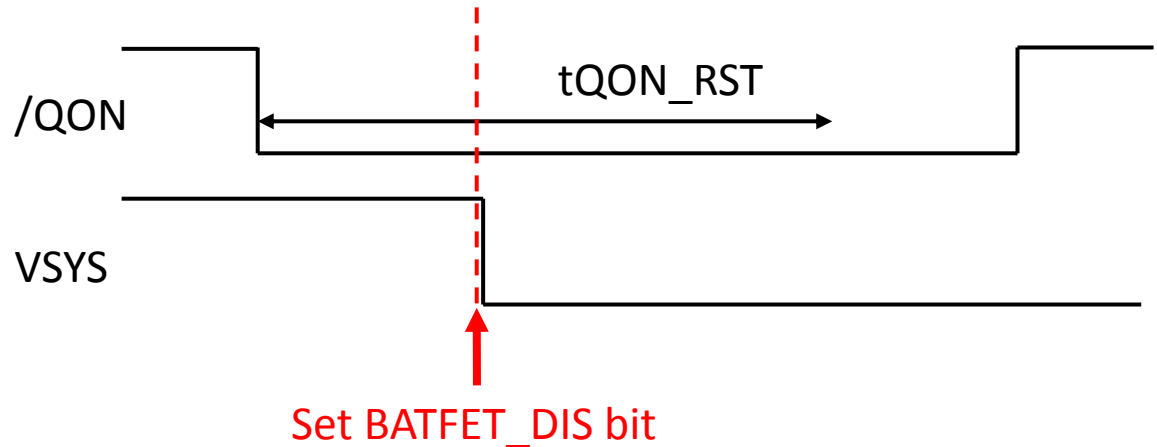
/QON

tQON_RST

VSYS

tBATFET_RST

*BATFET turned off*

**Fig.2**

**Set BATFET_DIS bit during /QON low**

/QON

tQON_RST

VSYS

Set BATFET_DIS bit

[Question]
Is BATFET turned off by set BATFET_DIS bit during /QON low to enable full system reset like Fig.2?
Or is BATFET_DIS bit ignored and enabled full system reset after tQON_RST?