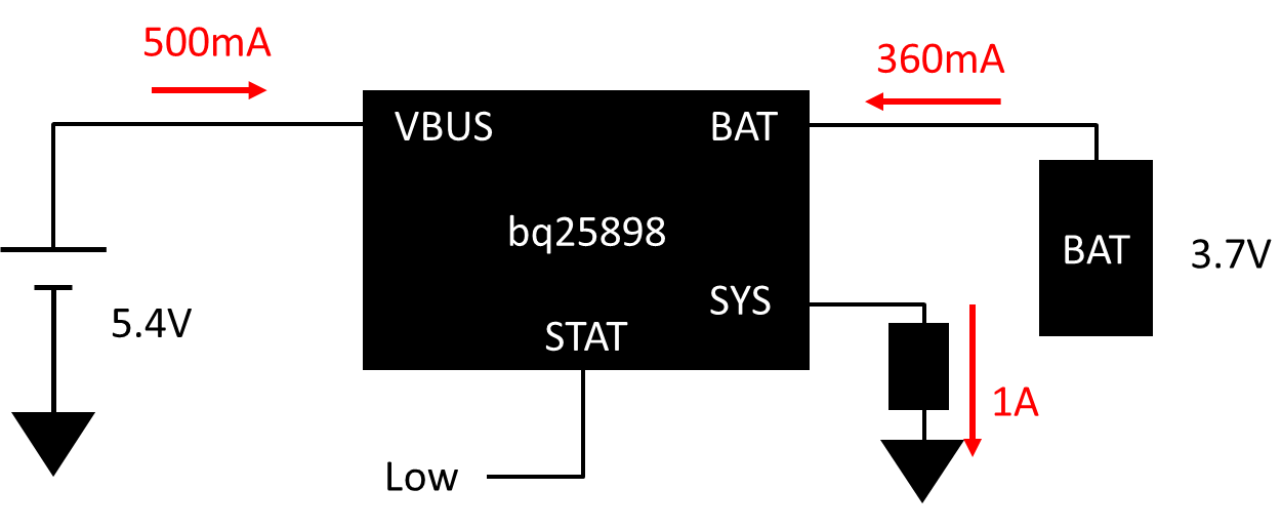


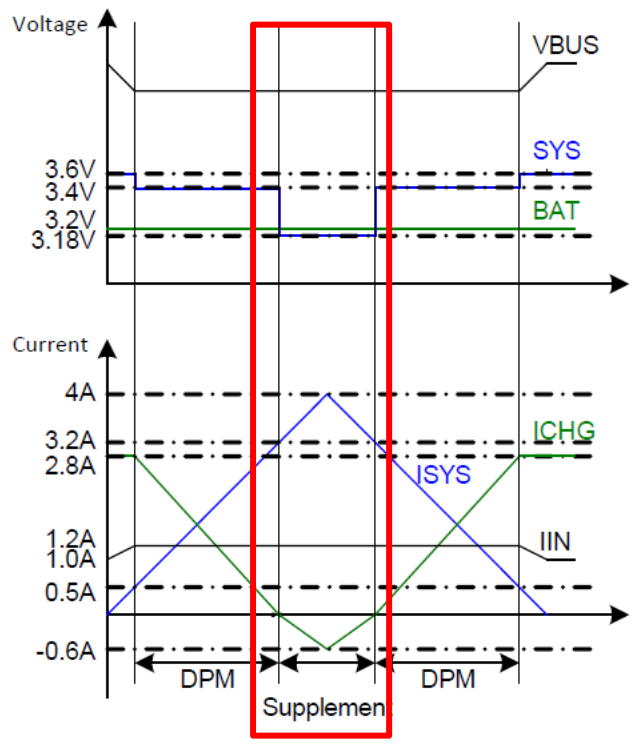


Figure 13. DPM Response

Registers

Save Registers | Load Registers | Start Log | Write Register | Read Register | Auto Read: OFF | Update Mode: Immediate | I2C Address: D6(6B) | Refresh Device ID | Default View | Device ACK OK

Multi-bit I2C Pulldown Menu

Input Current Limit	500 mA	VDPM Offset	600mV offset
ADC Conversion Rate	One Shot	Boost Frequency	1.5 MHz
VOK_OTG_EN	Disable	Minimum System Voltage	3.5 V
Minimum Battery Voltage	2.9V BAT faili	Fast Charge Current Limit	2.048 A
Pre-Charge Current Limit	128 mA	Termination Current Limit	256 mA
Charge Voltage Limit	4.208 V	Battery LowV	3.0 V
Recharge threshold	100 mV	WatchDog Timer	40 Sec
Fast Charge Timer	12 hrs	IR Compensation Resistor	0 mOhm
IR Comp Voltage Clamp	0 mV	Thermal Reg. Threshold	120 C
Boost Voltage Control	4.998 V	Boost Current Limit	1.5 A
Input Voltage Limit	4.80 V		

Single-bit I2C Selection

[ ] Enable HIZ	[x] Enable ILIM pin
[ ] Enable 12V	[ ] Start ADC
[x] Enable ICD	[ ] Enable Force D+/D- Dection
[x] Enable Auto-Input Detection	[ ] RESET WATCHDOG
[ ] Enable OTG	[x] Enable Charge
[ ] Enable Current Pulse Control	[x] Enable Termination
[ ] Disable STAT	[x] Enable Safety Timer
[x] JEITA Low Temp Fast Charge Current	[ ] Force ICD
[x] Enable 2X Extended Safety Timer	[ ] Turn off Q4
[ ] JEITA High Temp Charge Voltage	[ ] Turn Q4 Delay
[x] Q4 System Reset	[ ] MTK-PE Voltage Up
[ ] MTK-PE Voltage Down	[ ] Disable PFM_OTG_DIS
[x] Force VINDPM	[ ] Register Reset

Watch Dog Timer

Period: OFF | [ ] CE Low

Status

VBUS	USB Host SDP	Charge State	Fast Charging
PGOOD	Power Good	SDP	USB500
VSYS	Not in VSYSMIN Reg	Thermal Reg Status	Normal
VBUS Good	VBUS attached	VINDPM Status	Not in VINDPM
IINDPM Status	IINDPM	Input Current Limit	500 mA
ICD Optimized	In progress		

ADC

Battery Voltage	3.684 V	SYS Voltage	3.664 V
TS Percentage	59.130 %	VBUS Voltage	5.40 V
Charge Current	0 mA		

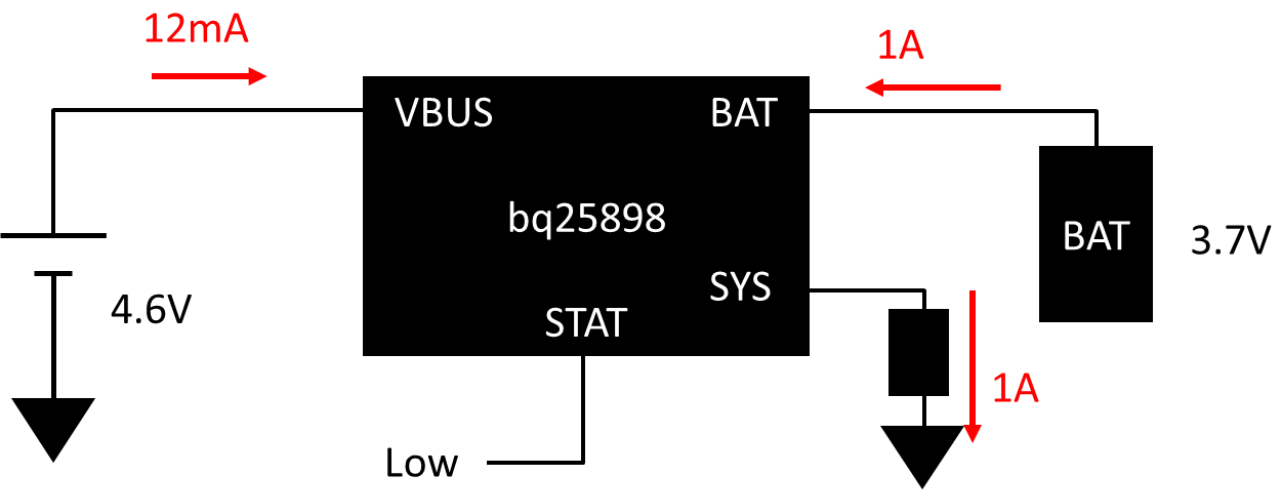
Fault

Watchdog Fault	Expired	Boost Fault	Normal
Charge Fault	Normal	BAT Fault	Normal
NTC Fault	Normal		

Part

Device ID	BQ25898	TS Profile	JEITA

06	0	1	0	1	1	1	1	0	5E	W	R
07	1	0	0	1	1	1	0	1	9D	W	R
08	0	0	0	0	0	0	1	1	03	W	R
09	0	1	0	0	0	1	0	0	44	W	R
0A	0	1	1	1	0	1	0	0	74	W	R
0B	0	0	1	1	0	1	1	0	36	W	R
0C	1	0	0	0	0	0	0	0	80	W	R
0D	1	0	0	1	0	1	1	0	96	W	R
0E	0	1	0	0	0	1	0	1	45	W	R
0F	0	1	0	0	0	1	0	0	44	W	R
10	0	1	0	1	0	0	1	0	52	W	R
11	1	0	0	1	1	1	0	0	9C	W	R
12	0	0	0	0	0	0	0	0	00	W	R
13	0	1	0	0	1	0	0	0	48	W	R
14	0	0	0	0	0	1	0	1	05	W	R



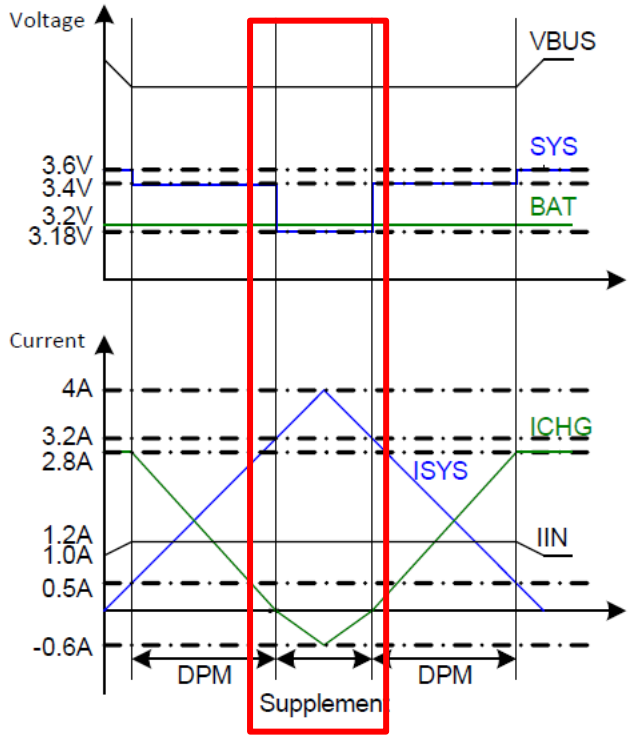


Figure 13. DPM Response

Registers

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Multi-bit I2C Pulldown Menu

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☑ Enable ICD	☐ Enable Force D+/D- Dection
☑ Enable Auto-Input Detection	☐ RESET WATCHDOG
☐ Enable OTG	☑ Enable Charge
☐ Enable Current Pulse Control	☑ Enable Termination
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☐ MTK-PE Voltage Down	☐ Disable PFM_OTG_DIS
☑ Force VINDPM	☐ Register Reset

Watch Dog Timer

Period: OFF | ☐ CE Low

Status

VBUS	USB Host SDP	Charge State	Fast Charging
PGOOD	Power Good	SDP	USB500
VSYS	Not in VSYSMIN Reg	Thermal Reg Status	Normal
VBUS Good	VBUS attached	VINDPM Status	VINDPM
IINDPM Status	Not in IINDPM	Input Current Limit	500 mA
ICD Optimized	In progress		

ADC

Battery Voltage	3.644 V	SYS Voltage	3.624 V
TS Percentage	58.665 %	VBUS Voltage	4.60 V
Charge Current	0 mA		

Fault

Watchdog Fault	Expired	Boost Fault	Normal
Charge Fault	Normal	BAT Fault	Normal
NTC Fault	Normal		

Part

Device ID	BQ25898	TS Profile	JEITA

06	0	1	0	1	1	1	1	0	5E	W	R
07	1	0	0	1	1	1	0	1	9D	W	R
08	0	0	0	0	0	0	1	1	03	W	R
09	0	1	0	0	0	1	0	0	44	W	R
0A	0	1	1	1	0	1	0	0	74	W	R
0B	0	0	1	1	0	1	1	0	36	W	R
0C	1	0	0	0	0	0	0	0	80	W	R
0D	1	0	0	1	0	1	1	0	96	W	R
0E	0	1	0	0	0	0	1	1	43	W	R
0F	0	1	0	0	0	0	1	0	42	W	R
10	0	1	0	1	0	0	0	1	51	W	R
11	1	0	0	1	0	1	0	0	94	W	R
12	0	0	0	0	0	0	0	0	00	W	R
13	1	0	0	0	1	0	0	0	88	W	R
14	0	0	0	0	0	1	0	1	05	W	R