

Device/System

The diagram shows the PMP20900EVM circuit. The solar array output is connected to the PACK pin (pin 8) of the PMP20900. The crossover connection is shown as a red line connecting the solar array output to the crossover pin (pin 13) of the PMP20900. The circuit includes various components such as resistors, capacitors, diodes, and a fuse. The battery cells are connected to the DSG pin (pin 7) of the PMP20900. The diagram is labeled with various pins and components, including VSS, SRN, SRP, TS1, SCL, SDA, VC1, VC2, PBI, CHG, PACK, DSG, and PMP20900.

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$V_{\text{mpp}} > V_{\text{bat}}$

Solar cell array

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