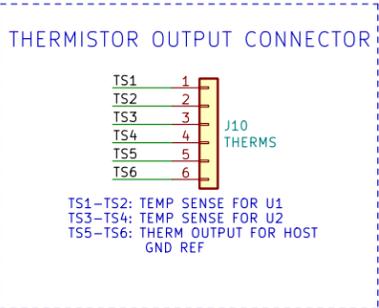


POP R32 TO KEEP PACK  
IN NORMAL MODE.  
NO POP FOR HIBERNATE MODE:  
SYSTEM MUST PULL PRES PIN HIGH  
TO EXIT HIBERNATE MODE

4-CELL STACK ON UPPER DEVICE  
-CCFG PIN TO VDD

4-CELL STACK ON LOWER DEVICE  
-CCFG PIN PULLED TO VDD



R27: OCDP RESISTOR = 487K  
FOR 350ms DELAY

DISCHARGE OVERCURRENT: 35A  
CHARGE OVERCURRENT: 20A