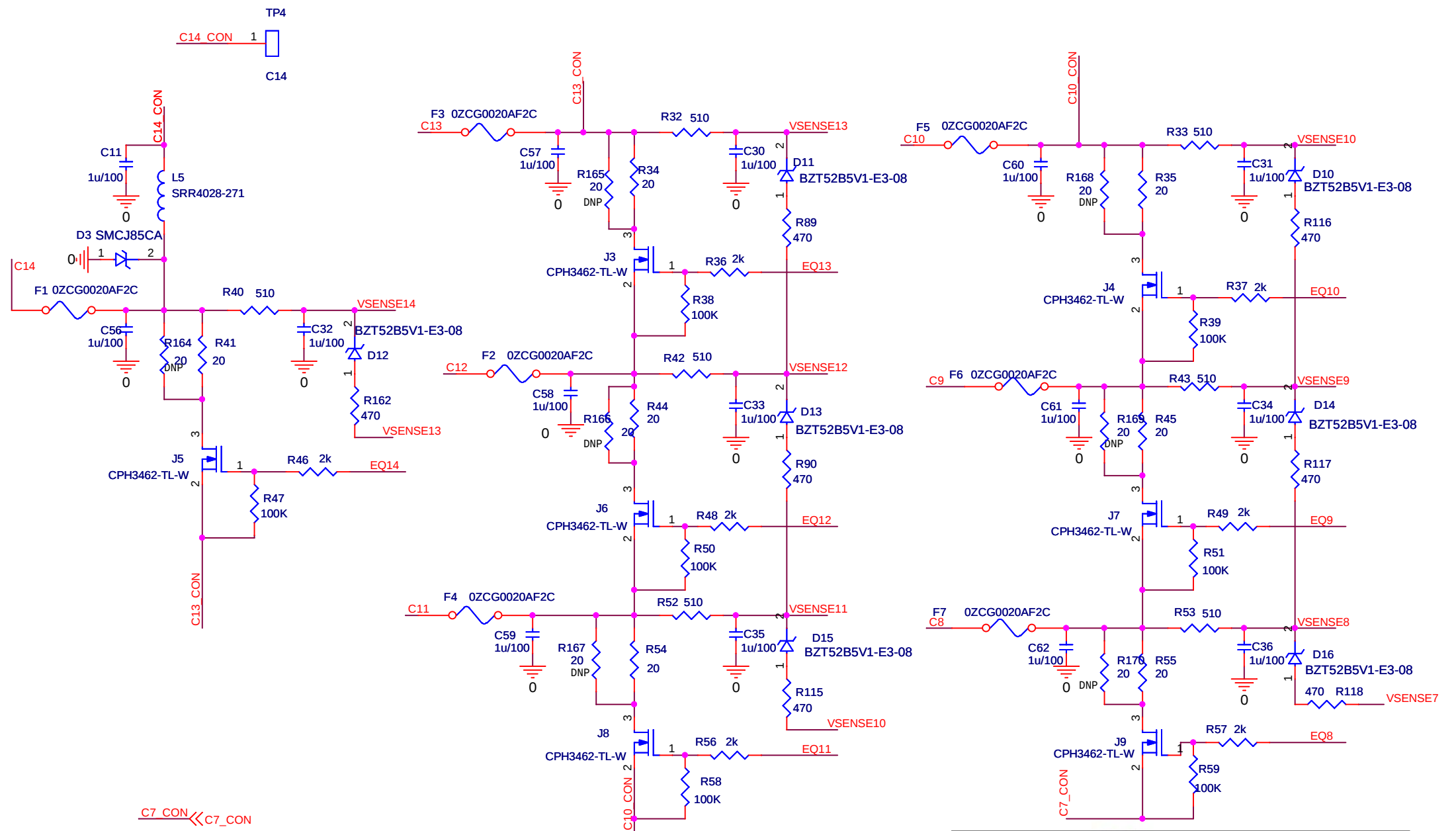




- C7_CON

C14_CON
- EQ14

EQ13

EQ12

EQ11

EQ10

EQ9

EQ8
- VSENSE14

VSENSE13

VSENSE12

VSENSE11

VSENSE10

VSENSE9

VSENSE8

VSENSE7
- C14

C13

C12

C11

C10

C9

C8

C7

Title

BMS_SCHEMATIC (BALANCING CIRCUIT)

Size	Document Number	Drawn by	Approved by	Rev
A4	SCH 2102-01.01.00	PREETI BIRADAR	JAYWANT MAHAJAN	BETA1_3

Date:

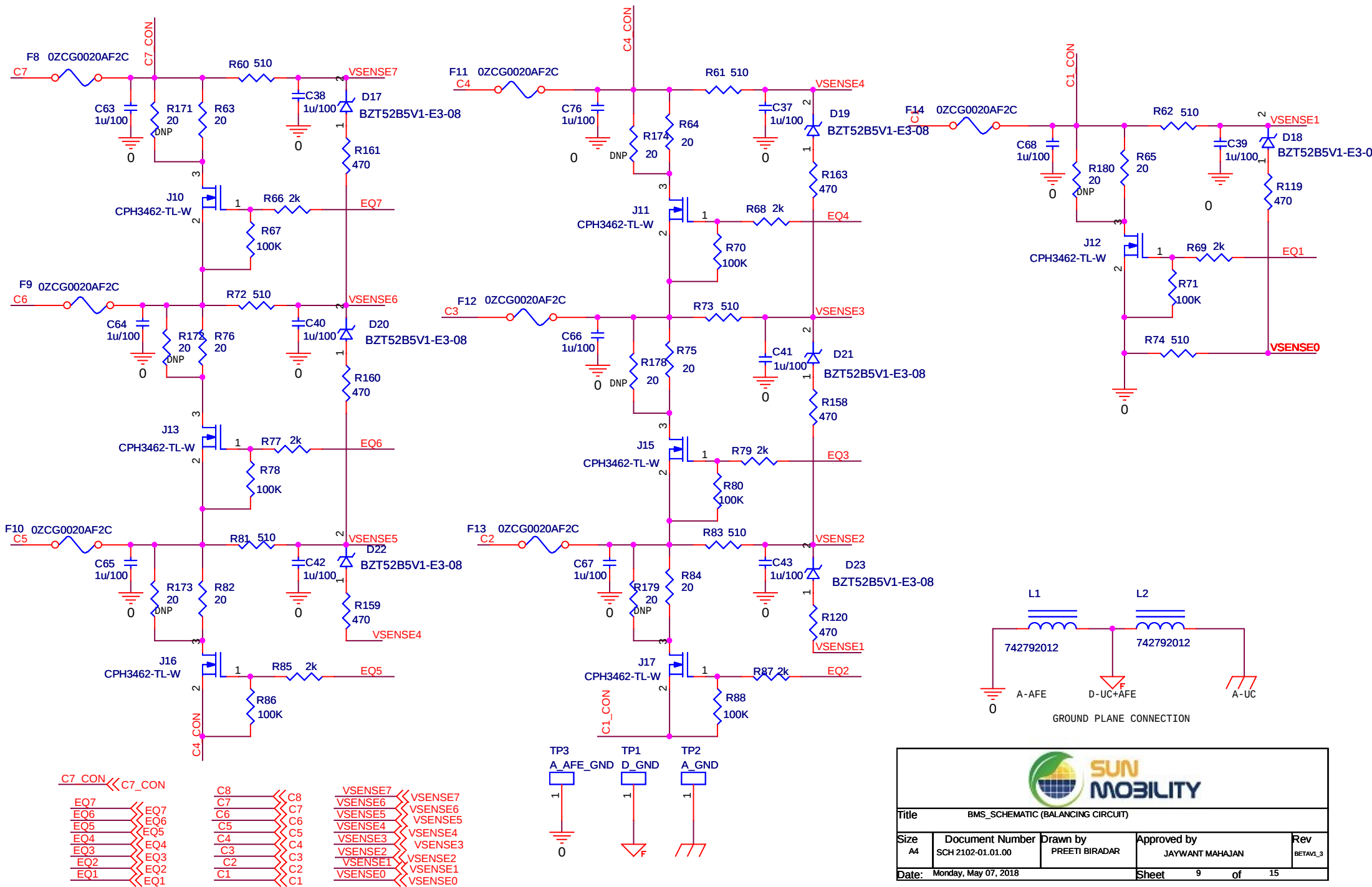
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- C7_CON

C7_CON

EQ7

EQ6

EQ5

EQ4

EQ3

EQ2

EQ1
- C8

C7

C6

C5

C4

C3

C2

C1
- VSENSE7

VSENSE6

VSENSE5

VSENSE4

VSENSE3

VSENSE2

VSENSE1

VSENSE0



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SizeA4	Document NumberSCH 2102-01.01.00	Drawn byPREETI BIRADAR	Approved byJAYWANT MAHAJAN	RevBETA_V1_3
Date: Monday, May 07, 2018			Sheet9	of15

