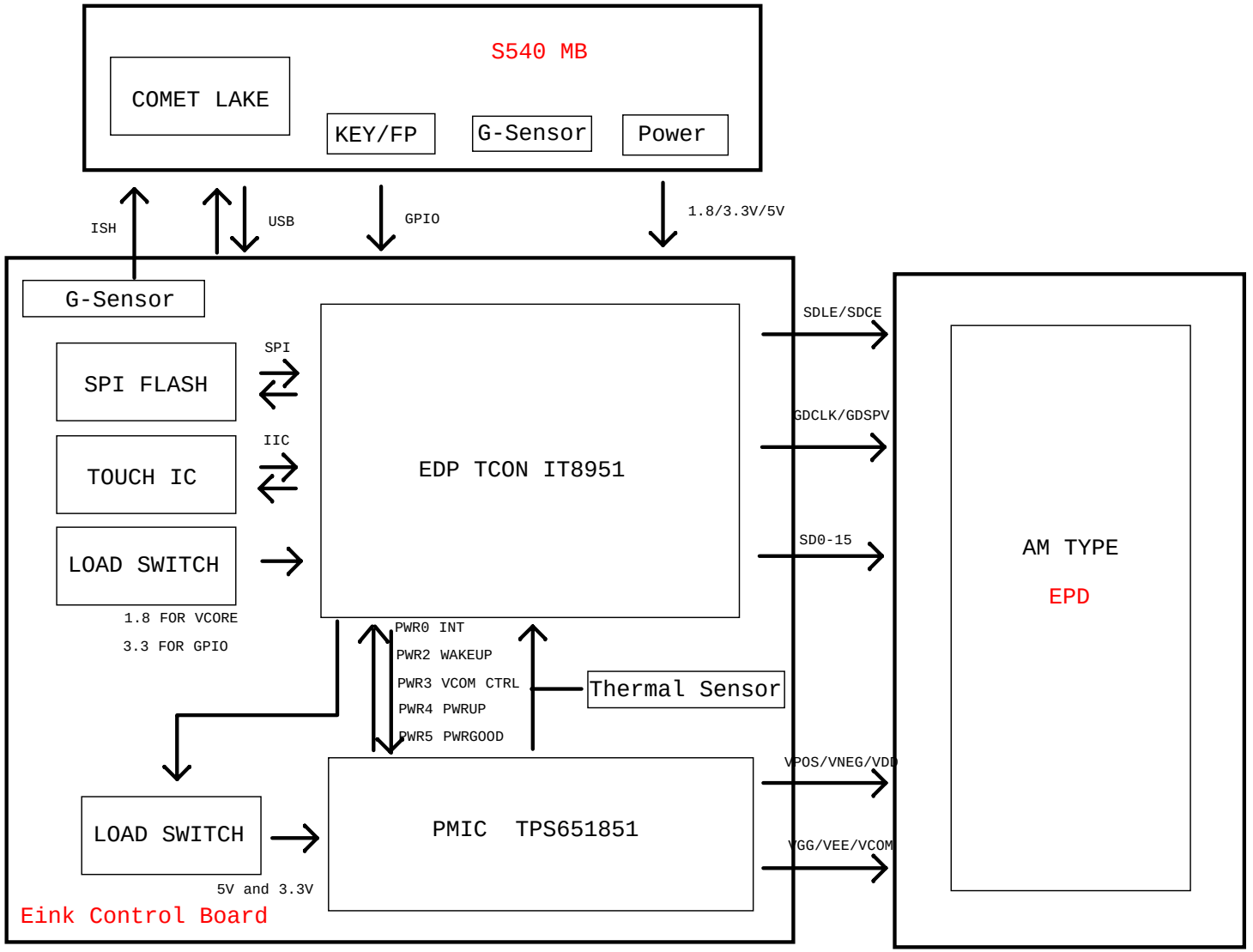

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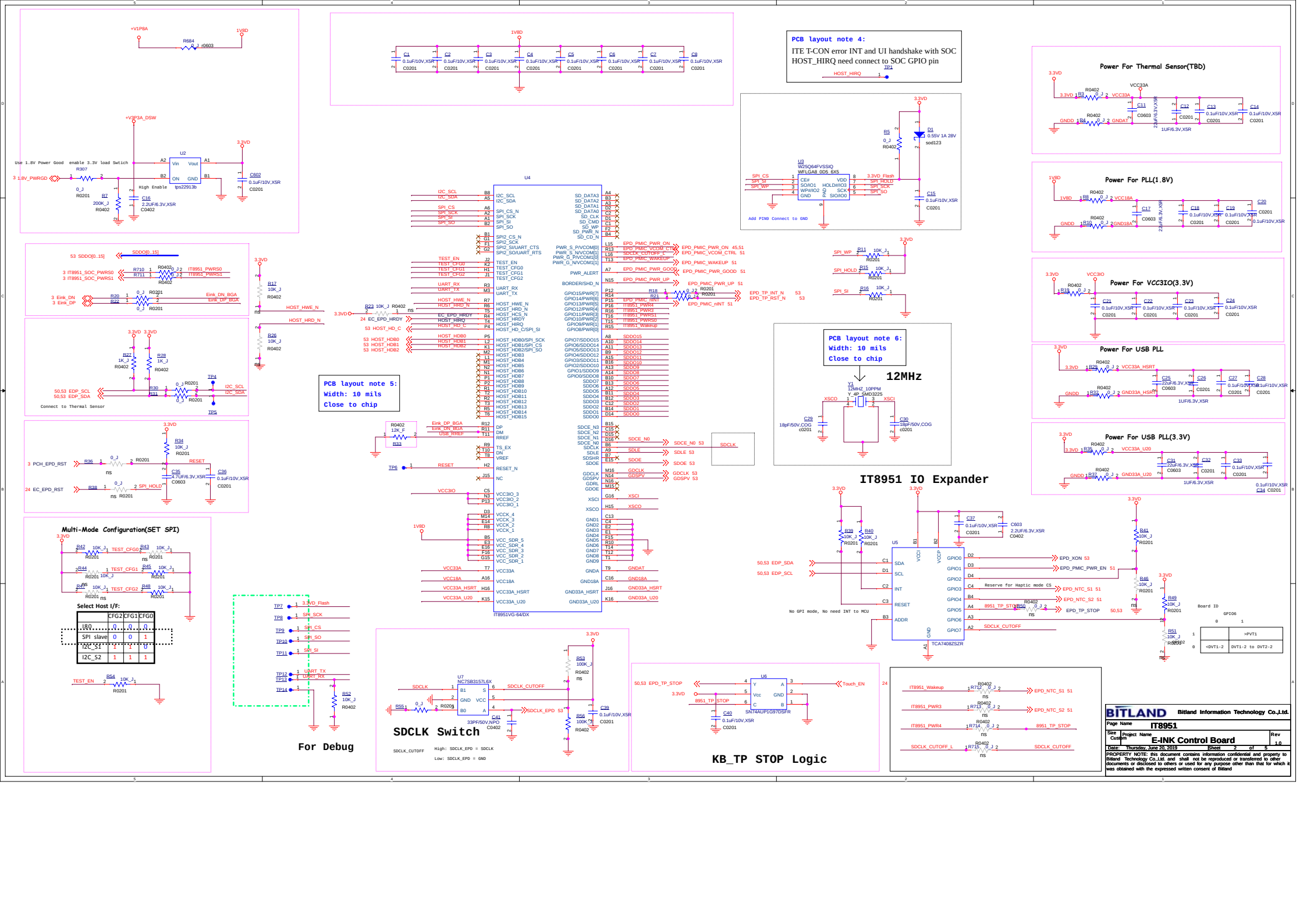
PCB layout note 4:
ITE T-CON error INT and UI handshake with SOC
HOST_HIRQ need connect to SOC GPIO pin
PCB layout note 5:
Width: 10 mils
Close to chip
PCB layout note 6:
Width: 10 mils
Close to chip
12MHz
IT8951 IO Expander
Power For Thermal Sensor(TBD)
Power For PLL(1.8V)
Power For VCC3IO(3.3V)
Power For USB PLL
Power For USB PLL(3.3V)
Multi-Mode Configuration(SET SPI)
Select Host I/F:
SDCLK Switch
For Debug
KB_TP STOP Logic
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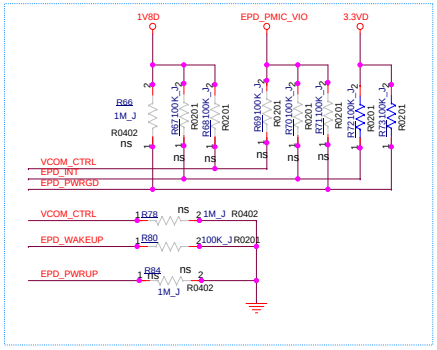
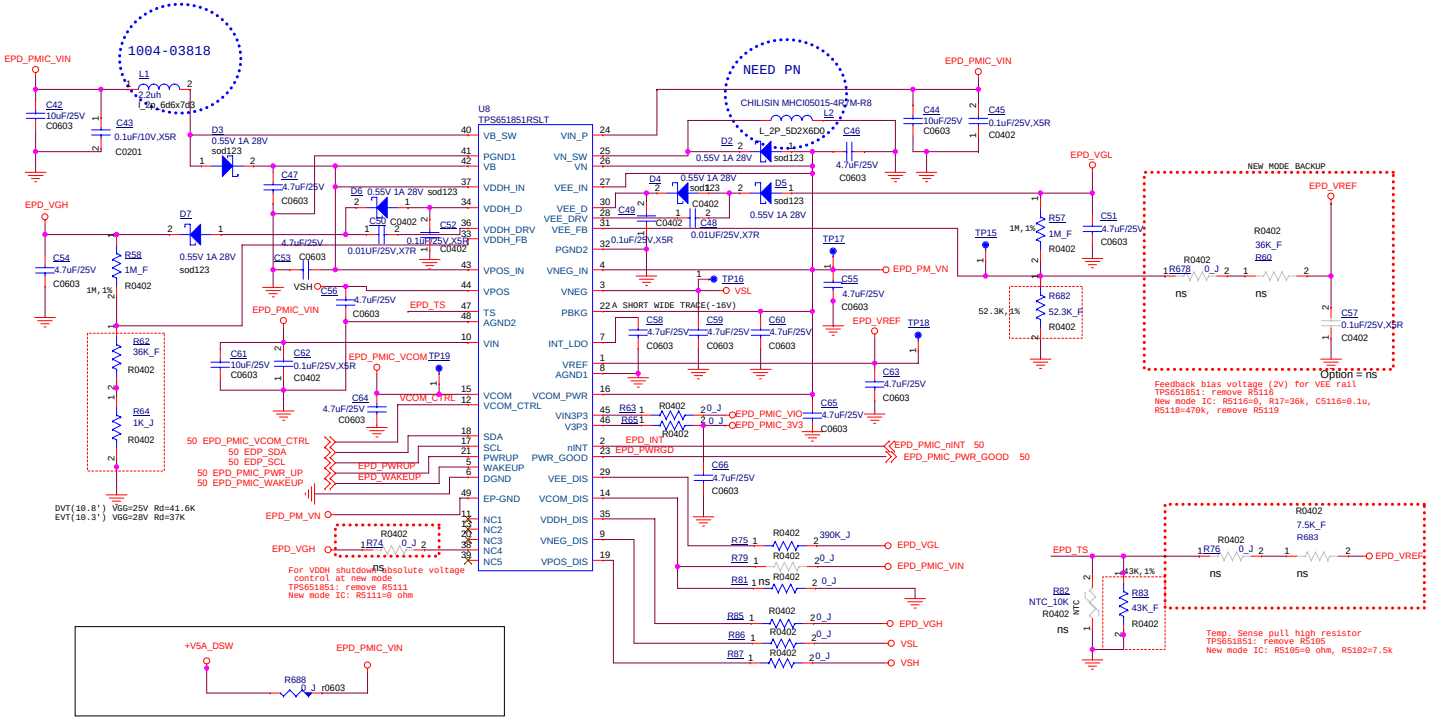
Note:
1. Reserved BOM for co-layout is highlighted by red dash block
2. Output capacitance will be tuned by practical application

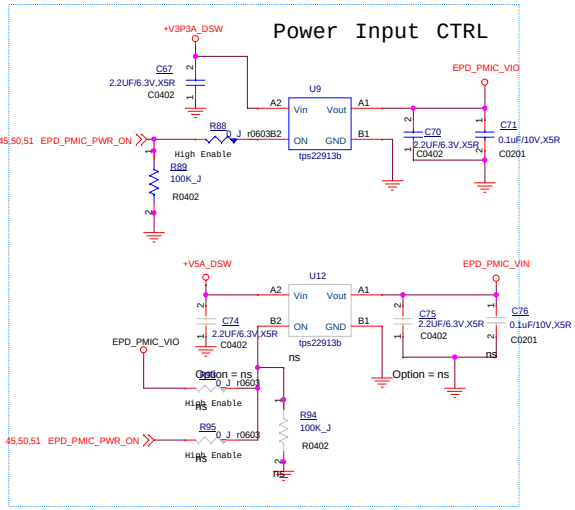
Summary of BOM discrepancy for co-layout

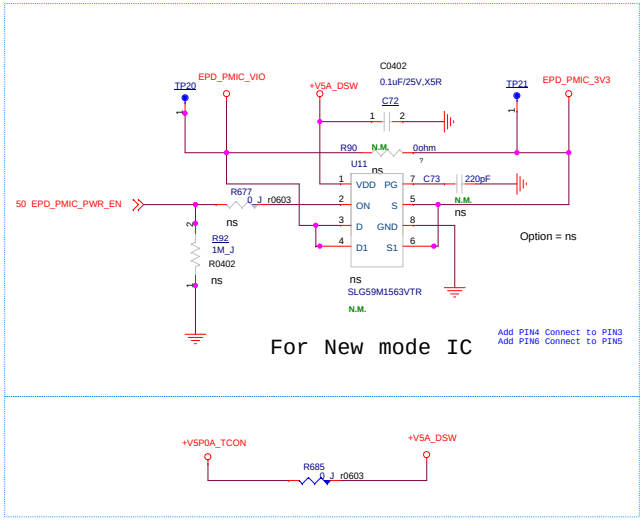
Component No.	TPS651851	New mode IC
R5111	Open	0
R5105	Open	0
R5102	7.5k	7.5k
R5118	1M	470k
R5119	52.3k	Open
R5117	36k	36k
R5116	Open	0
R5113	1M	470k
R5115	37k	20k
L5101	2.2uH	4.7uH



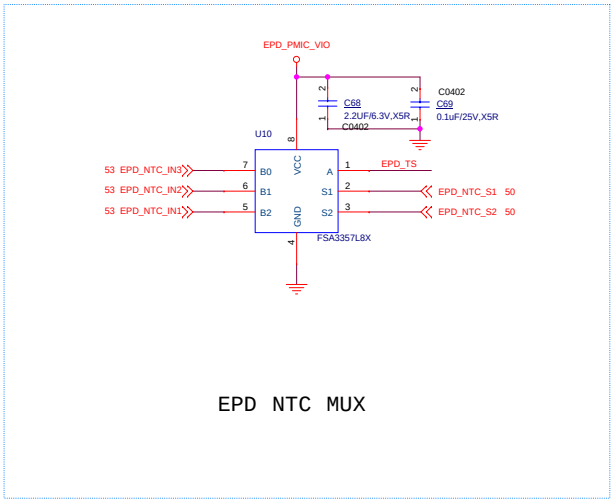
IT8951 CTRL Default CONFIG







For New mode IC



EPD NTC MUX

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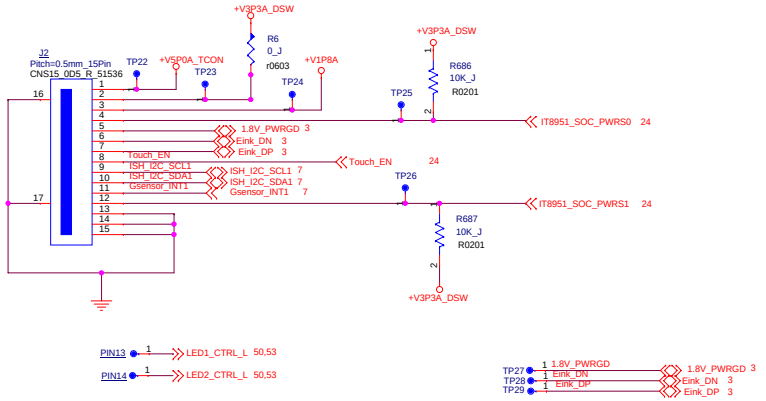
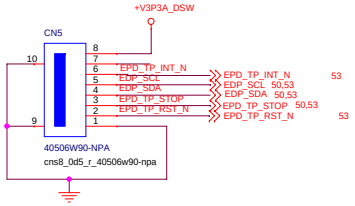
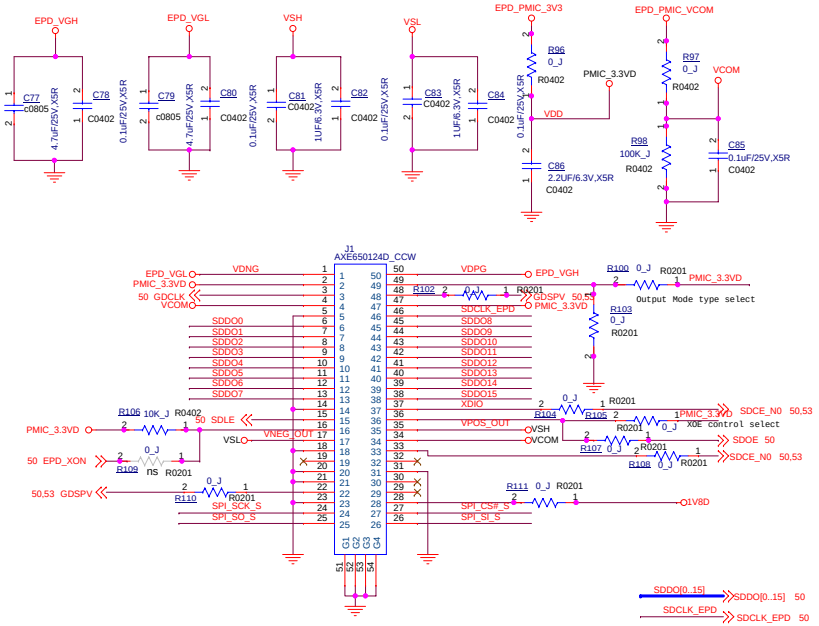
TO MB Connector

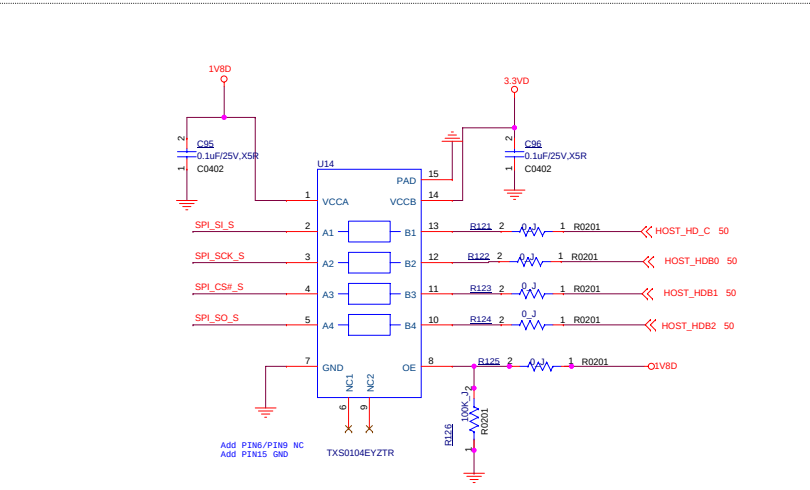


Touch Panel Connector



E-ink Connector





E-ink SPI Flash Level Shift

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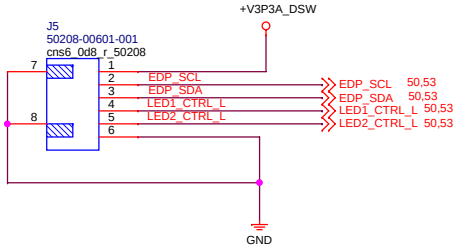
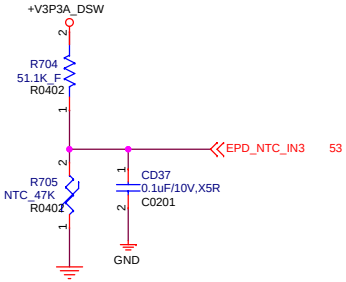
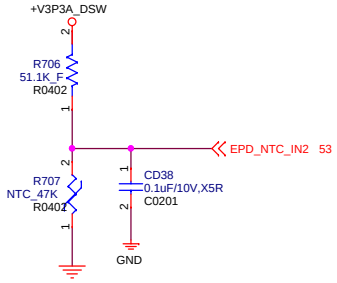
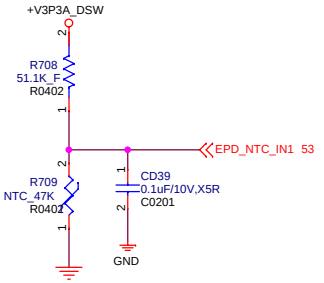
Thermal Sensor

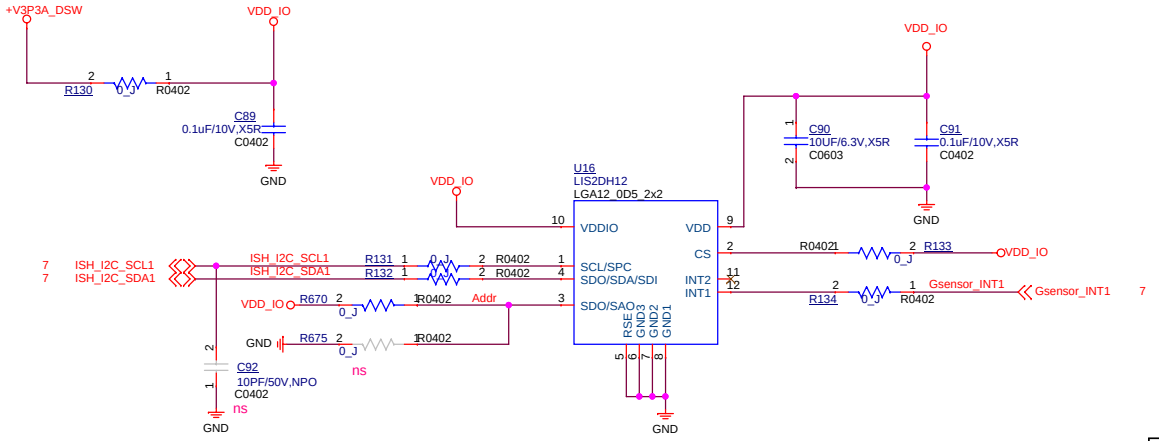








G Sensor



address: 0X19

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