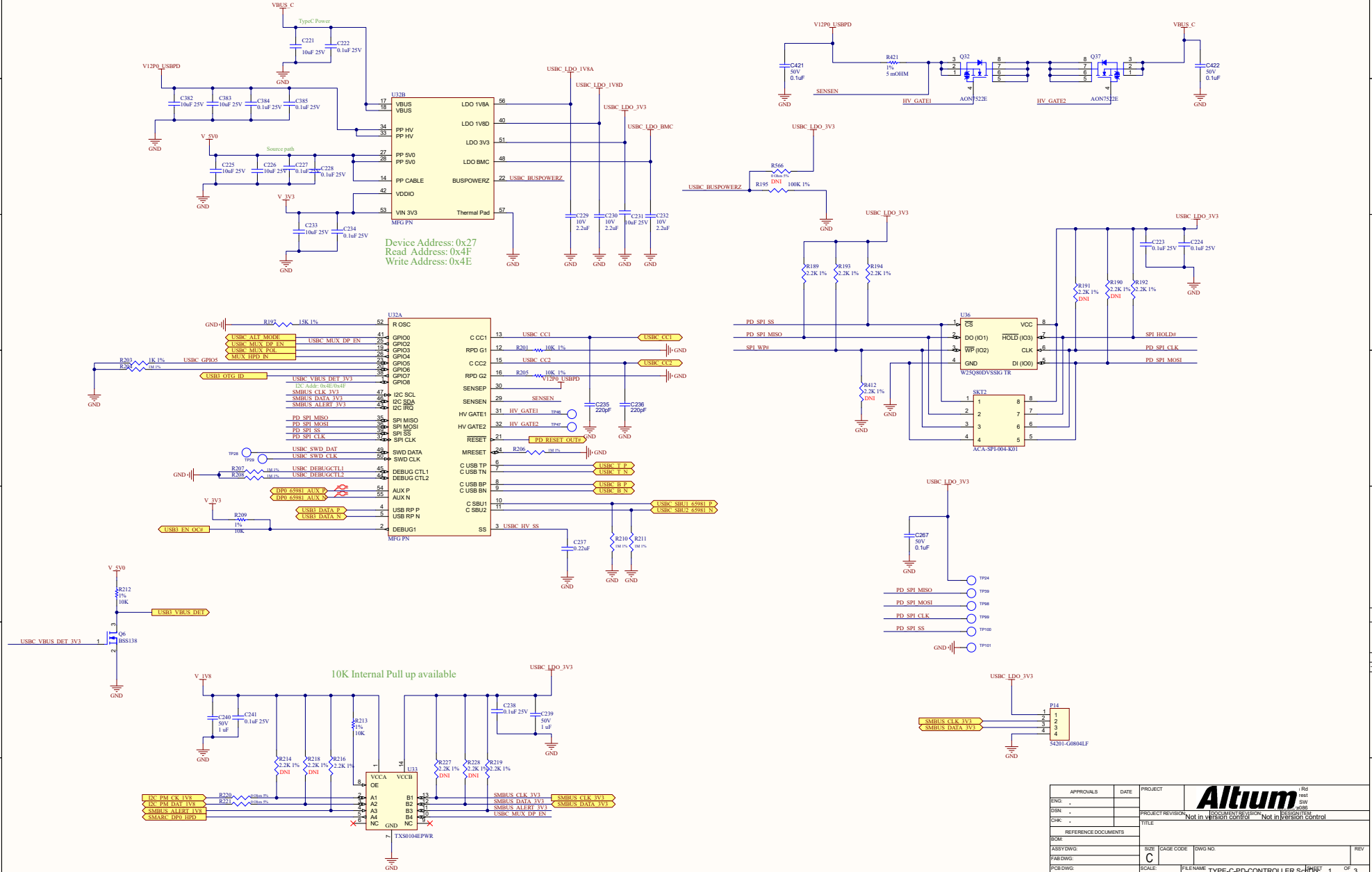
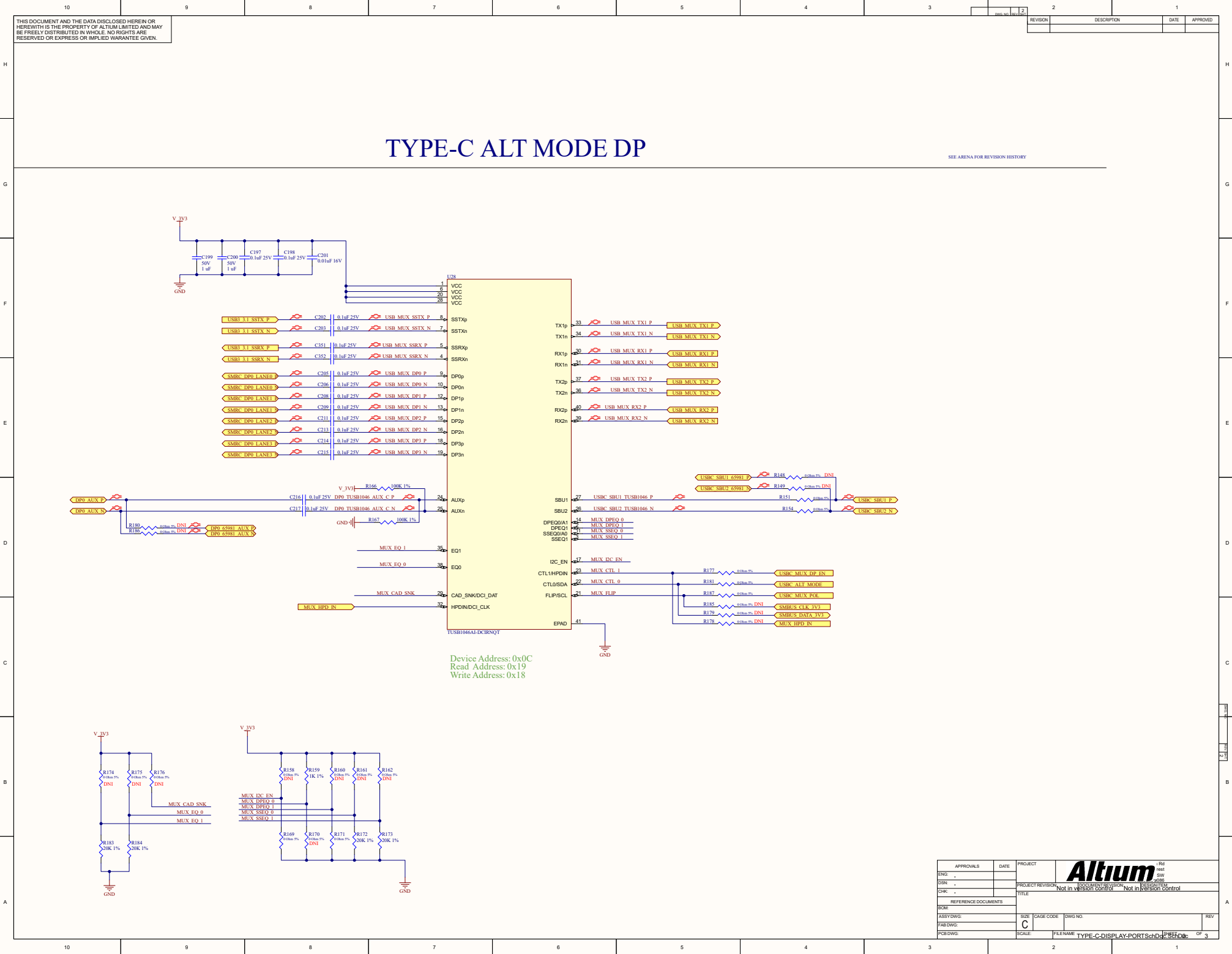


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PD CONTROLLER

SEE ARENA FOR REVISION HISTORY





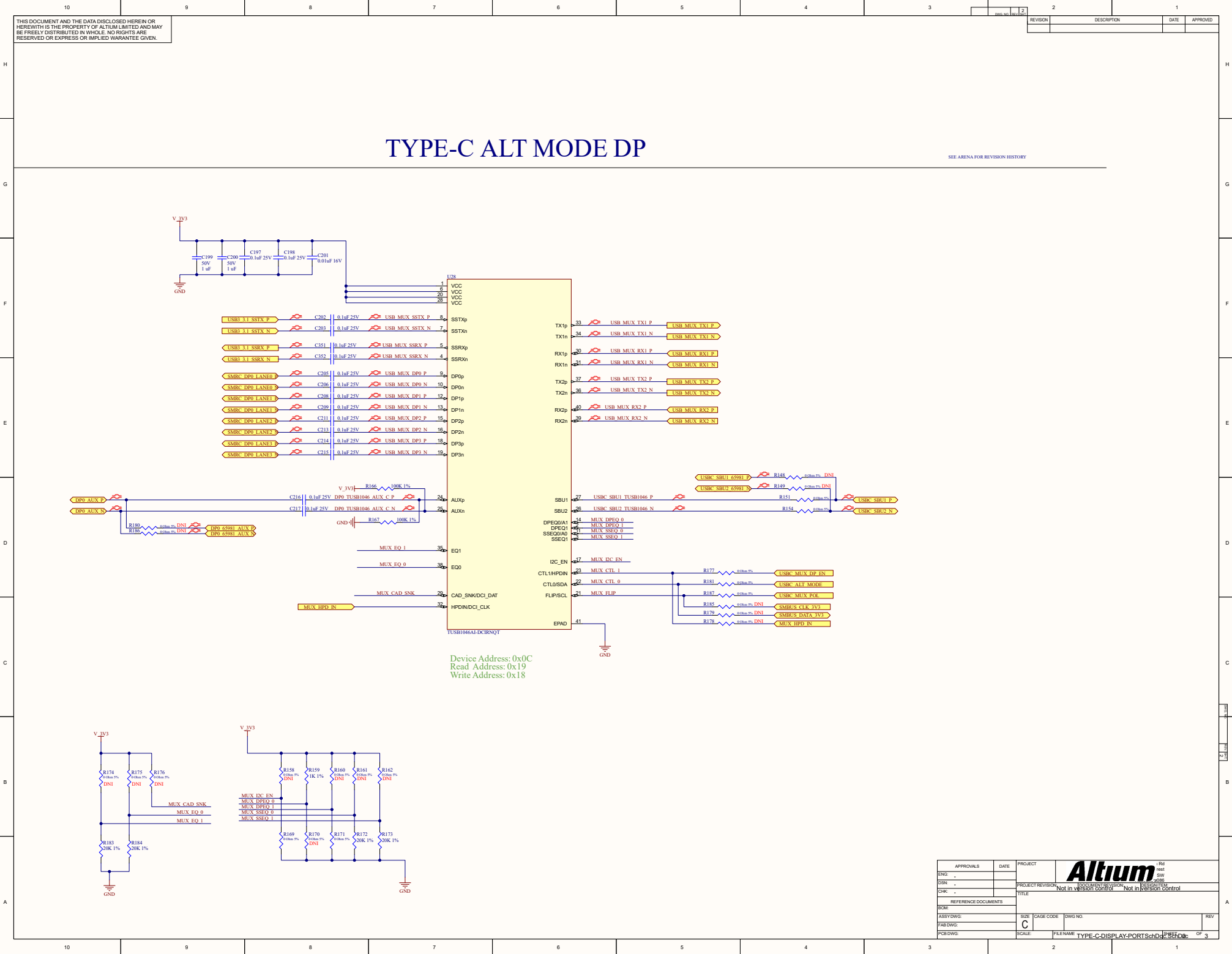
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TYPE-C ALT MODE DP

SEE ARENA FOR REVISION HISTORY

Device Address: 0x0C
Read Address: 0x19
Write Address: 0x18

APPROVALS	DATE	PROJECT	1 Rev
ENG -			rest SW
DSN -			pcdb
CHK -			release
REFERENCE DOCUMENTS			
BOB			
ASSY DWG	SIZE	CAGE CODE	DWG NO.
FAB DWG			
PCB DWG	SCALE	FILENAME	TYPE-C-DISPLAY-PORTSched



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TYPE-C ALT MODE DP

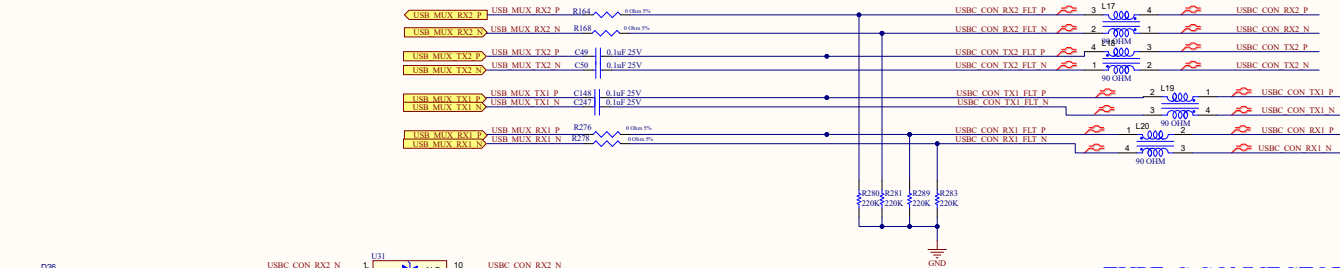
SEE ARENA FOR REVISION HISTORY

Device Address: 0x0C
Read Address: 0x19
Write Address: 0x18

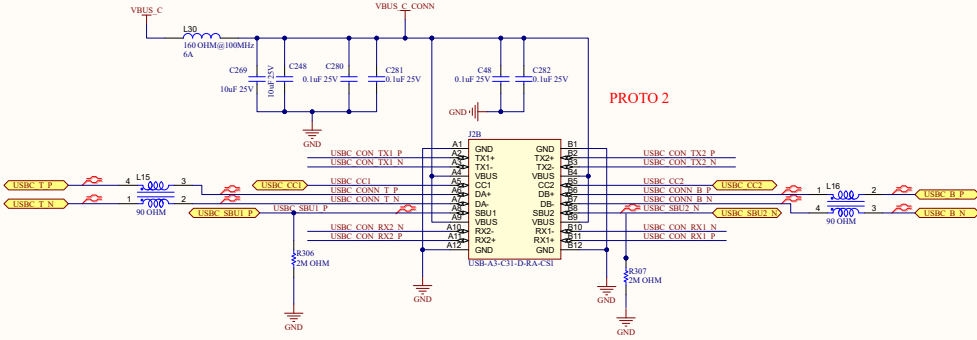
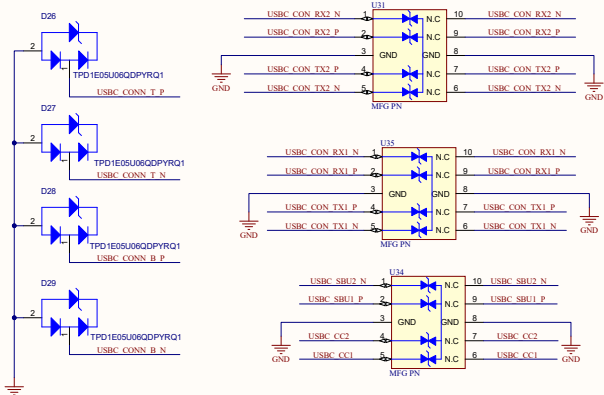
APPROVALS	DATE	PROJECT	1. Rev
ENG -			rest
DSN -			SW
CHK -			PCB
REFERENCE DOCUMENTS		Not in version control	
BOB	SIZE	CAGE CODE	DWG NO.
ASSY DWG	C		
FAB DWG	SCALE	FILENAME	TYPE-C-DISPLAY-PORTS
PCB DWG			Sheet 3 of 3

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REV	DESCRIPTION	DATE	APPROVED



TYPE-C CONNECTOR



PROTO 2

APPROVALS	DATE	PROJECT	Altium
ENG			
DSN			
CHK			
REFERENCE DOCUMENTS			
ASBY DWG			
PAR DWG			
PCB DWG			