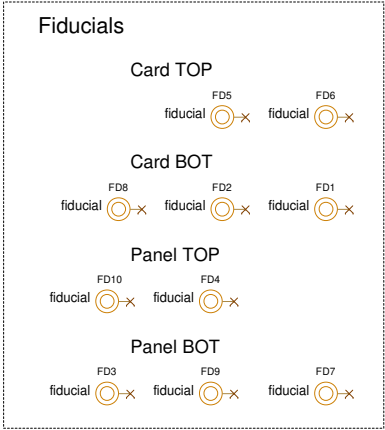
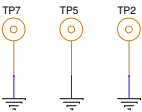


POWER RAILS

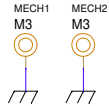
REVISIONS			
1.0	DESCRIPTION	DATE	APPROVED
1.0	ENG. RELEASE PC00002470_A00	23-FEB-23	D.Davidov



PLACE TOP
GND Vias

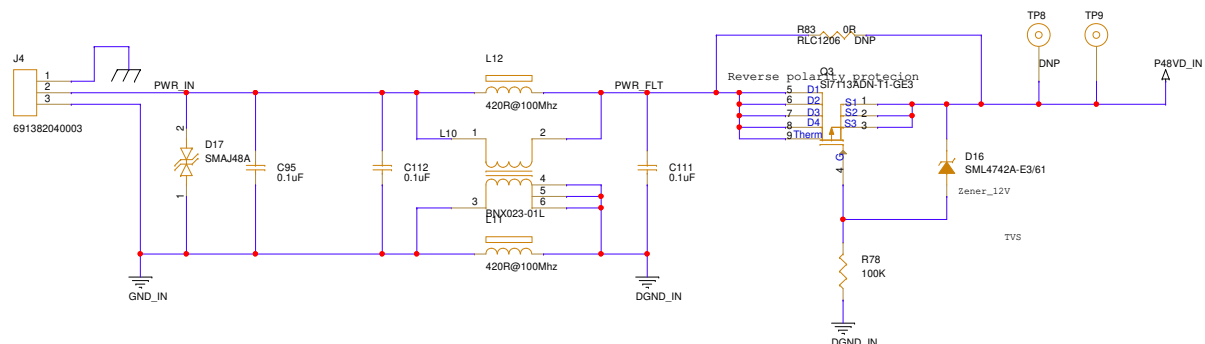


SCREW

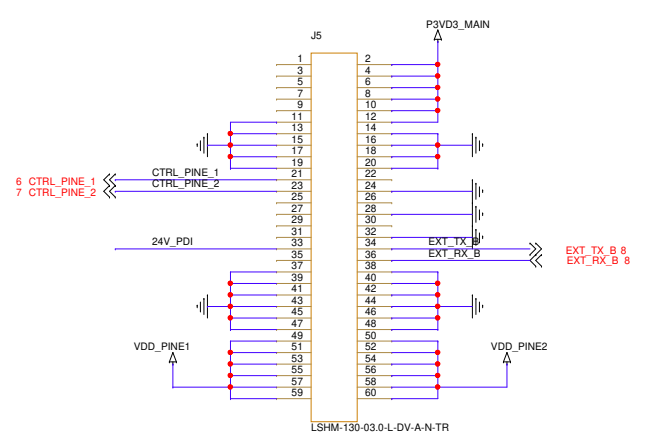
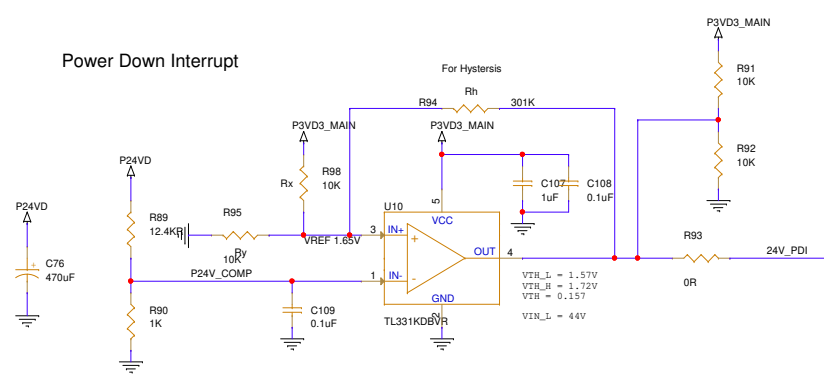


Drawn: Aviv F
Checker:
Approved: David D

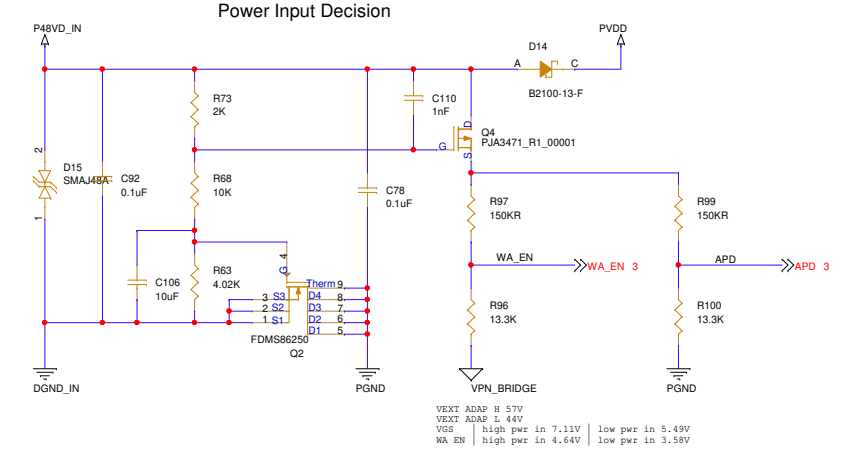
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Title			
POWER BOARD ALDER POE JL-PC0002470			
Size	Document Number	Rev	
A3	JL-PC0002470	A00	
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Power Down Interrupt



Power Input Decision



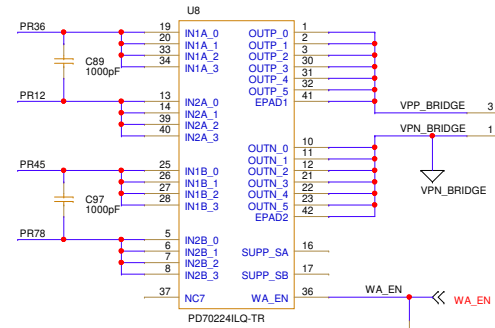
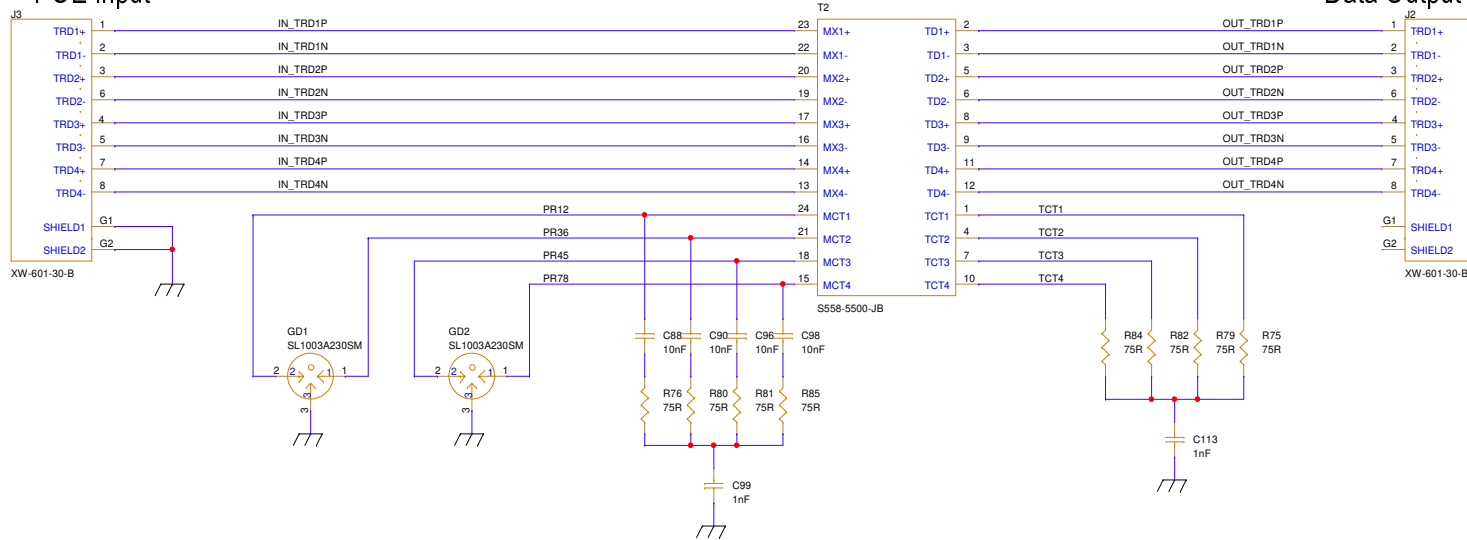
VEXT ADAP H 57V
VEXT ADAP L 44V
VGS high pwr in 7.11V low pwr in 5.49V
WA EN high pwr in 4.64V low pwr in 3.58V

<Variant Name>

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POE input

Data Output

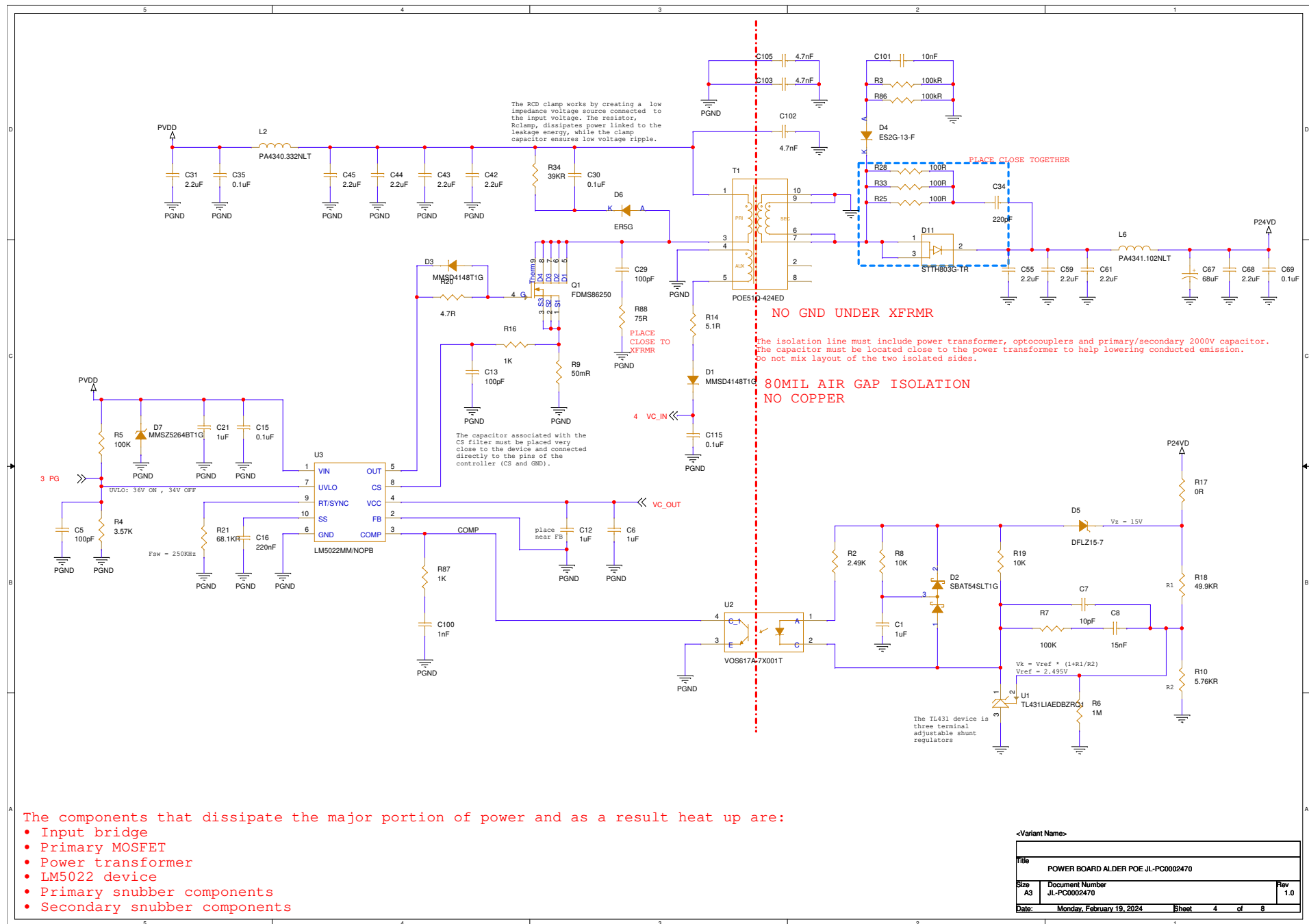


PD Class	CLASS SIGNATURE A	CLASS SIGNATURE B	MINIMUM POWER AT PD (W)	MAXIMUM POWER AT PD (W)	NUMBER OF CLASS CYCLES @ MAX POWER	RESISTOR CLSA (Ω)	RESISTOR CLSB (Ω)
0	0	0	0.44	12.95	1	1210	1210
1	1	1	0.44	3.84	1	249	249
2	2	2	3.84	6.40	1	140	140
3	3	3	6.40	12.95	1	90.9	90.9
4	4	4	12.95	25.5	2.3	63.4	63.4
5	4	0	25.5	40	4	63.4	1210
6	4	1	40	51	4	63.4	249
7	4	2	51	62	5	63.4	140
8	4	3	62	71	5	63.4	90.9

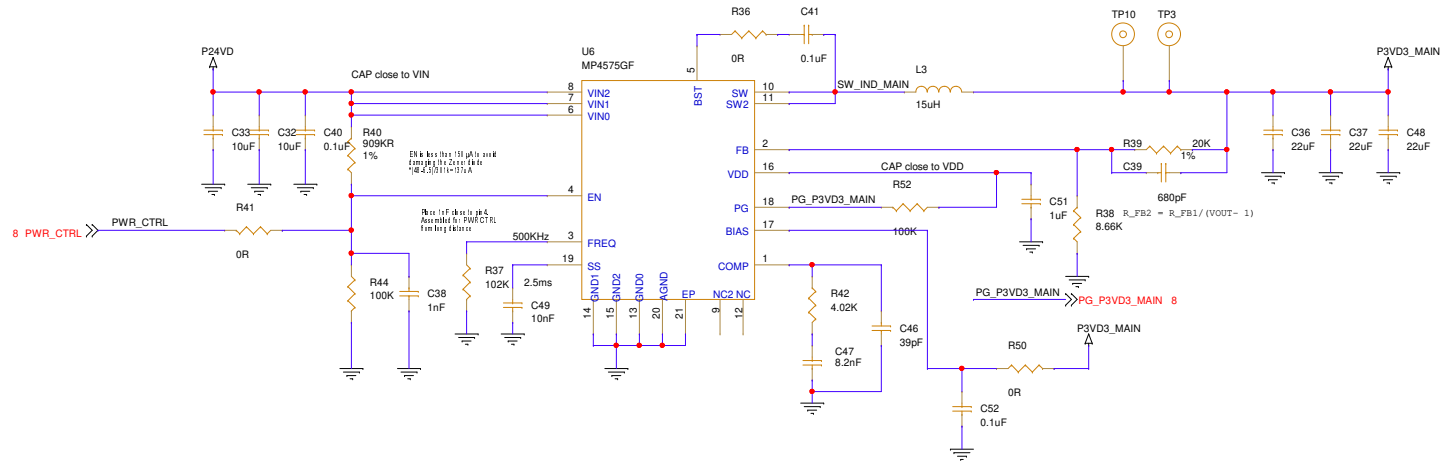
PSE Type	MPS_DUTY	MPS Duty-Cycle
1.2	-	26%
3.4	Short to VSS	12.5%
3.4	Resistance (60.4K typ.) to VSS	8.1%
3.4	Open	5.4%

<Variant Name>

Title		
POWER BOARD ALDER POE JL-PC0002470		
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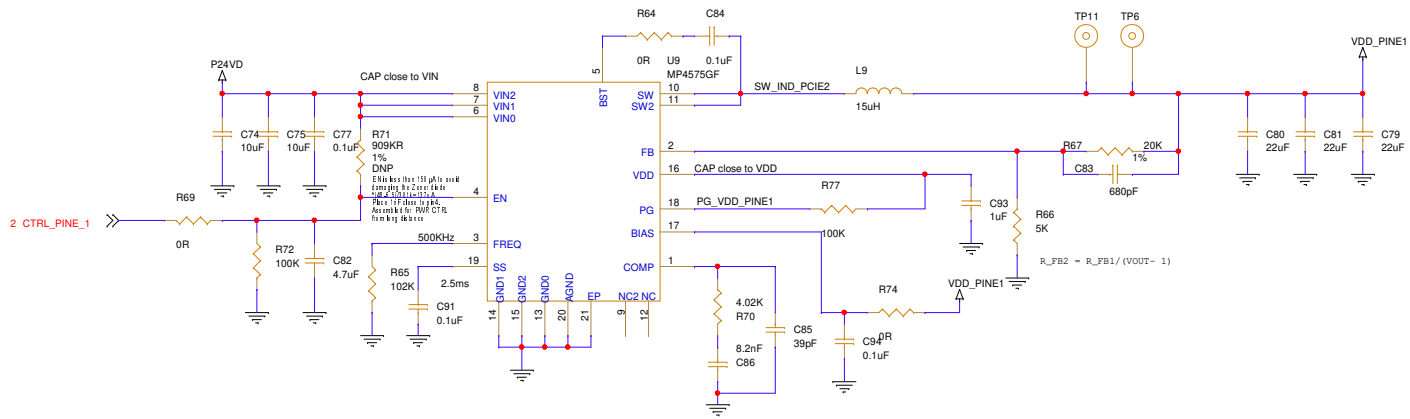
48V to 3.3V main power



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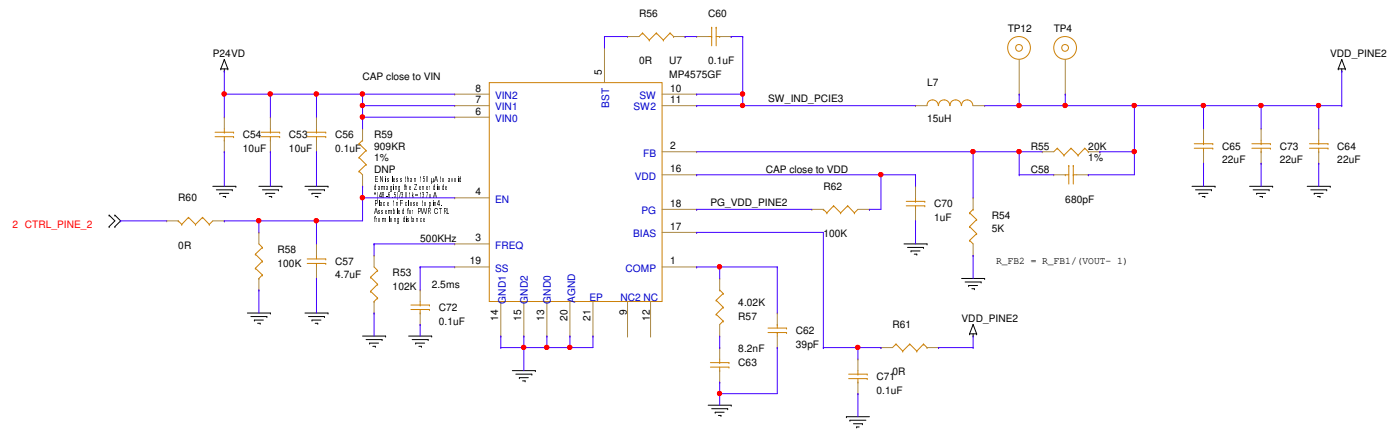
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POWER BOARD ALDER POE JL-PC0002470		
Size	Document Number	Rev
A3	JL-PC0002470	A00
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VDD PINE 1



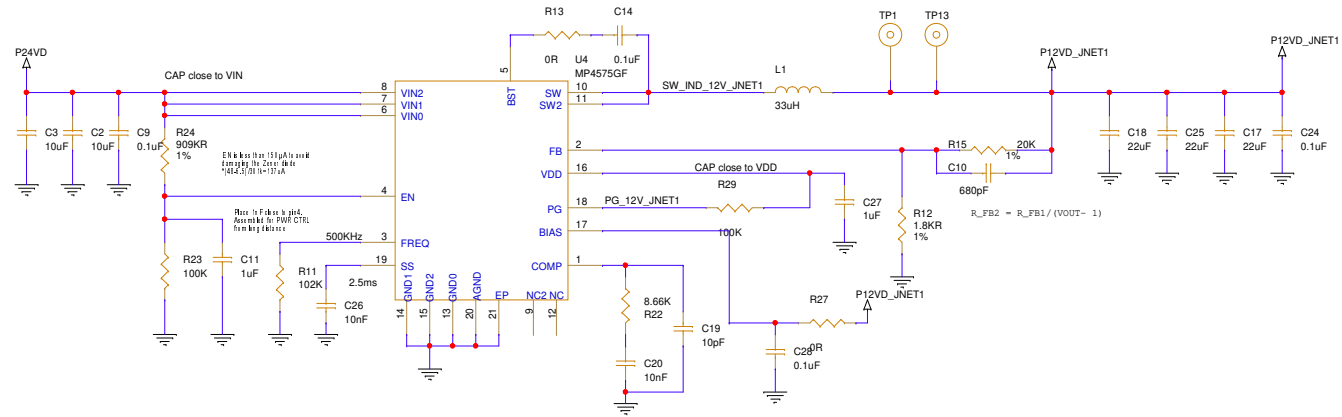
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Size A3	Document Number JL-PC0002470			Rev A00
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VDD PINE 2

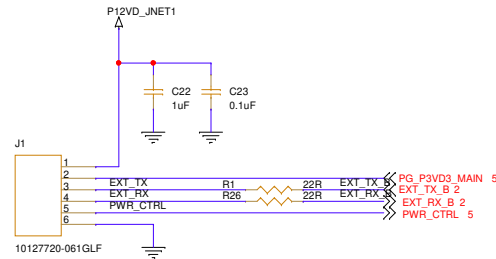


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POWER BOARD ALDER POE JL-PC0002470			
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48V to 12V JNET1 power



JNET1 power and communication channel



<Variant Name>

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POWER BOARD ALDER POE JL-PC0002470		
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