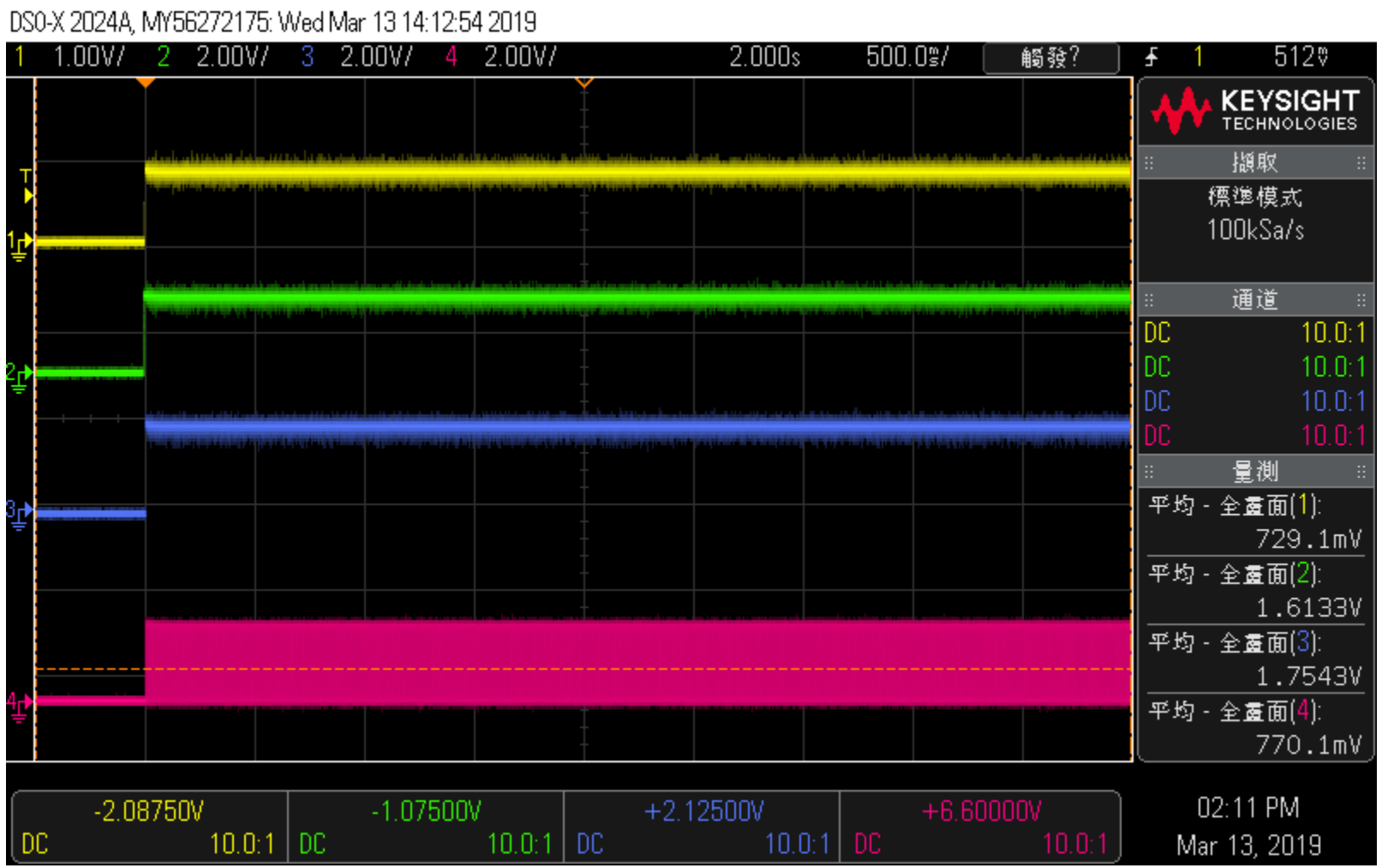
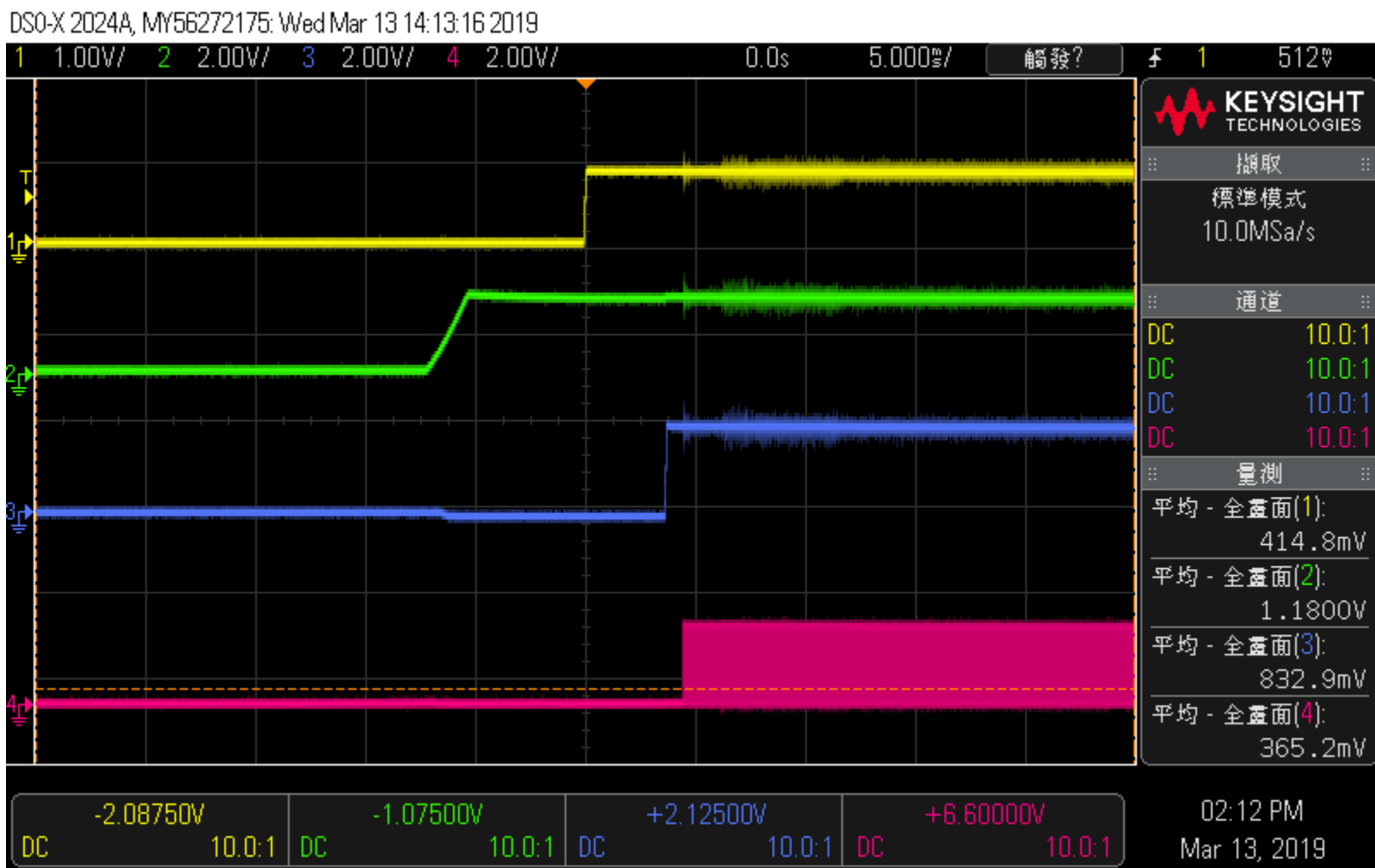


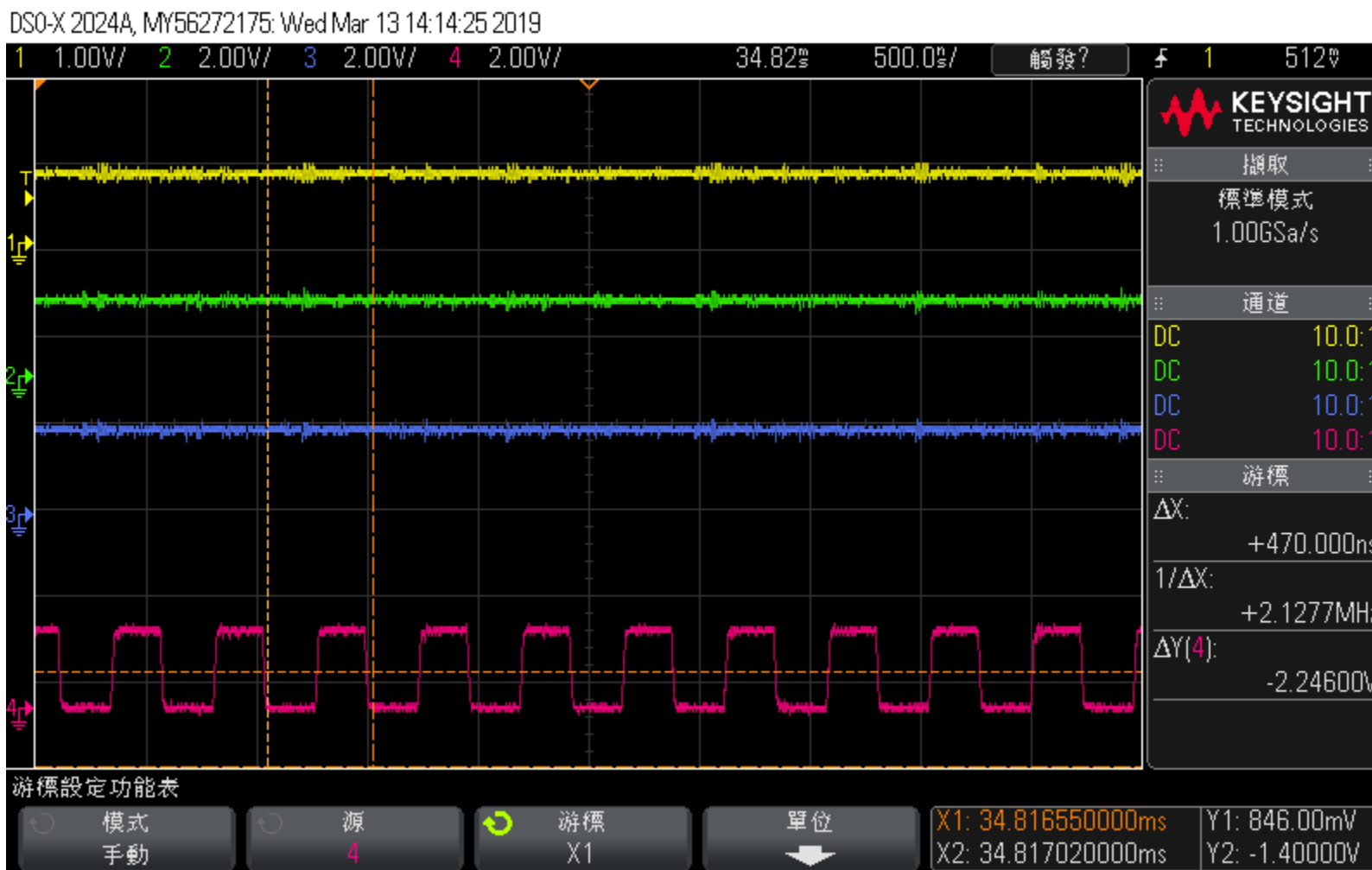
CH1:VBG                    CH2:LDOVANA\_OUT  
 CH3:LDOVRTC\_OUT        CH4:SYNCCLKOUT



CH1:VBG                    CH2:LDOVANA\_OUT  
CH3:LDOVRTC\_OUT        CH4:SYNCCLKOUT

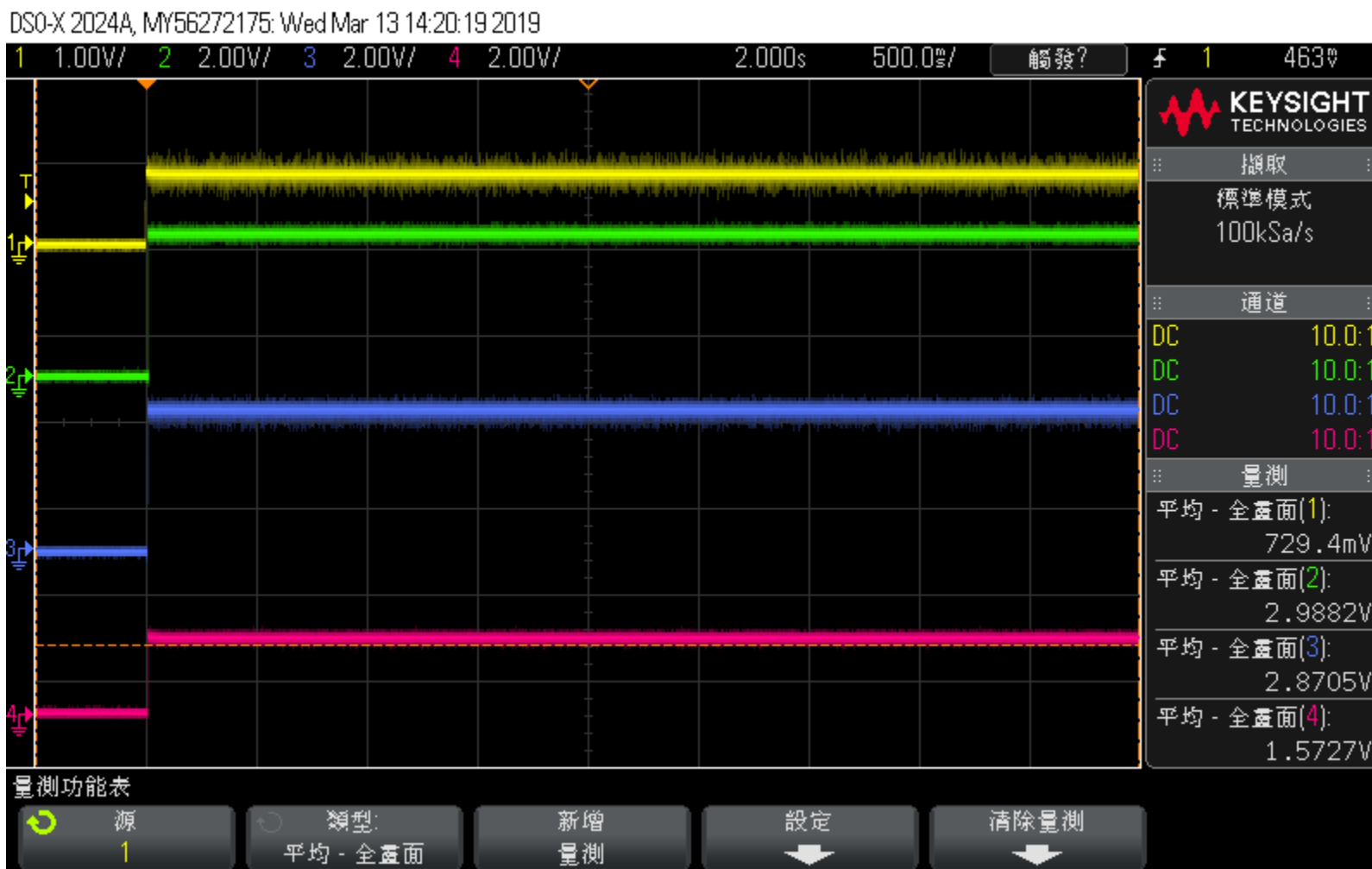


CH1:VBG                    CH2:LDOVANA\_OUT  
CH3:LDOVRTC\_OUT        CH4:SYNCCLKOUT



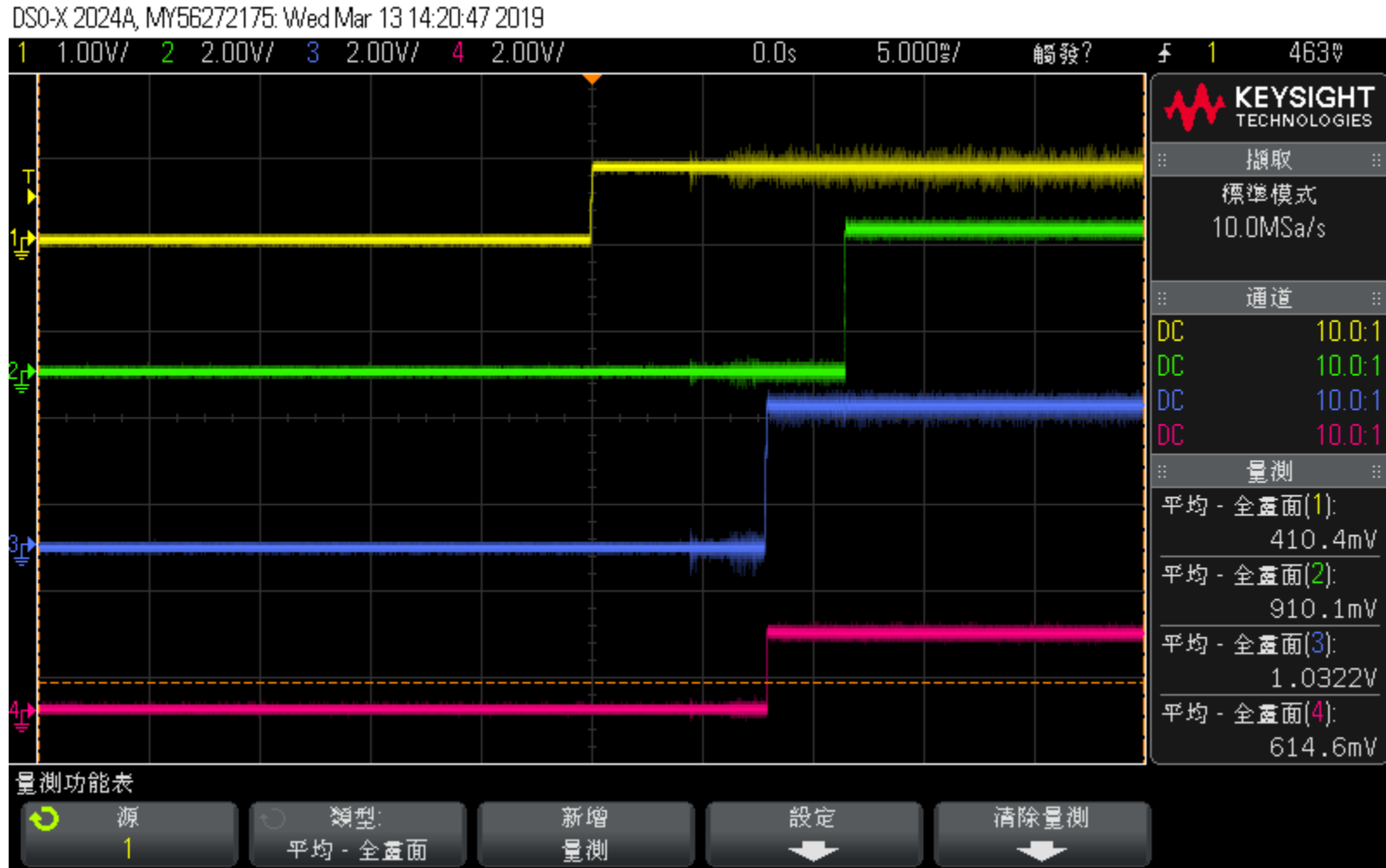
CH1:VBG  
CH3:LDO2\_OUT

CH2:LDO1\_OUT  
CH4:LDO3\_OUT



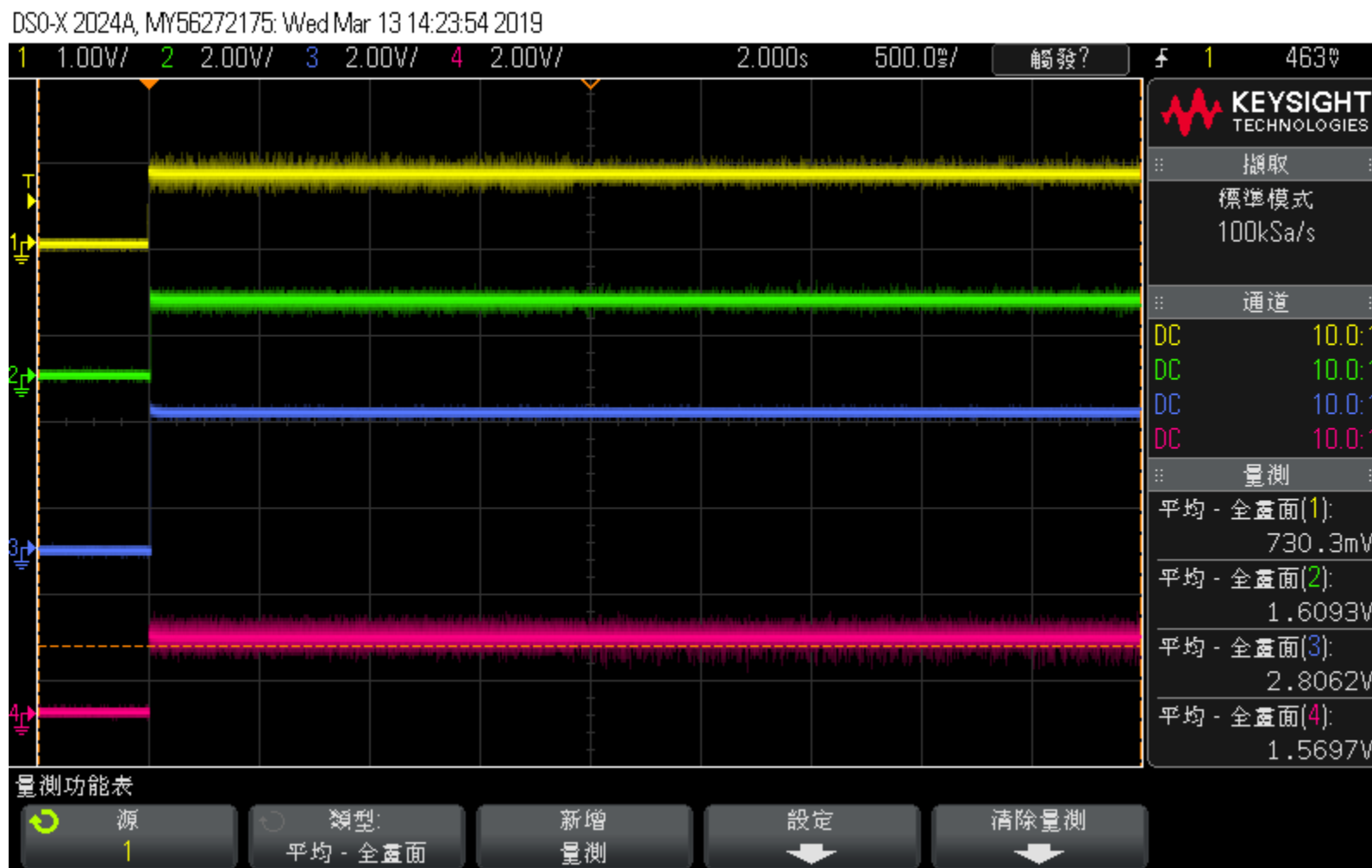
CH1:VBG  
CH3:LDO2\_OUT

CH2:LDO1\_OUT  
CH4:LDO3\_OUT



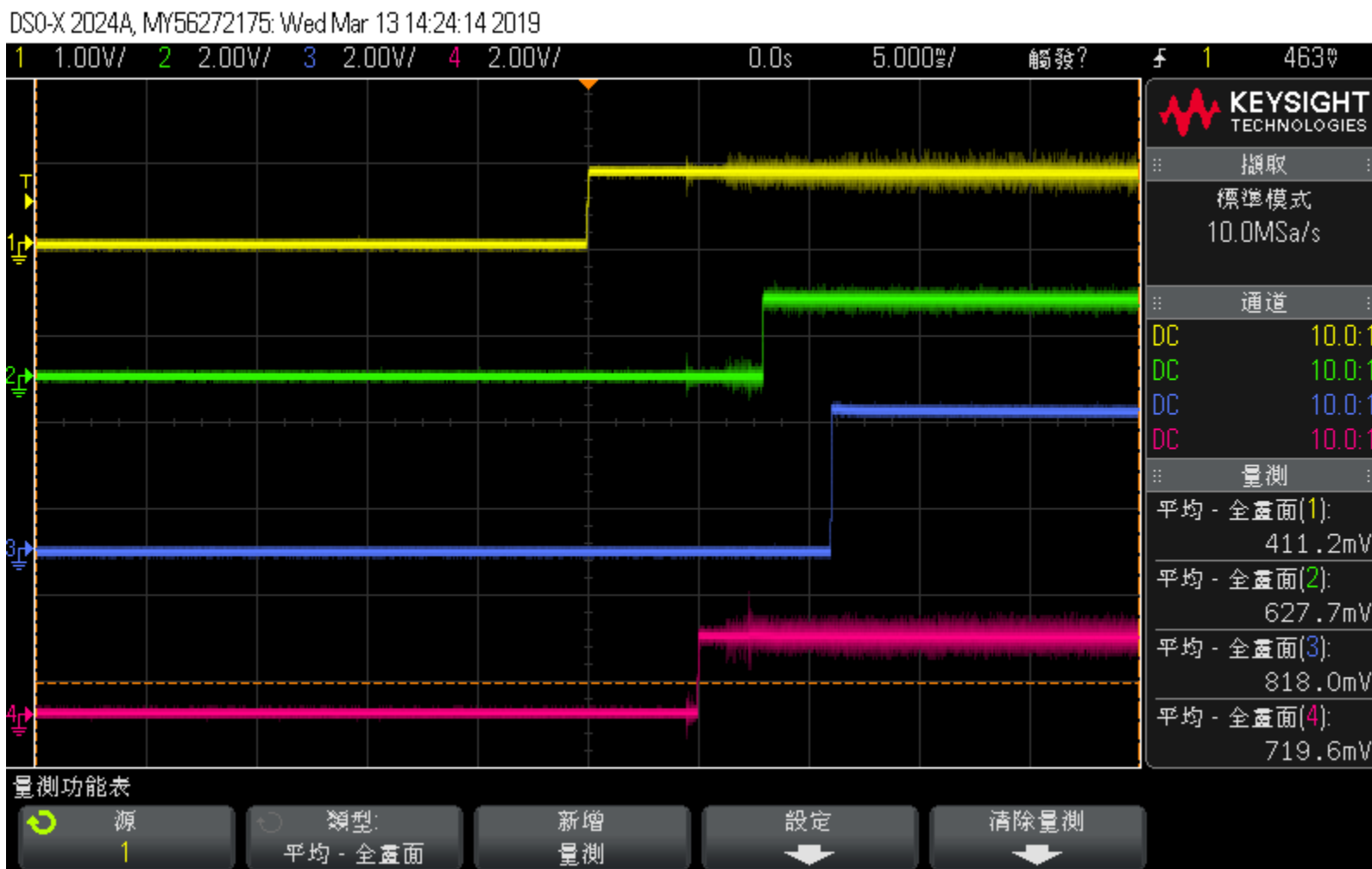
CH1:VBG  
CH3:LDO4\_OUT

CH2:LDO3\_OUT  
CH4:LDO5\_OUT



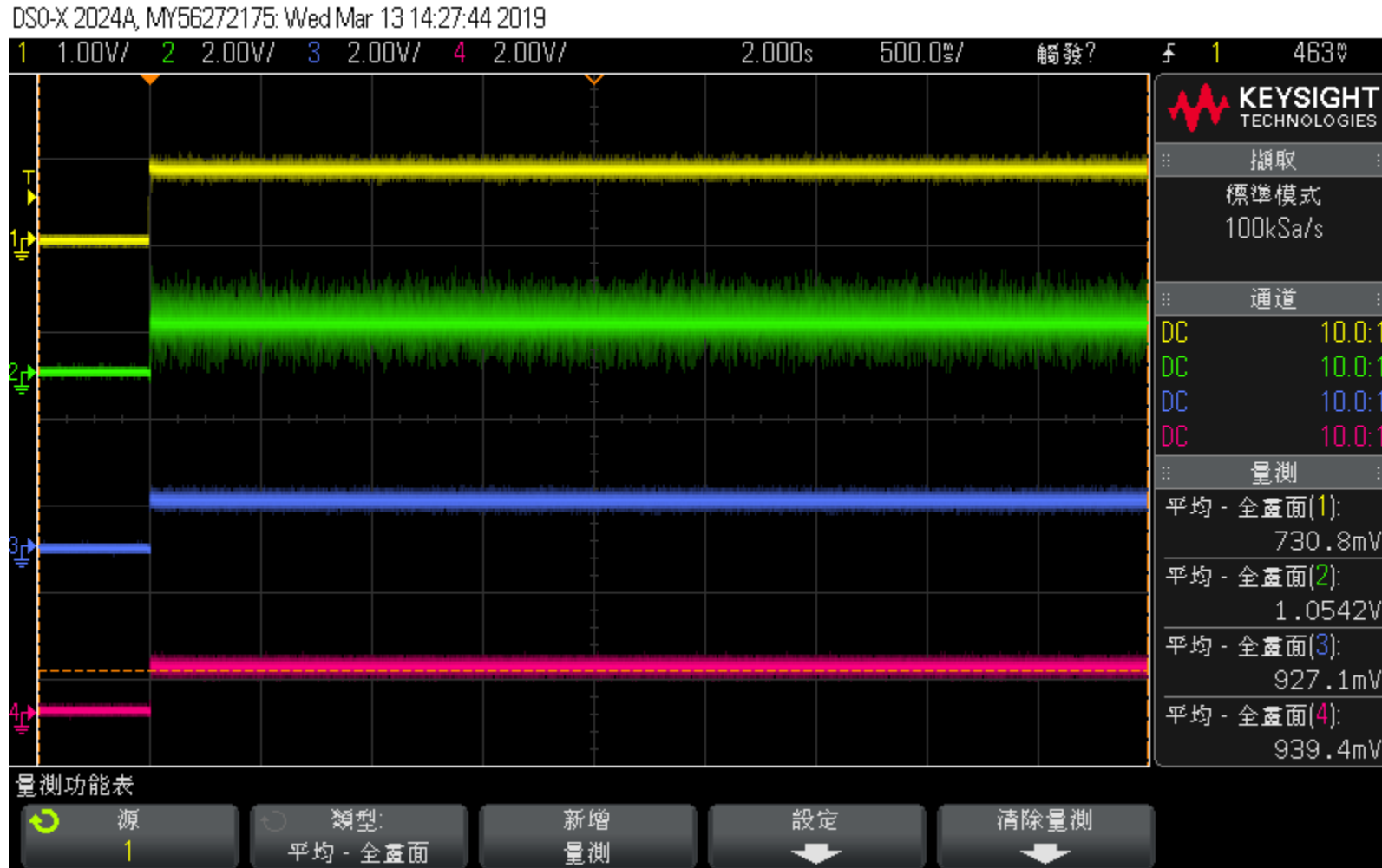
CH1:VBG  
CH3:LDO4\_OUT

CH2:LDO3\_OUT  
CH4:LDO5\_OUT



CH1:VBG  
CH3:SMPS2\_OUT

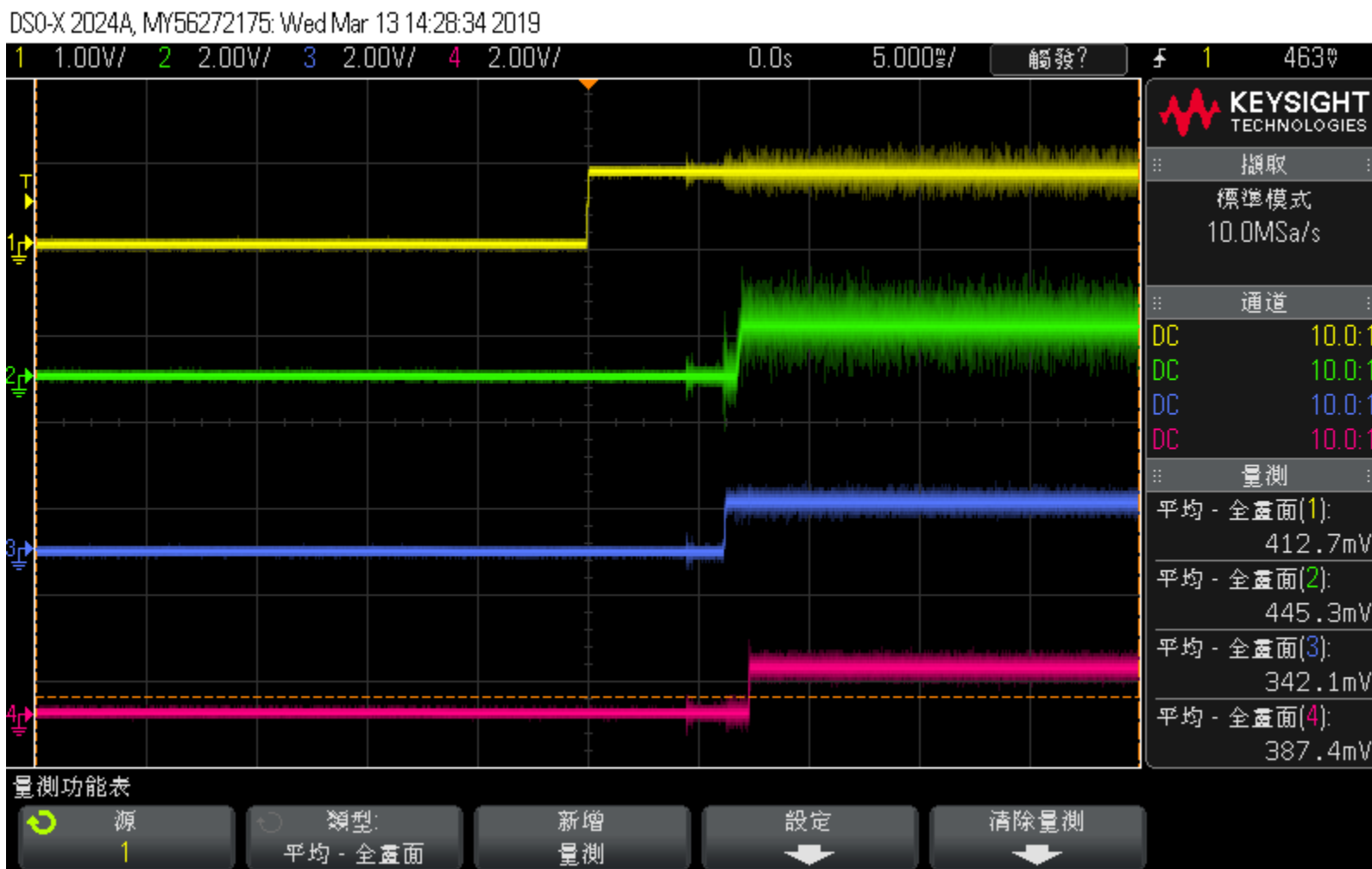
CH2:SMPS1\_OUT  
CH4:SMPS3\_OUT





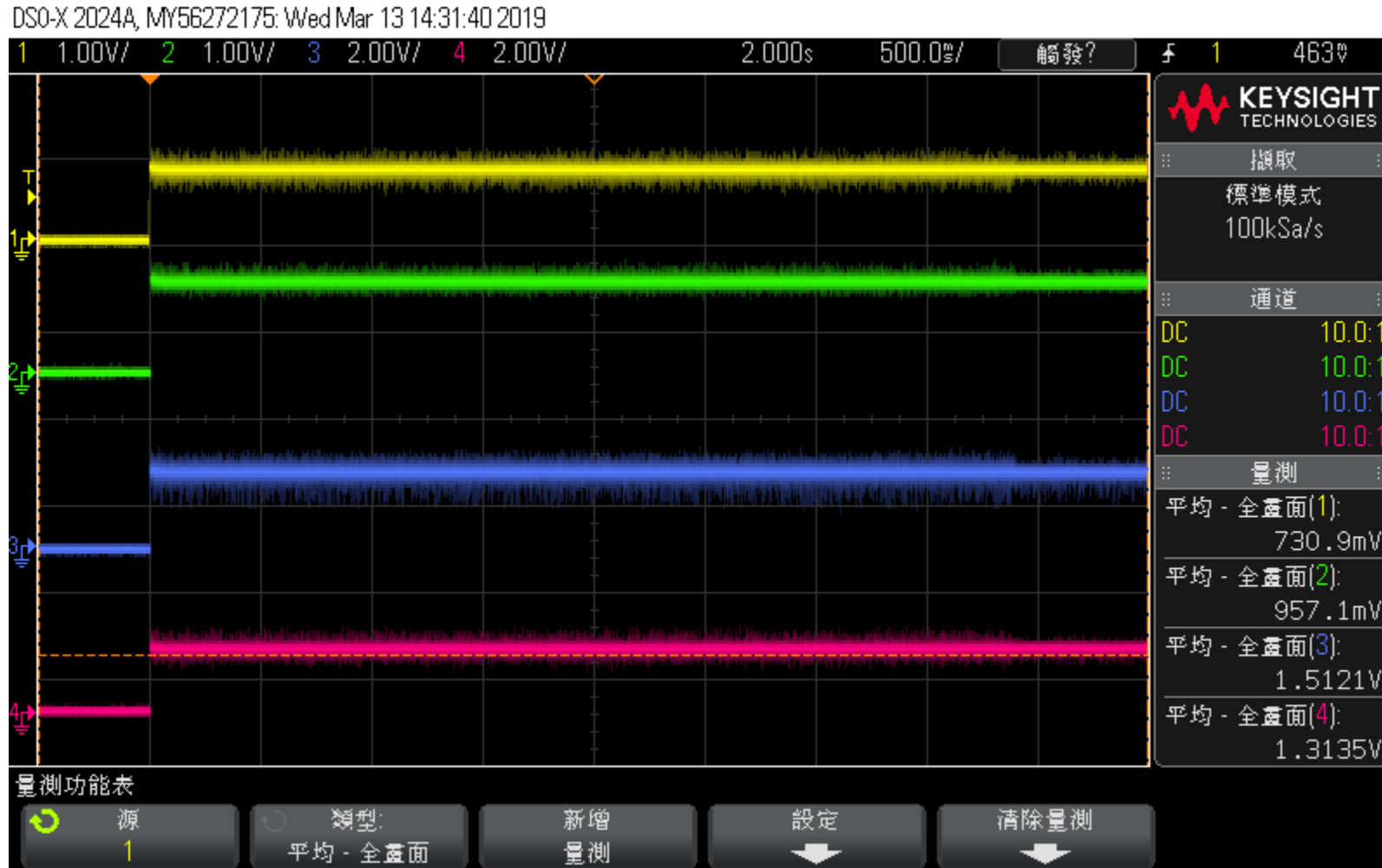
CH1:VBG  
CH3:SMPS2\_OUT

CH2:SMPS1\_OUT  
CH4:SMPS3\_OUT



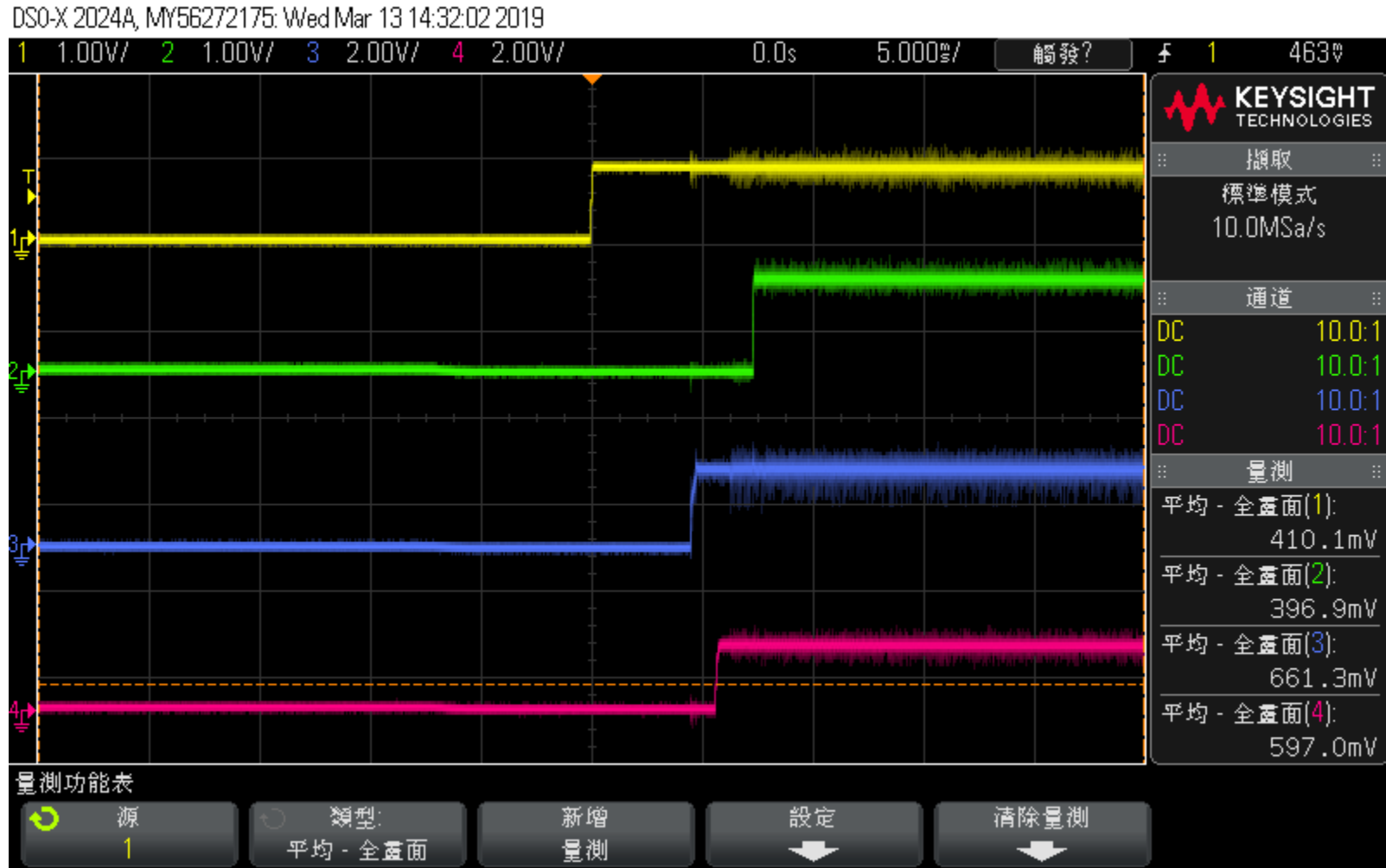
CH1:VBG  
CH3:SMPS4\_OUT

CH2:SMPS3\_OUT  
CH4:SMPS5\_OUT



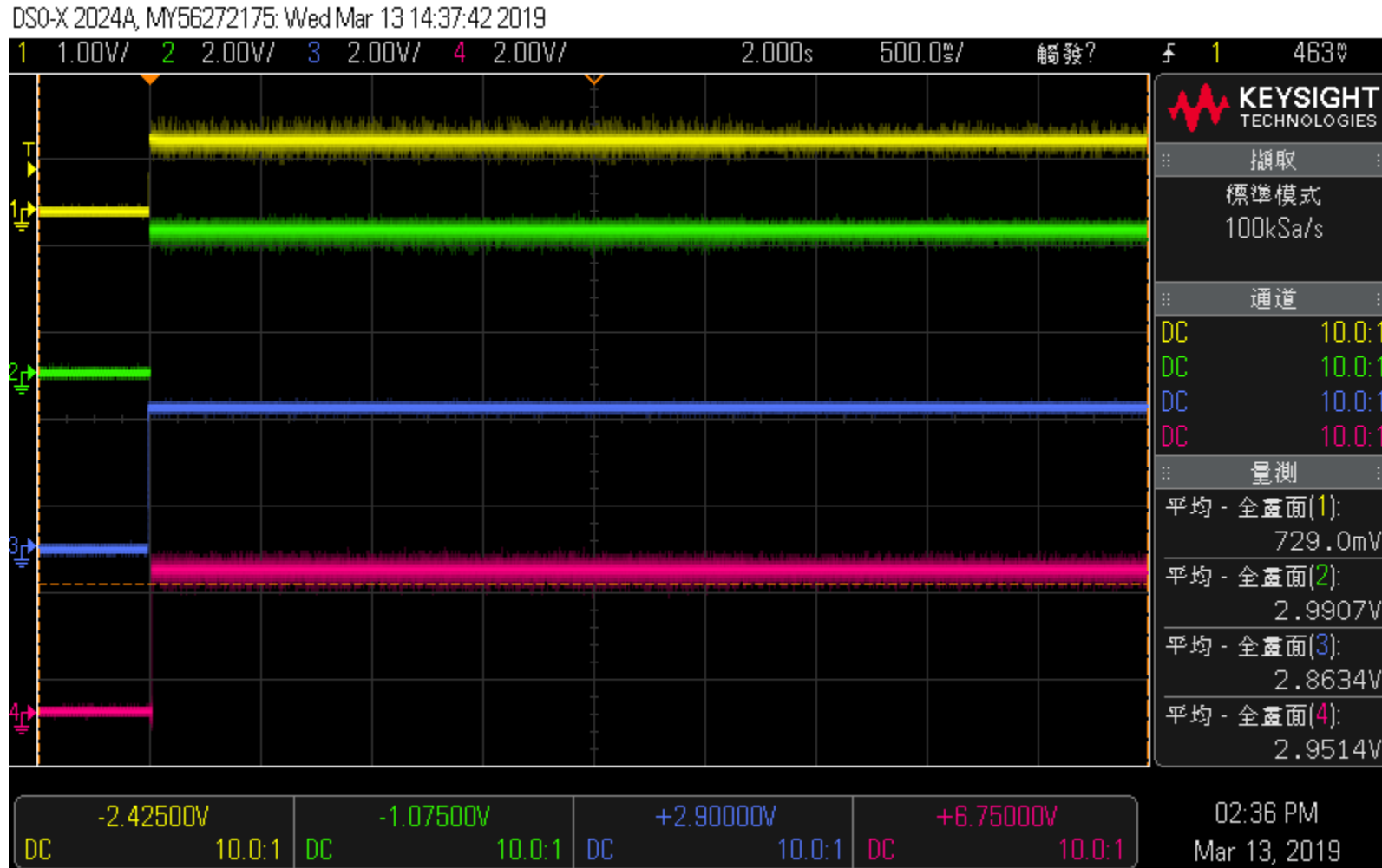
CH1:VBG  
CH3:SMPS4\_OUT

CH2:SMPS3\_OUT  
CH4:SMPS5\_OUT



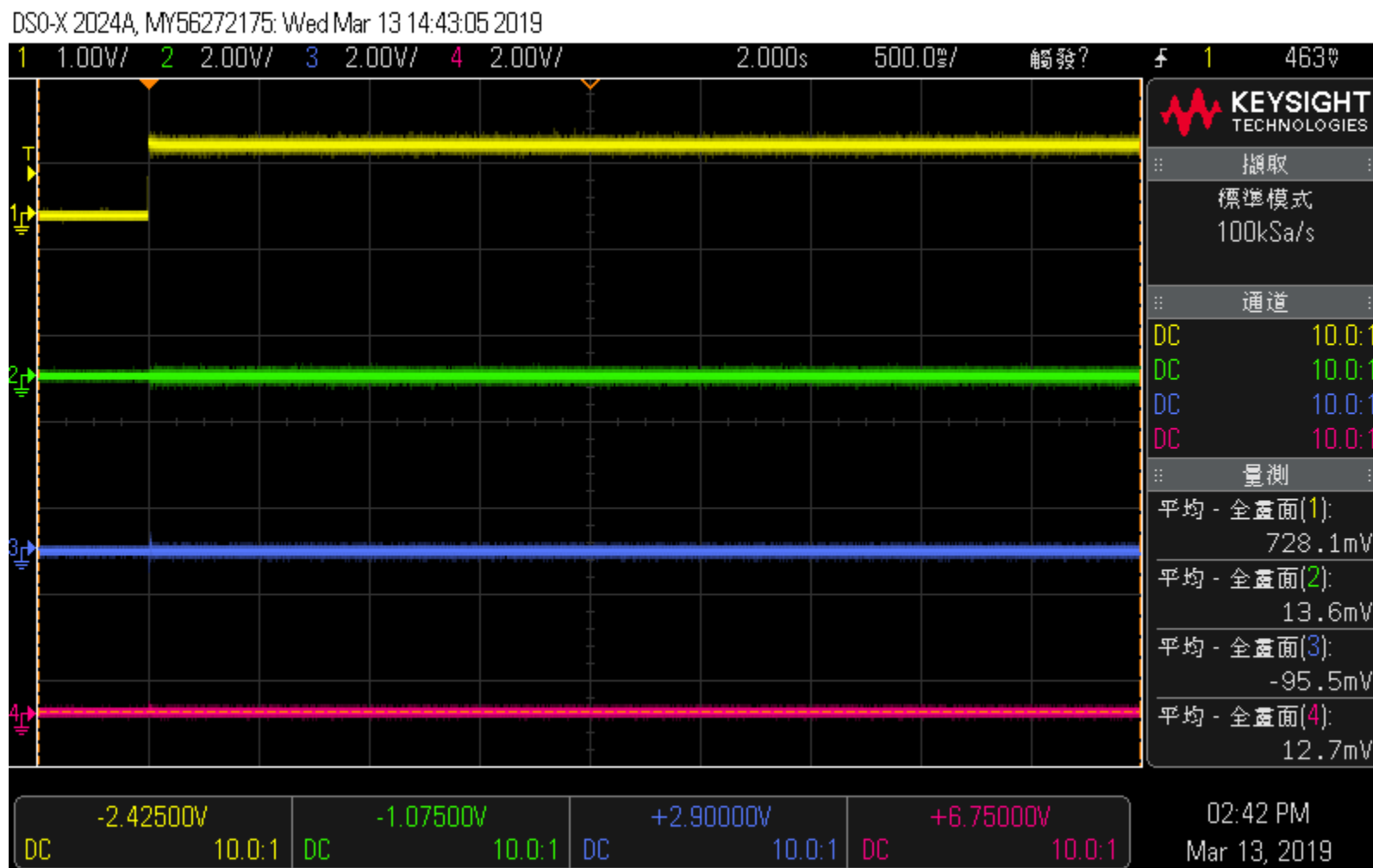
CH1:VBG  
CH3:PWRON

CH2:INT  
CH4:RESETOUT



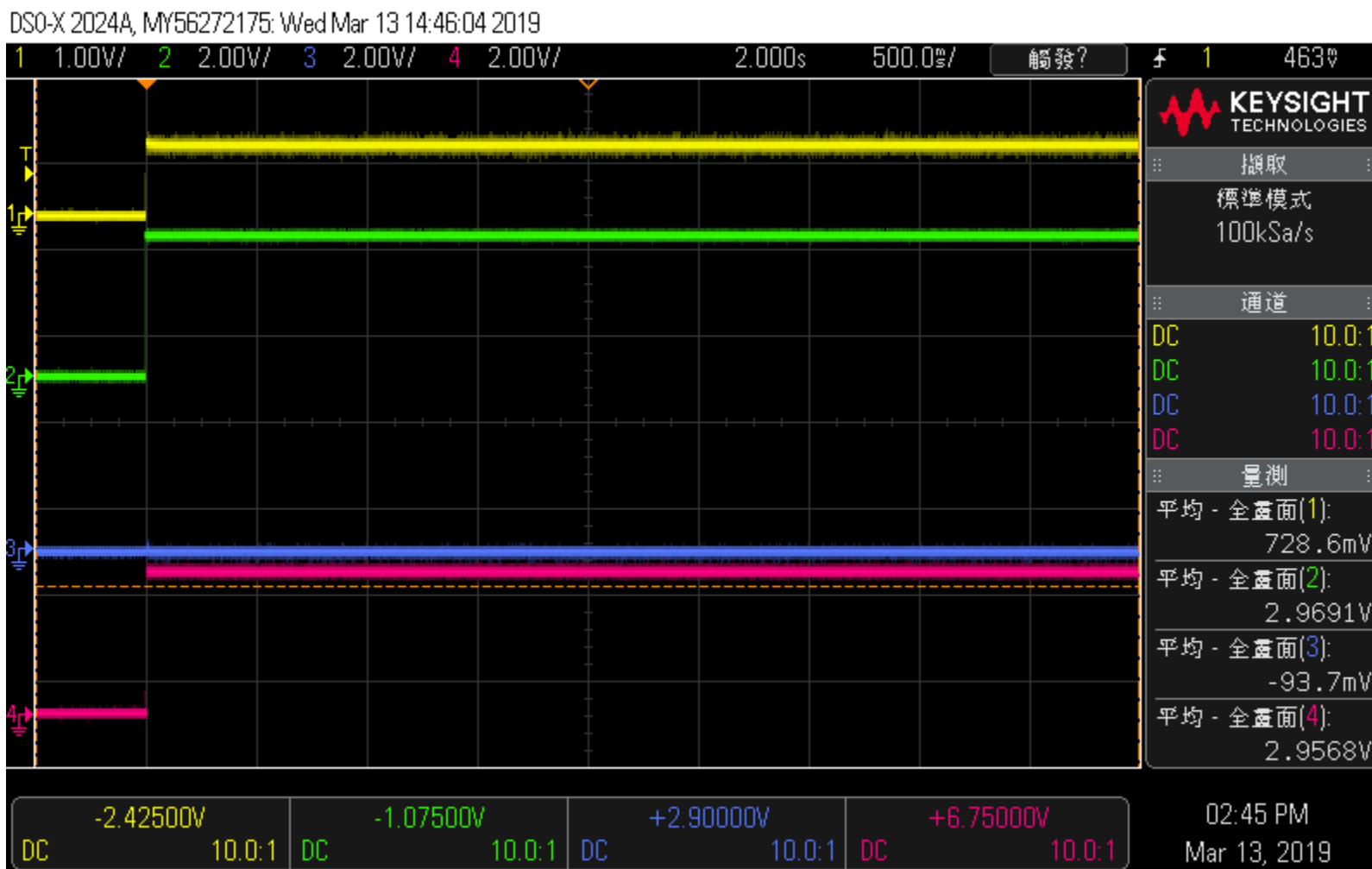
CH1:VBG  
CH3:GPIO0\_OUT

CH2:VPROG  
CH4:GPIO1\_OUT



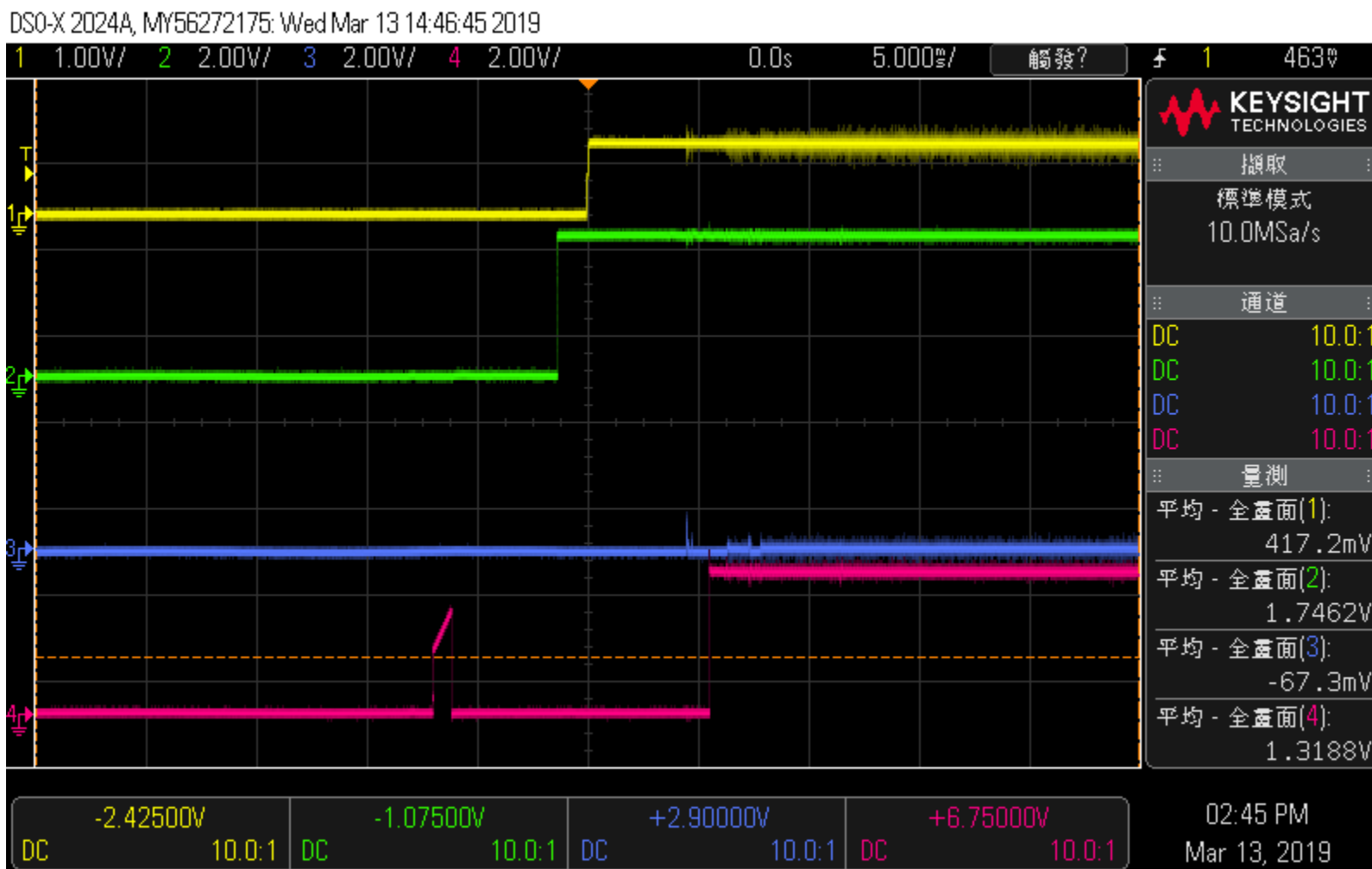
CH1:VBG  
CH3:GPIO3\_OUT

CH2:GPIO2\_OUT  
CH4:GPIO4\_OUT



CH1:VBG  
CH3:GPIO3\_OUT

CH2:GPIO2\_OUT  
CH4:GPIO4\_OUT



CH1:VBG  
CH3:LDO\_IN

CH2:GPIO6\_OUT  
CH4:VIO\_IN

