#### 30V Nch+Nch Middle Power MOSFET

V <sub>DSS</sub>	30V
R <sub>DS(on)</sub> (Max.)	21.4mΩ
I <sub>D</sub>	±9.0A
$P_D$	3.0W

#### Features

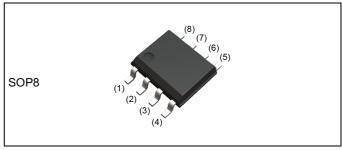
- 1) Low on resistance.
- 2) Small Surface Mount Package (SOP8).
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

# Application

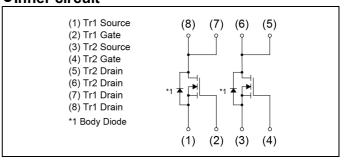
Switching

Motor Drive

#### Outline



## ●Inner circuit



Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	12
	Basic ordering unit (pcs)	2500
	Taping code	ТВ
	Marking	SH8KA4

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit		
Drain - Source voltage		V <sub>DSS</sub>	30	V	
Continuous drain current		I <sub>D</sub> *1	±9.0	А	
Pulsed drain current		I <sub>DP</sub> *2	±18	Α	
Gate - Source voltage	V <sub>GSS</sub>	±20	V		
Avalanche current, single pulse	I <sub>AS</sub> *3	8.0	А		
Avalanche energy, single pulse		E <sub>AS</sub> *3	4.6	mJ	
		P <sub>D</sub> *1	3.0		
Dower dissination	total	P <sub>D</sub> *4	2.0	10/	
Power dissipation		P <sub>D</sub> *5	1.4	W	
	element	P <sub>D</sub> *4	1.4		
Junction temperature	T <sub>j</sub>	150	°C		
Operating junction and storage te	T <sub>stg</sub>	-55 to +150	°C		

#### ●Thermal resistance

Parameter		Cymbol	Values			Lleit
		Symbol	Min.	Тур.	Max.	Unit
	total	D *4	-	-	62.5	
Thermal resistance, junction - ambient	element	$R_{thJA}^{^{*4}}$	-	-	89.2	°C/W
	total	R <sub>thJA</sub> *5	-	-	89.2	

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Damanatan	0	0 1141	Values			l leit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	30	-	-	V	
Breakdown voltage	ΔV <sub>(BR)DSS</sub>	I <sub>D</sub> = 1mA		21		mV/°C	
temperature coefficient	ΔT <sub>j</sub>	referenced to 25°C	-	21	-	IIIV/ C	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{DS} = 0V$ , $V_{GS} = \pm 20V$	-	-	±100	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage	$\Delta V_{GS(th)}$	I <sub>D</sub> = 1mA		-3		m) //°C	
temperature coefficient	ΔT <sub>j</sub>	referenced to 25°C	-	-3	-	mV/°C	
Static drain - source	D *6	V <sub>GS</sub> = 10V, I <sub>D</sub> = 9.0A	-	16.5	21.4	0	
on - state resistance	R <sub>DS(on)</sub> *6	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 8.0A	-	22.2	28.9	mΩ	
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	3.4	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *6	$V_{DS} = 5V, I_{D} = 8.0A$	4.6	-	-	S	

<sup>\*1</sup> Pw ≤ 1s, Mounted on a ceramic board (30×30×0.8mm), Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  0.1mH, V<sub>DD</sub> = 15V, R<sub>G</sub> = 25 $\Omega$ , STARTING T<sub>i</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*5</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*6</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Parameter Sun		Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	640	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	-	110	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	90	-	
Turn - on delay time	t <sub>d(on)</sub> *6	V <sub>DD</sub> ≈ 15V,V <sub>GS</sub> = 10V	-	8	-	
Rise time	t <sub>r</sub> *6	I <sub>D</sub> = 4.5A	-	19	-	no
Turn - off delay time	$t_{d(off)}^{*6}$	$R_L = 3.3\Omega$	-	33	-	ns
Fall time	t <sub>f</sub> *6	$R_G = 10\Omega$	-	7	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Darameter	Cumbal	Conditions		Values			1 1:4
Parameter Symbol Condition		uoris	Min.	Тур.	Max.	Unit	
Total meta abanna	$Q_g^{*6}$	V <sub>DD</sub> ≈ 15V	V <sub>GS</sub> = 10V	-	15.5	-	
Total gate charge				-	7.9	-	<b>~</b> C
Gate - Source charge	Q <sub>gs</sub> *6	I <sub>D</sub> = 9.0A	V <sub>GS</sub> = 4.5V	-	3.1	-	nC
Gate - Drain charge	Q <sub>gd</sub> *6			-	2.8	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

# <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.67	
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	-	-	18	A
Forward voltage	V <sub>SD</sub> *6	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.67A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

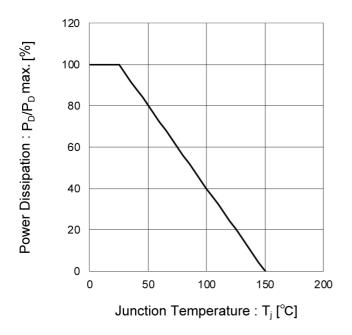
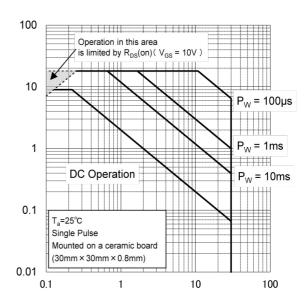


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

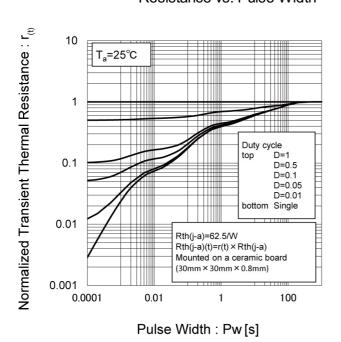
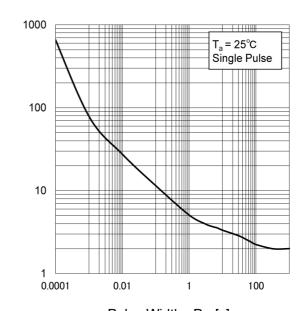


Fig.4 Single Pulse Maximum Power dissipation



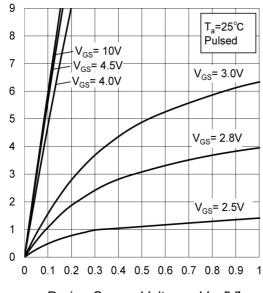
Pulse Width : Pw [s]

Peak Transient Power: P(W)

Drain Current : I<sub>D</sub> [A]

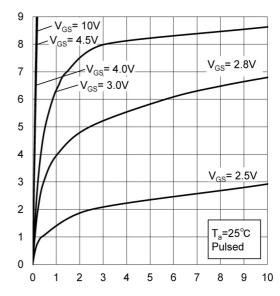
#### • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



 $Drain - Source \ Voltage : V_{DS} [V]$ 

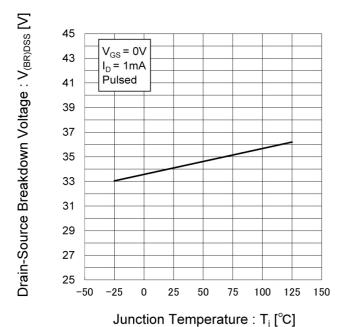
Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs. Junction Temperature



ROHM

Fig.8 Typical Transfer Characteristics

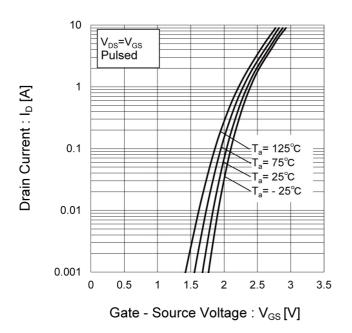


Fig.9 Gate Threshold Voltage vs. Junction Temperature

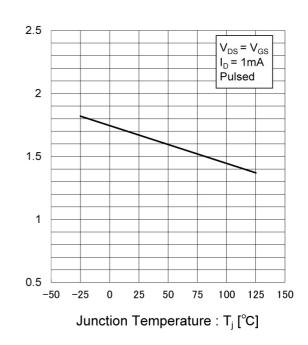
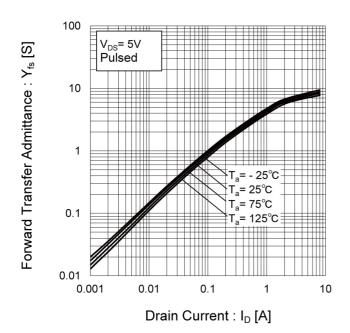


Fig.10 Forward Transfer Admittance vs. Drain Current



Gate Threshold Voltage : V<sub>GS(th)</sub> [V]

Fig.11 Drain Current Derating Curve

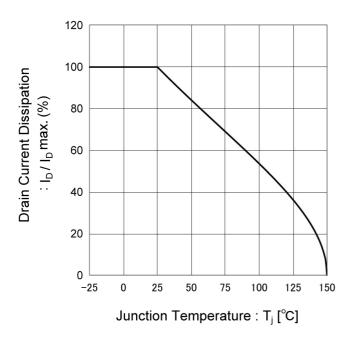


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

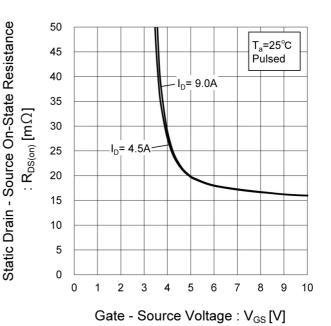


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

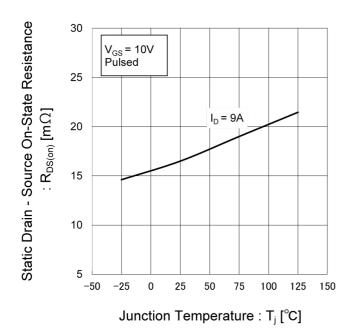


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

Static Drain Current :  $I_D$  [A]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

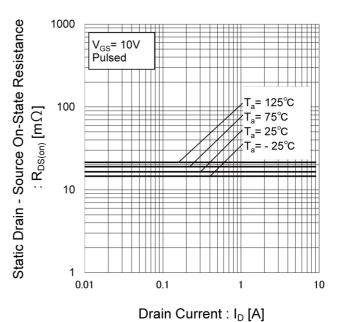


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

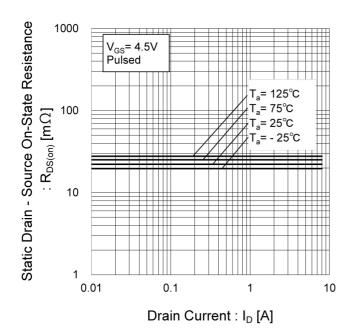
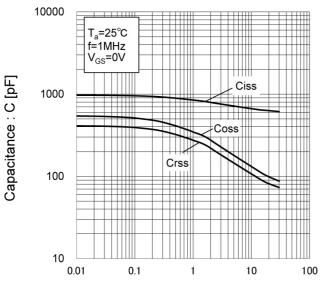
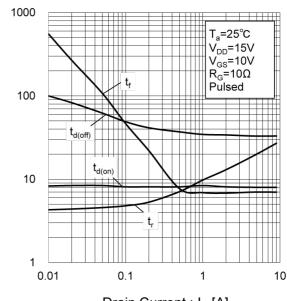


Fig.17 Typical Capacitance vs. Drain - Source Voltage



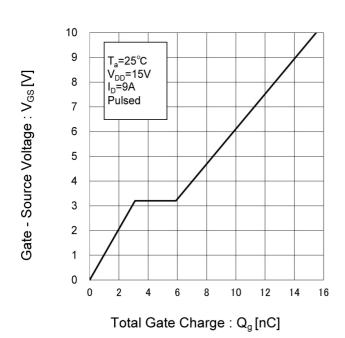
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.18 Switching Characteristics



Drain Current : I<sub>D</sub> [A]

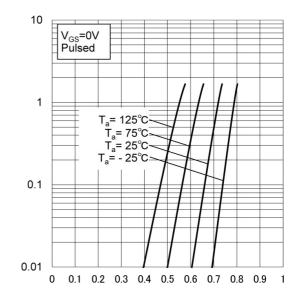
Fig.19 Dynamic Input Characteristics



Source Current :I<sub>S</sub> [A]

Switching Time : t [ns]

Fig.20 Source Current vs. Source Drain Voltage



Source-Drain Voltage: V<sub>SD</sub>[V]

# • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

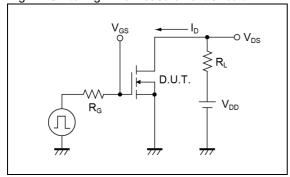


Fig.2-1 Gate Charge Measurement Circuit

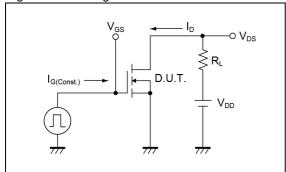


Fig.3-1 Avalanche Measurement Circuit

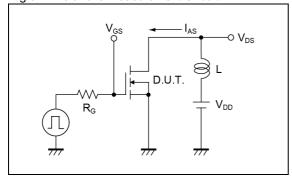


Fig.1-2 Switching Waveforms

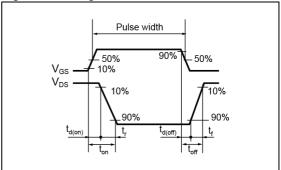


Fig.2-2 Gate Charge Waveform

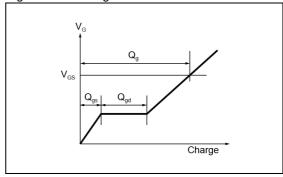
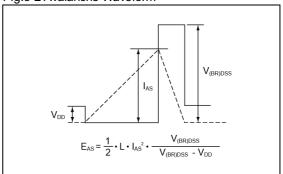


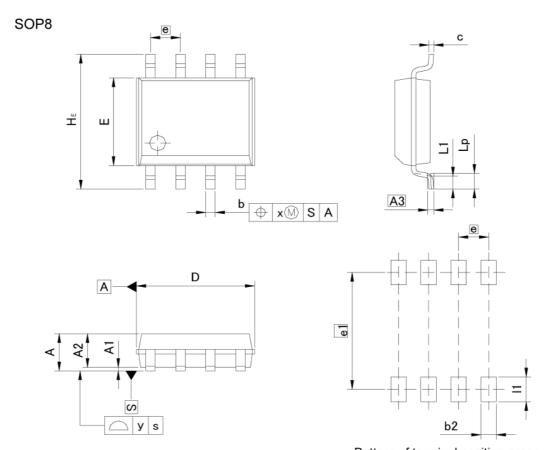
Fig.3-2 Avalanche Waveform



#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMI	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	-	1.75	-	0.069	
A1	0.	15	0.0	006	
A2	1.40	1.60	0.055	0.063	
A3	0.3	25	0.0	10	
b	0.30	0.50	0.012	0.020	
С	0.10	0.30	0.004	0.012	
D	4.80	5.20	0.189	0.205	
E	3.75	4.05	0.148	0.159	
е	1.3	27	0.0	50	
HE	5.70	6.30	0.224	0.248	
L1	0.40	0.60	0.016	0.024	
Lp	0.65	0.85	0.026	0.033	
х	0.15		0.006		
У	0.	0.10		004	
DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	

0.65

1.15

5.15

Dimension in mm/inches

b2

e 1 11



0.026

0.045

0.203

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