

**Power Supply Design Seminar** 

# **Considerations for Measuring Loop Gain in Power Supplies**

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# **Considerations for Measuring Loop Gain in Power Supplies**

**Manjing Xie** 



#### **Outline**

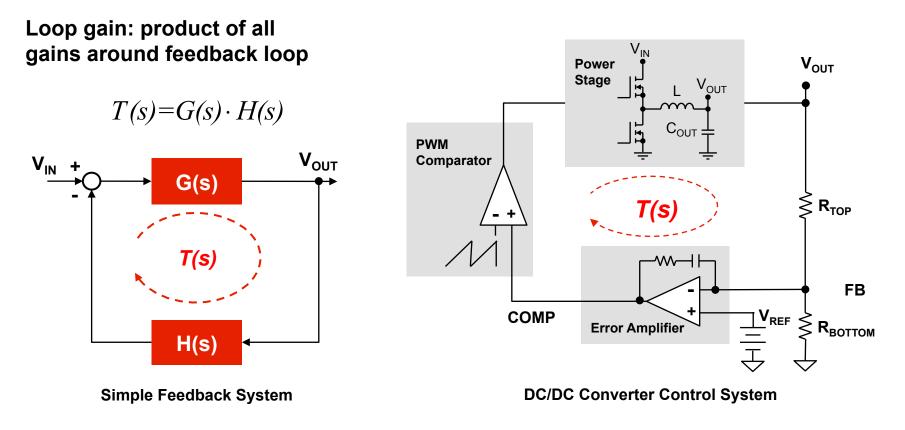
- Introduction loop gain overview
- Empirical loop gain measurement methods
- Test setup and test examples
  - Frequency analyzer setup
  - Preparing converter for loop gain measurement
  - Connecting equipment to circuit under test
- Summary

# Introduction – Loop Gain Overview

- What is loop gain?
- Why do we measure loop gain?

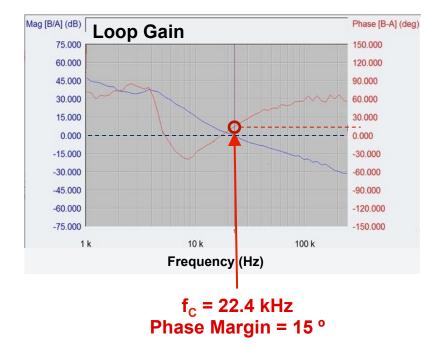


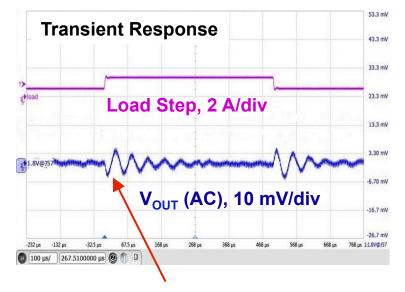
#### **Introduction – What Is Loop Gain?**



### Why Do We Measure Loop Gain?

#### 1) Loop gain is good indicator of stability

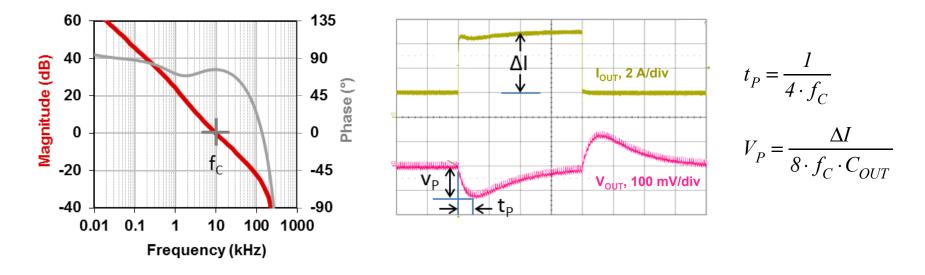




Output Oscillates at about 20 kHz

### Why Do We Measure Loop Gain?

#### 2) Loop gain results guide us to improve load transient response

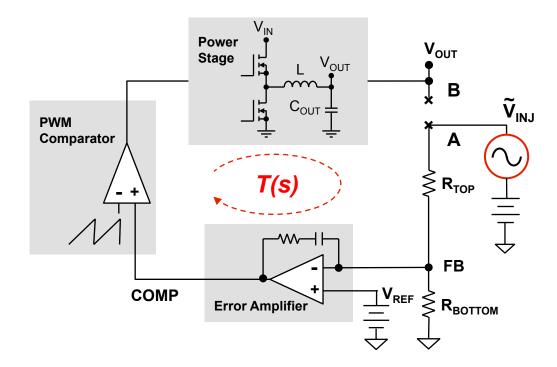


Given the loop is stable, higher loop bandwidth improves transient response [1]

# **Loop Gain Measurement Method**



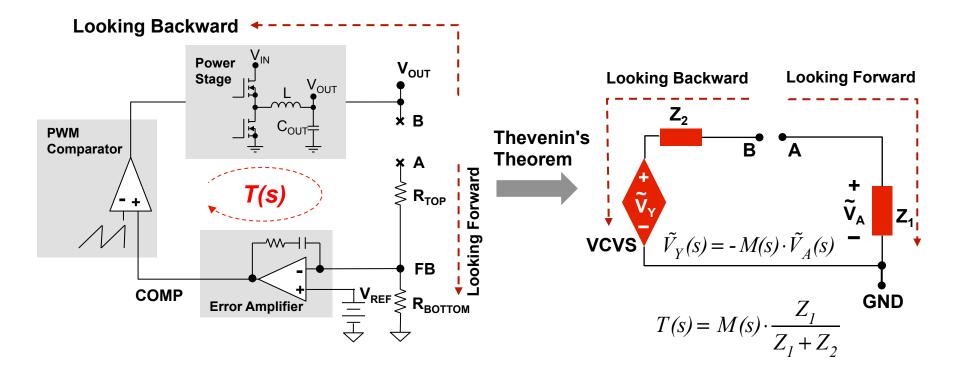
### Loop Gain Measured in Open-Loop Setup



- Difficult to maintain correct DC operating point due to high DC gain
- Easy to saturate circuits by injecting too much AC disturbance

It is not practical to measure loop gain in open-loop setup

### **Equivalent Circuit of Feedback System**



#### Loop Gain Measurement – Voltage Injection Method

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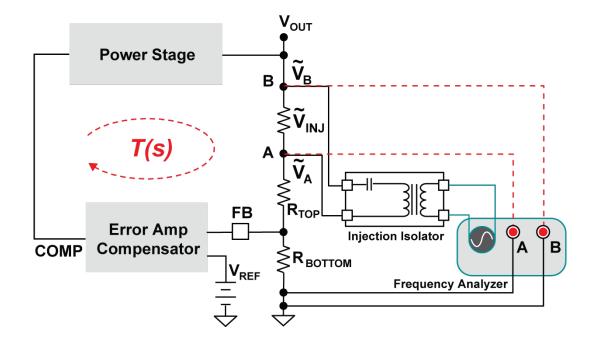
# **Test Setup**

- Frequency analyzer setup
- Selecting the correct injection isolator
- Preparing converter for loop gain measurement
- Minimizing error

6-11

### **Typical Loop Gain Measurement Setup**

- Setting up equipment
  - Frequency analyzer
  - Source injection isolator
- Setting up power supply
  - Identify voltage injection point
  - Connect equipment to circuit



### **Frequency Analyzer's Functions**

 $R_{OUT} = 50 \Omega$ 

- Provides AC voltage source:  $\tilde{V}_{SRC}$
- Measures response:  $\tilde{V}_{A}$  and  $\tilde{V}_{R}$
- Calculates loop gain:  $T(s) = -\frac{V_B}{\tilde{V}}$

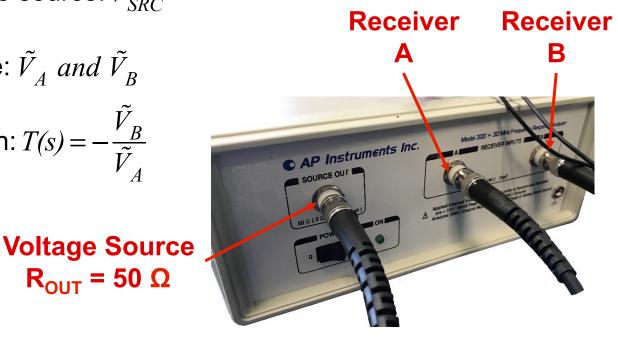
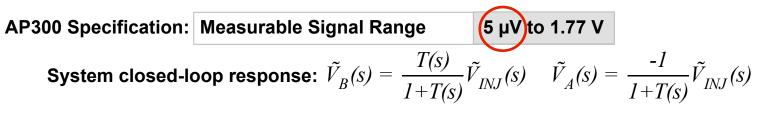
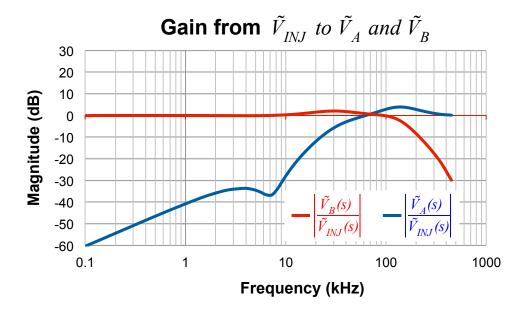


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### **Injecting Sufficient AC Voltage**

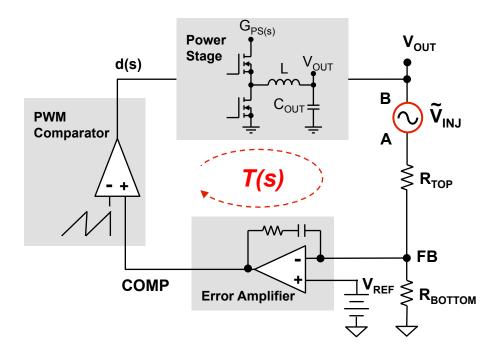




- Both  $\tilde{V}_A$  and  $\tilde{V}_B$  should be of sufficient amplitude for loop gain measurement
- At f = 100 Hz, to have  $|\tilde{V}_A| > 5 \mu V, |\tilde{V}_{INJ}| > 5 mV$
- At f = 300 kHz, to have

 $\left|\tilde{V}_{B}\right| > 5 \,\mu V, \left|\tilde{V}_{INJ}\right| > 50 \,\mu V$ 

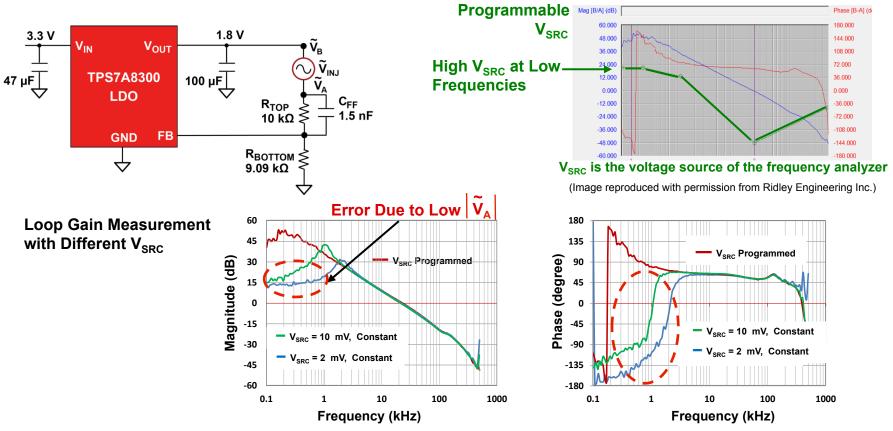
### **Excessive Voltage Injection Leads to Saturation**



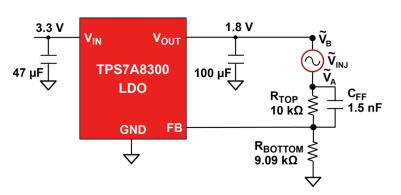
# What could happen if voltage injection is excessive?

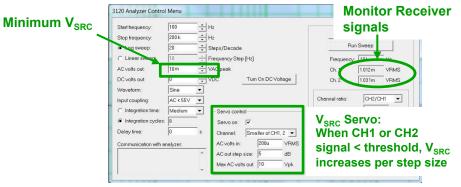
- Duty-cycle saturation
- Error amplifier saturation
- False triggering of over-current protection
- False triggering of over-voltage protection
- LDO driver and pass device saturation

### **Programmable V**<sub>SRC</sub>

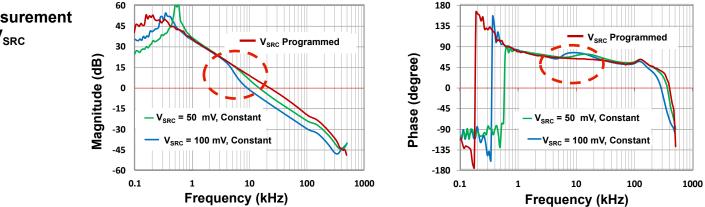


# **Servo Control V**<sub>SRC</sub>





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Loop Gain Measurement with Different  $\mathbf{V}_{\text{SRC}}$ 

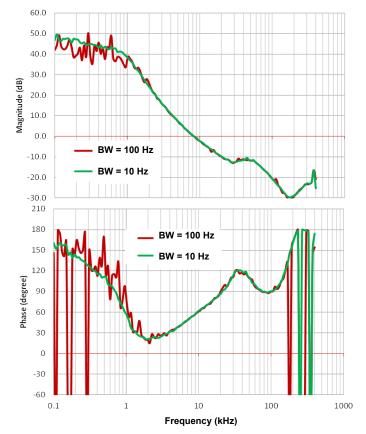
#### Frequency Analyzer– IF Bandwidth (Integration Time)

MANJING.nac - AP Instruments Frequency Response Analyzer
<u>Eile E</u> dit <u>V</u> iew <u>G</u> raph <u>I</u> ools <u>H</u> elp
III → II Start 1,000.00 Hz Stop 500,000.00 Hz 日本 III 1,000.00 Hz
[On Off ]
Markers 2 🕂 Active 1 🗸 🌃 🔍 🔨 Search 0.00 dB 🔍 🔍 🖓

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top frequency:	20 k	÷н	z		Take Data at S	Start Freq
Log sweep:	10	_÷ s	teps/Decade		Run Swe	зер
C Linear sweep:	10	F	requency Step [Hz]		Frequency:	Hz
AC volts out:	194.8 m	÷V	AC peak		Ch. 1:	VRMS
DC volts out:	0	÷v	DC Tur	m On DC Voltage	Ch. 2:	VRMS
Waveform:	0	-				
wavelonn.	Sine	-				
leparcoupling:	AC < 500 V	- -	Nor	row intorm		
le par coupling:			-Servi		nediate frequ	• • •
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<ul> <li>Integration time:</li> <li>Integration cycles</li> </ul>	AC < 500 V Medium	· ·		ess susce	•	• • •
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#### **Using Injection Isolator with Correct Frequency Range**

#### **Injection Isolator**

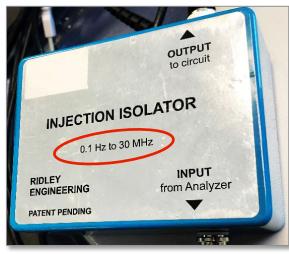


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#### BODE BOX<sup>™</sup>: Injection Isolator + Pre-Connected Signal Receiver

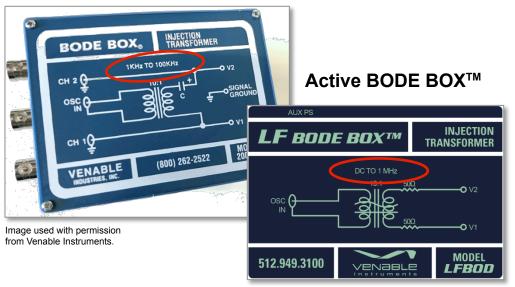


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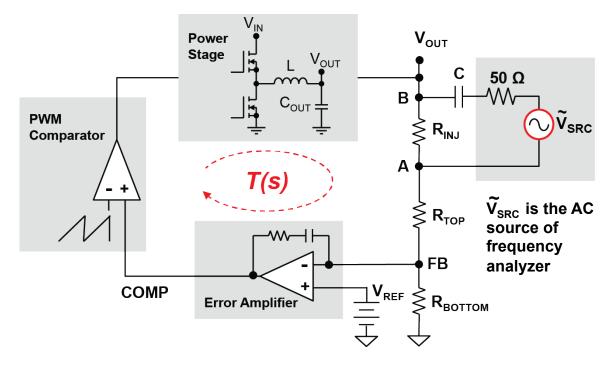
# Setting Up Circuit for Loop Gain Measurement

- Identify correct voltage injection point
- Connect equipment to circuit under test



### **Maintaining DC Static Operating Point**

Insert small resistor (10  $\Omega$  to 100  $\Omega$  ) between point A and B to maintain ~ same static operating point



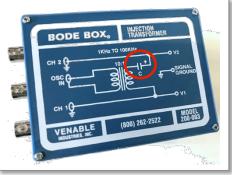


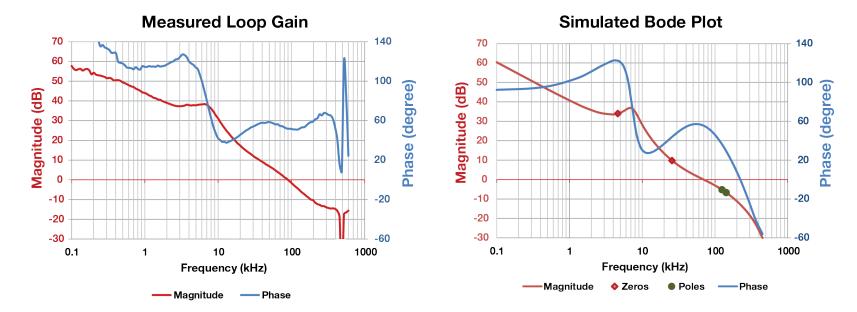
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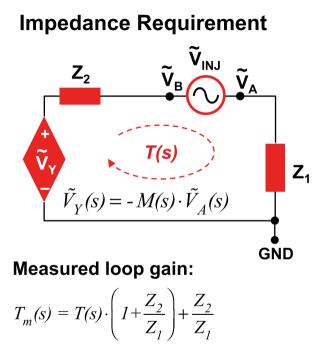
### **Measuring Loop-Gain for VMC**

#### TPS40425, Dual-Phase Buck Converter



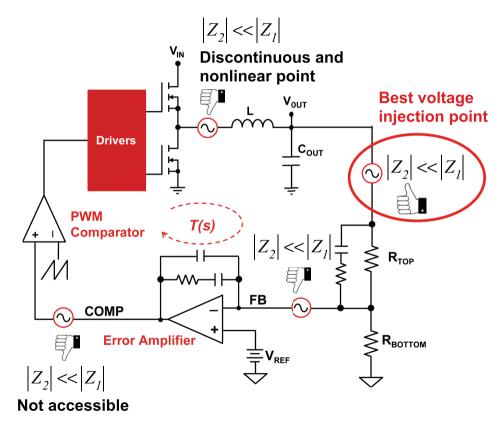
Measured crossover frequency and phase margin match simulation well!

### **Correct Voltage Injection Point**

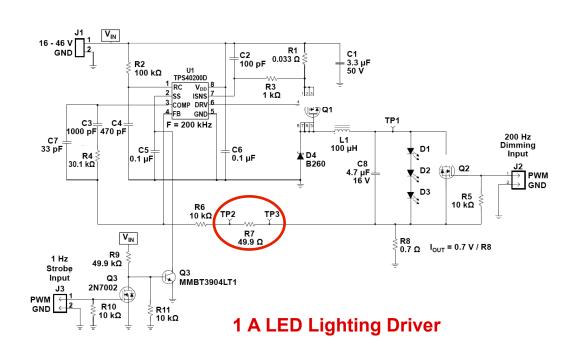


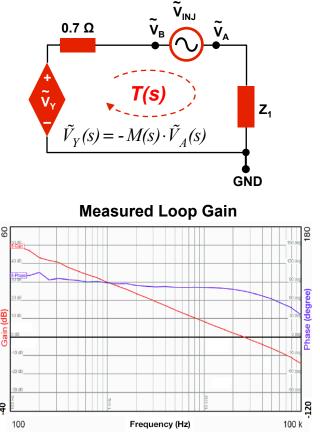
Injection point should have:

 $\left|Z_{2}\right| << \left|Z_{1}\right|$ 



### Measuring Loop Gain for LED Driver



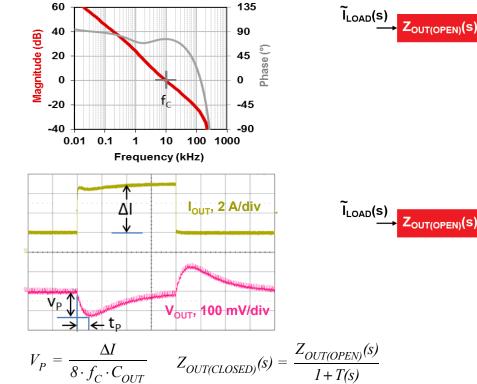


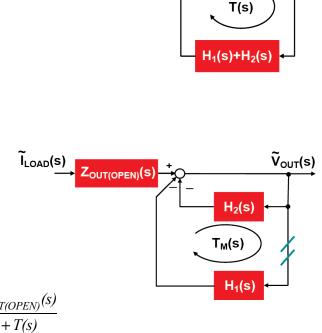
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### **Multiple Feedback Paths**





$$T(s) = H_1(s) + H_2(s)$$
$$Z_{OUT(CLOSED)}(s) = \frac{Z_{OUT(OPEN)}(s)}{1 + H_1(s) + H_2(s)}$$

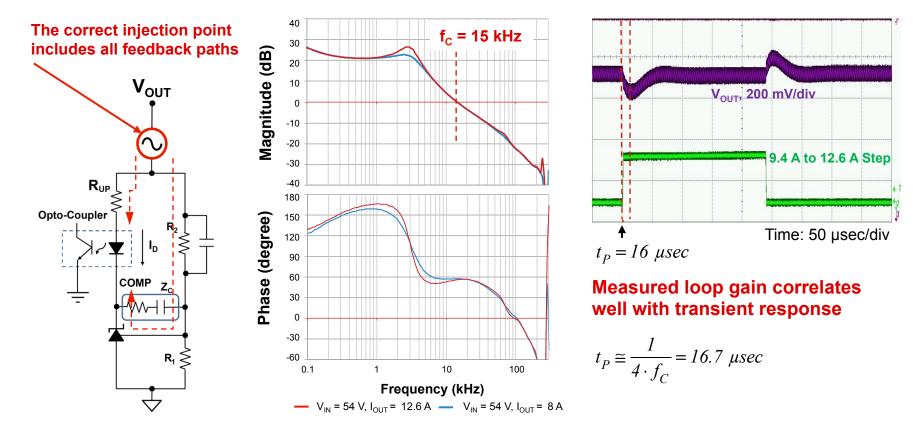
- Bandwidth of T(s) can predict transient performance
- Loop gain with feedback path 2 closed:

Ũ<sub>о∪т</sub>(s)

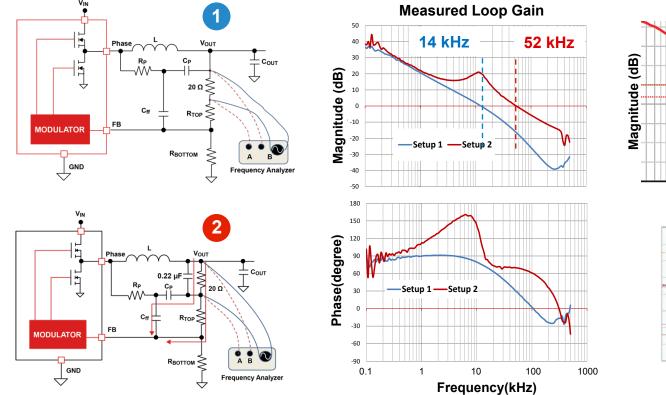
$$T_M(s) = \frac{H_1(s)}{1 + H_2(s)}$$

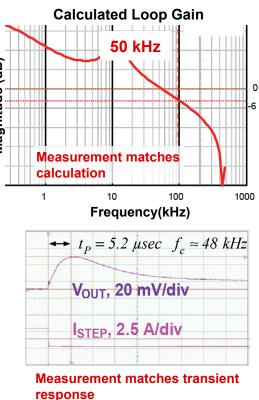
 Bandwidth of T<sub>M</sub>(s) might not tell how transient response performs

#### **Multiple Feedback Paths – Isolated Converter**

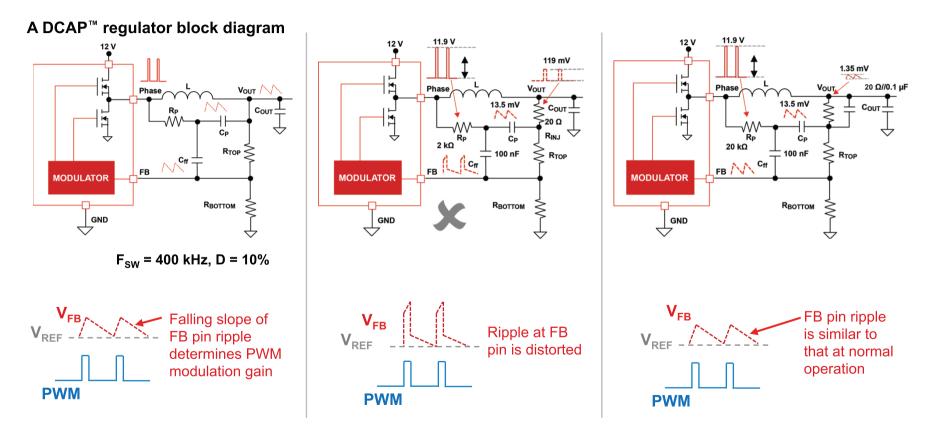


### Multiple Feedback Paths – D-CAP<sup>™</sup> Control





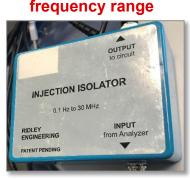
### Maintaining AC Operating Point – DCAP<sup>™</sup> Control



### **Measuring Loop Gain for PFC Converter**

# Challenges of measuring loop gain for power factor correction converter

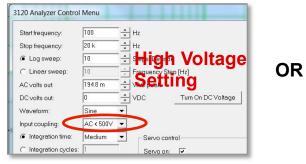
- Low control bandwidth
- High output voltage
- Use DC input



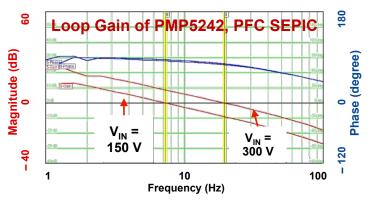
Isolator of correct

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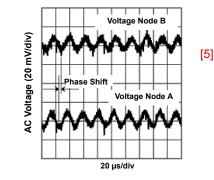
#### High-voltage frequency analyzer



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#### Oscilloscope for gain and phase measurement

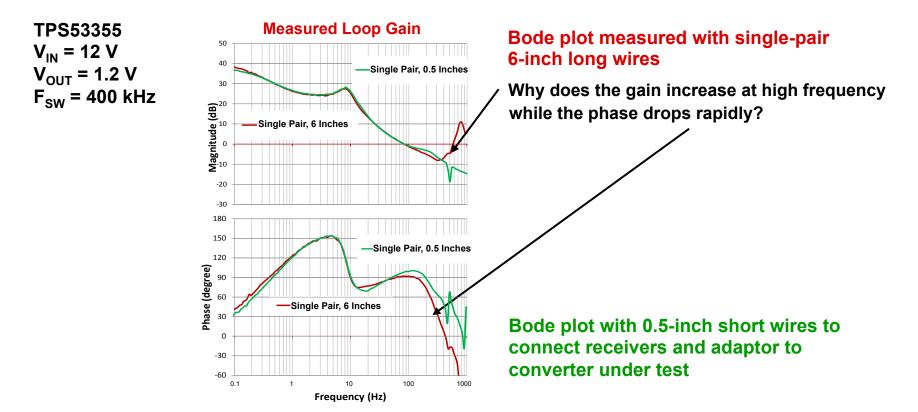


# **Connecting Equipment to Converter**

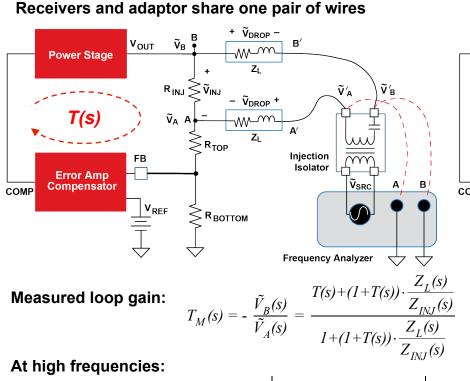
- How connection wires affect loop gain measurement
- Where to connect reference leads of two receivers



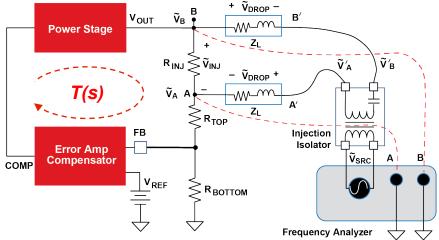
#### **Connection Wires Affect Loop Gain Measurement**



#### **How Connection Wires Affect Loop Gain Measurement**



#### Receivers and adaptor use separate wires



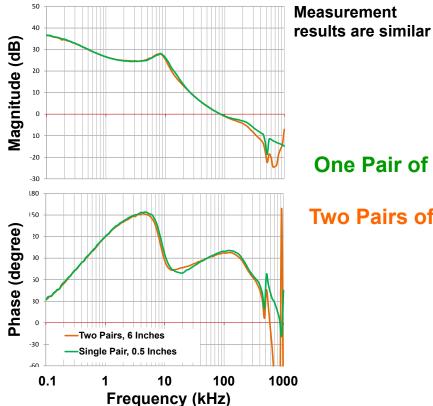
#### **Recommendation:**

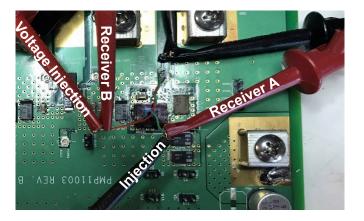
- Use wires as short as possible
- Use separate wires for measurement and voltage injection

$$\left|T_{M}(+j\infty)\right| \approx \left|\frac{Z_{L}(+j\infty)}{Z_{INJ}(+j\infty) + Z_{L}(+j\infty)}\right|$$

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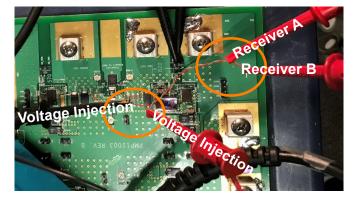
#### **Bench Verifications**



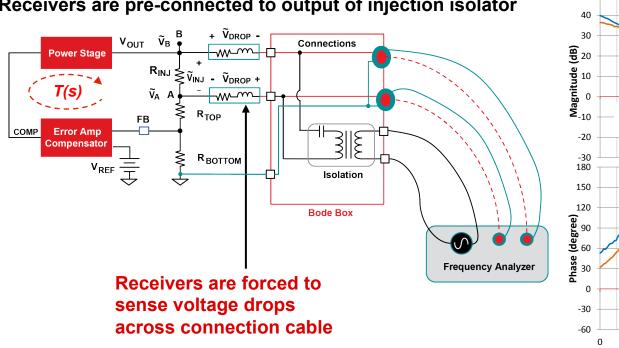


#### **One Pair of Wires**

#### **Two Pairs of Wires**



#### **Considerations for Bode Box**<sup>™</sup>



50

-Bode Box, 3 Feet

-Two Pairs, 6 Inches

-Bode Box, 3 Feet

1

-Two Pairs, 6 Inches

10

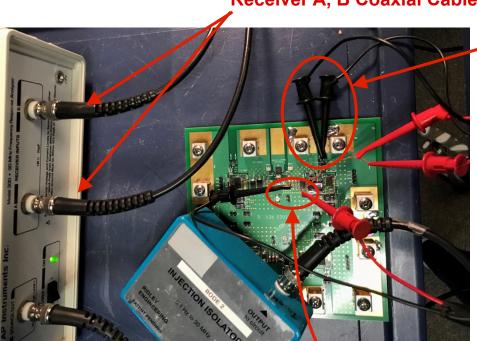
Frequency (kHz)

100

Receivers are pre-connected to output of injection isolator

1.000

#### Which Is the Correct Reference Point?



**Receiver A, B Coaxial Cables** 

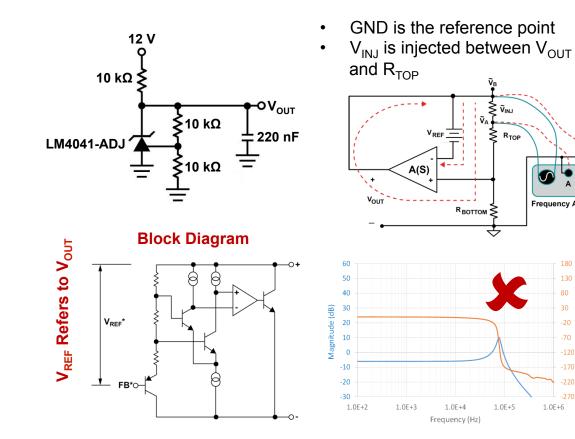


- For single-ended system, use controller signal ground for reference
- For converter with fully differential remote sensing, use remote negative sense for reference

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Voltage Injection Point A and B

### LM4041-N Shunt-Regulator



- $V_{OUT}$  is the reference point
- V<sub>IN.I</sub> is injected between GND and R<sub>BOTTOM</sub>

. V<sub>INJ</sub>

1.0E+5

R<sub>TOP</sub>

**Frequency Analyzer** 

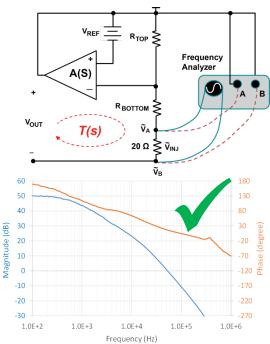
180

130

se (degree)

-120 님

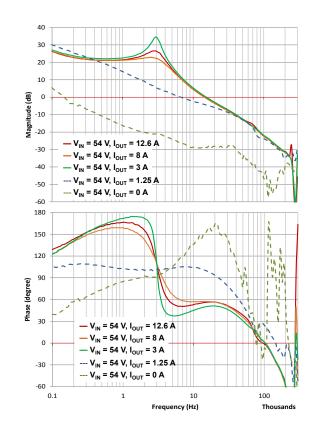
1.0E+6



### **Check Stability Over All Conditions**

# Compensation network should be designed so that system is stable over all conditions:

- Over input voltage range
- Over output current range
- Over temperature range
- Over output voltage range



# **Summary**

- Prepare circuit for test
  - Identify correct voltage injection point
    - Impedance looking backward should be as small as possible
    - Injection point should include all output feedback paths
  - Identify correct reference point
- Setup frequency analyzer with correct voltage source amplitude
- Select right injection isolator
- Maintain same DC and AC static operating point
- Receivers should not include voltage drops on connection wires
- · Check stability over all conditions

### References

- [1] Bob Sheehan, "How to determine bandwidth from the transientresponse measurement," Power Tips Blog.
- [2]John Betten, "Calculating capacitance for load transients", Power Tips, May 17th, 2015.
- [3] John Betten, "Control Loop Considerations for an LED Driver," EETimes, August 17, 2007.
- [4] Datasheet of LM4041-N.
- [5] Application Note AN1889, "How to measure the loop transfer function of power supplies."

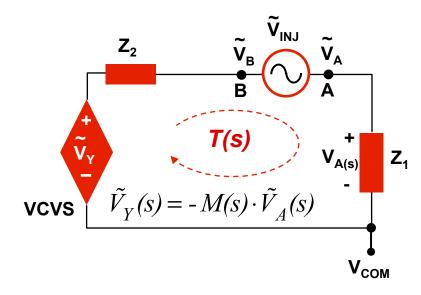






#### **Derivation of Closed-Loop Responses**

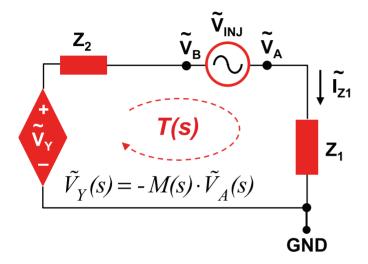
When  $Z_2 = 0 \Omega$ , T(s) = M(s)



$$\begin{split} \tilde{V}_B(s) &= -T(s) \cdot \tilde{V}_A(s) \\ \tilde{V}_{INJ}(s) &= \tilde{V}_B(s) - \tilde{V}_A(s) = -\left(T(s) + 1\right) \cdot \tilde{V}_A(s) \\ \Rightarrow \tilde{V}_A(s) &= -\frac{1}{T(s) + 1} \cdot \tilde{V}_{INJ}(s) \\ \Rightarrow \tilde{V}_B(s) &= \frac{T(s)}{T(s) + 1} \cdot \tilde{V}_{INJ}(s) \end{split}$$

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#### How Z<sub>2</sub> Affects Loop Gain Measurement



Loop gain: 
$$T(s) = M(s) \cdot \frac{Z_1(s)}{Z_1(s) + Z_2(s)}$$
  

$$\Rightarrow M(s) = T(s) \cdot \left(1 + \frac{Z_2(s)}{Z_1(s)}\right)$$

$$\tilde{V}_{ZI}(s) = \frac{V_A(s)}{Z_I(s)}$$
$$\tilde{V}_B(s) = -M(s) \cdot \tilde{V}_A(s) - \frac{Z_2(s)}{Z_I(s)} \cdot \tilde{V}_A(s) = -\left(M(s) + \frac{Z_2(s)}{Z_I(s)}\right) \cdot \tilde{V}_A(s)$$

Measured loop gain:

$$T_{m}(s) = -\frac{\tilde{V}_{B}(s)}{\tilde{V}_{A}(s)} = M(s) + \frac{Z_{2}(s)}{Z_{1}(s)}$$
$$T_{m}(s) = T(s) \cdot \left(1 + \frac{Z_{2}(s)}{Z_{1}(s)}\right) + \frac{Z_{2}(s)}{Z_{1}(s)}$$

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