

PSE Conformance Test Suite				Sifos Technologies		802.3at Conformance Report								
September 30 2020 5:17 PM				Test Mode: 30 Watt PHY		version 4.2.13								
Port Count: 2				Sifos Image Index: 87%		report version 4.2.01								
Loop Count: 1				Error Log: C:\Program Files\Sifos\PSA1200\Results\000.000.000.000.pse_error1										
Chassis ID: 10.100.89.73														
TestLoop: 1														
PSA-3000 Ports														
TestLoop: 1				1-1	1-2	UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F	
Test: det_v														
Open Circuit Det Voce				25.05	See Log	volts	25.05	25.05	25.05	2.8	Pass	30	Pass	
Peak Det Vvalide				7.11	See Log	volts	7.11	7.11	7.1	3.8	Pass	10	Pass	
Min Det Vvalide				4.53	See Log	volts	4.53	4.53	4.5	2.8	Pass	9	Pass	
Det Volt Stop Overv				2.54	See Log	volts	2.54	2.54	2.5	1	Pass	7.2	Pass	
Detection Slown				0	See Log	Vuasec	0	0	0	0	Pass	0.1	Pass	
Good Sig Det Pulses				7	See Log	edges	7	7	7	7	Pass	5	Pass	
Backoff Voltages				0.3	See Log	volts	0.3	0.3	0.3	0	Pass	2.6	Pass	
Non 802 Step Vn				0	See Log	volts	0	0	0	0	Pass	0.1	Pass	
High Sig MaxVn				11.3	See Log	volts	11.3	11.3	11.3	3.8	Pass	11	Info	
Non 802 Slact Tn				0	See Log	***	0	0	0	0	Pass	0	Pass	
Detect Strategyn				0	See Log	***	0	0	0	0	Pass	2	Pass	
Test: det_i														
Init Current Iacc				0.21	See Log	mA	0.21	0.21	0.21	0	Pass	5	Pass	
Det Current Iacc				0.2	See Log	mA	0.2	0.2	0.2	0	Pass	5	Pass	
Test: det_range														
Rgood Maxn				29	24	Kohm	24	29	26.5	26	Fail	32	Pass	
Rgood Minn				17	24	Kohm	17	24	20.5	16	Pass	19	Fail	
Rbad Maxn				24	24	Kohm	24	29	26.5	26	Fail	33	Pass	
Rgood Maxn				0	0	uF	0.1	0.1	0.1	0	Pass	10	Pass	
Rbad Chsd State				0	0	***	0	0	0	0	Pass	0	Pass	
Test: det_time														
Backoff Time Tdcon				320	305	msec	305	320	312.5	-1	Pass	1500	Pass	
Sfr Backoff Tdpo affn				320	15000	msec	320	15000	7690	-1	Pass	1500	Info	
Backoff Typen				0	0	***	0	0	0	0	Pass	0	Pass	
Detection Time Tdact				773	12	msec	0	12	773	392.5	5	Pass	500	Fail
Total Det Timen				781	31	msec	31	781	406	5	Pass	1000	Pass	
Test: det_resource														
Output Impedance Rout				450	See Log	KOhm	450	450	450	45	Pass	2000	Pass	
Test: class_v														
Class Voltage Vclassn				19	See Log	volts	19	19	19	15.5	Pass	20.5	Pass	
Voltage Minn				19	See Log	volts	19	19	19	15.5	Pass	20.5	Pass	
Mark Voltage Vmarkn				9.5	See Log	volts	9.5	9.5	9.5	7	Pass	10	Pass	
Mark Voltage Minn				9.5	See Log	volts	9.5	9.5	9.5	7	Pass	10	Pass	
Test: class_time														
Event Countn				3	See Log	***	3	3	3	2	Pass	3	Pass	
Event Tolein				88.9	See Log	msec	88.9	88.9	88.9	5.6	Pass	30	Fail	
Event Tolein				9.7	See Log	msec	9.7	9.7	9.7	5.6	Pass	30	Pass	
Mark Timein				7.8	See Log	msec	7.8	7.8	7.8	5.6	Pass	12.4	Pass	
Mark Timein				7.8	See Log	msec	7.8	7.8	7.8	5.6	Pass	376	Pass	
Test: class_err														
Class Limn				-1	-1	mA	-1	-1	-1	51	Fail	100	Pass	
Per Cl Limn				-1	-1	***	-1	-1	-1	0	Fail	0	Pass	
Per Cl V55n				-1	-1	***	-1	-1	-1	0	Fail	0	Pass	
Mark Limn				-1	-1	mA	-1	-1	-1	5	Fail	100	Pass	
Per Cl Unresn				-1	-1	***	-1	-1	-1	0	Fail	0	Pass	
Transn				0	0	msec	0	0	0	15	Fail	10000	Pass	
Test: perup_time														
Per-On Rise Time Trisen				93	See Log	usec	93	93	93	15	Pass	50000	Pass	
Power-On Time Tponn				142.6	See Log	msec	142.6	142.6	142.5	0	Pass	400	Pass	
Test: perup_inrush														
Init Inrushn				416.75	See Log	mA	416.75	416.75	416.8	400	Pass	450	Pass	
Max Inrush c4n				415.88	See Log	mA	415.88	415.88	415.9	400	Pass	450	Pass	
Min Inrushn				415	See Log	mA	415	415	415	400	Pass	450	Pass	
Inrush Tleinn				59.6	See Log	msec	59.6	59.6	59.6	50	Pass	75	Pass	
Inrush Tleinn				54.3	See Log	Volts	54.3	54.3	54.3	50	Pass	57	Pass	
Inrush Voltagen				31.1	See Log	Volts	31.1	31.1	31.1	30	Pass	57	Pass	
Max Inrushn				590.3	See Log	mA	590.3	590.3	590.3	0	Pass	2000	Pass	
Inrush Strategy c4n				0	See Log	***	0	0	0	0	Pass	1	Pass	
Test: peron_v														
Vpost min 2n				53	See Log	V	53	53	53	50	Pass	57	Pass	
Vpost max 2n				54.5	See Log	V	54.5	54.5	54.5	50	Pass	57	Pass	
Vpost ripple 2n				213	See Log	mVpp	213	213	213	0	Pass	500	Pass	
Vpost noise 2n				30	See Log	mVpp	30	30	30	0	Pass	200	Pass	
Vtrans min 2n				53	See Log	V	53	53	53	50	Pass	57	Pass	
Vtrans max 2n				54.8	See Log	V	54.8	54.8	54.8	50	Pass	57	Pass	
Test: peron_perup														
Pcon c4n				36.1	See Log	watts	36.1	36.1	36.1	28.7	Pass	38.9	Pass	
Icon 4 c4n				123	See Log	%	123	123	123	100	Pass	9999	Pass	
Pcon-3 Enablen				1	See Log	***	1	1	1	1	Pass	1	Pass	
Test: peron_max														
Ilim Peakn				200.8	See Log	mA	200.8	200.8	200.8	0	Pass	1750	Pass	
Ilim Min 2n				68.5	See Log	mA	68.5	68.5	68.5	68.1	Pass	1750	Pass	
Ilim 2n				9999	See Log	msec	9999	9999	9999	10	Pass	75	Fail	
Ilim 2n				52.4	See Log	V	52.4	52.4	52.4	50	Pass	57	Pass	
Ilim Max 2n				903	See Log	mA	903	903	903	0	Pass	1750	Pass	
Ilim Low V Vol 2n				9999	See Log	msec	9999	9999	9999	10	Pass	9999	Pass	
Ktran 10 2n				104.5	See Log	%	104.5	104.5	104.5	92.4	Pass	115	Pass	
Test: peron_overid														
Vlpeak 2n				125	See Log	%	125	125	125	100	Pass	125	Pass	
Vpost Ipeak 2n				52.6	See Log	V	52.6	52.6	52.6	50	Pass	57	Pass	
Vpost VDC 2n				52.7	See Log	V	52.7	52.7	52.7	50	Pass	57	Pass	
Test: ngs_dc_valid														
Min Valid Time Tngasn				10	See Log	msec	10	10	10	1	Pass	60	Pass	
Duty Cycle Tolei				1	See Log	***	1	1	1	1	Pass	1	Pass	
Test: ngs_dc_perdn														
Min Valid I holdn				7	See Log	mA	7	7	7	5	Pass	10	Pass	
Time-to-Undervoltage Tngasn				303	See Log	msec	303	303	303	300	Pass	400	Pass	
Max Voltage Vopen Backn				25	See Log	volts	25	25	25	-1	Pass	30	Pass	
Test: perdn_overid														
Iout 2n				-1	See Log	mA	-1	-1	-1	-1	Pass	1750	Pass	
Iout 2n				9999	See Log	msec	9999	9999	9999	10	Pass	9999	Pass	
Isodr 2n				-1	See Log	mA	-1	-1	-1	-1	Pass	883	Pass	
Isodr 2n				-1	See Log	msec	-1	-1	-1	-1	Pass	2000	Pass	
Test: perdn_time														
Turn-Off Time Toffn				44.4	See Log	mSec	44.4	44.4	44.4	0	Pass	500	Pass	
Output Cap. Cutoffn				0.1722	See Log	uF	0.1722	0.1722	0.17	-1	Pass	0.52	Pass	
Output Load Resn				125.4	See Log	KOhm	125.4	125.4	125	45	Pass	50000	Pass	
Test: perdn_v														
Avg Idle Volffn				0.4	See Log	VDC	0.4	0.4	0.4	0	Pass	2.8	Pass	
Error Delay Deln				9999	See Log	msec	9999	9999	9999	750	Pass	2000	Pass	
Peak Error Delay Vdeln				15.6	See Log	VDC	15.6	15.6	15.6	0	Pass	20.5	Pass	
Test Port Model Number				3202	3202									
Test Port Hardware Version				4	4									
Test Port Firmware Version				4.06	4.06									