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Texas Instruments Enhanced Plastic Products Reliability Report

(Subject To Attached Disclaimers)

Device Type/Device Family: DAC8831MCDREP

Package Type: 14/D Wafer Fabrication Facility: DM5

Assembly/Test Facility: TAI

Compiled: 09/14

Biased Life Test

Test Method: JESD22-A108

Test Condition: 125°C / 1000 hours or equivalent

Sample Size: 542 Rejects: 0

Activation Energy (eV): 0.7 Equivalent Device Hours: 5.21E+07 Failure Rate (FIT)*: 17.7

*Derated to +55°C with a 60% Confidence Level

Package Related Tests

<u>Description</u>	<u>Condition</u>	Referenced Method	Sample Size	Rejects	
Biased Humidity	85°C / 85% / 1000 hours	JESD22-A101	388	0	*
or HAST	or 130°C / 85% / 96 hours	JESD22-A110			
Autoclave	121°C @ 2 atmospheres absolute for 96 hours	JESD22-A102	388	0	*
Temperature Cycle	-65°C to +150°C non-biased for 500 cycles or equivalent	JESD22-A104	89	0	*
High Temp Storage	150°C / 1,000 hours	JESD22-A103-A	286	0	*

^{*} Preconditioning per JEDEC Std. 22, Method A112/A113

Initial Product Qualification

The subject Enhanced Plastic device, device family, and/or package family have passed Texas Instruments product qualification as follows:

<u>Description</u>	Condition	Referenced Method	Sample Size	
Electrical Characterization	TI Data Sheet	N/A	1 lot(s)/30 Units	
Electrostatic Discharge Sensitivity	HBM MM CDM	EIA/JESD22-A114 EIA/JESD22-A115 JESD22-C101	3 Units/voltage N/A N/A	
Latch-up	Per Technology	EIA/JESD78	6/0	
Physical Dimensions	TI Data Sheet	EIA/JESD22- B100	Note 12/Note 12	
Thermal Impedance	Theta-JA on board	EIA/JESD51	Per Pin-Package	
Bias Life Test	125°C / 1000 hours or equivalent	JESD22-A108	231/0	
Biased Humidity or	85°C / 85% / 1000 hours or	JESD22-A101	388/0	*
HAST	130°C / 85% / 96 hours	JESD22-A110		
Autoclave	121°C @ 2 atmospheres absolute for 96 hours	JESD22-A102	539/0	*
Temperature Cycle	-65°C to +150°C non-biased for 500 cycles or equivalent	JESD22-A104	231/0	*
High Temp Storage	150°C / 1,000 hours	JESD22-A103-A	77/0	*
Solder Heat	260°C for 10 seconds	JESD22-B106	15/0	
Solderability	Condition A (steam age for 8 hours)	ANSI/J-STD-002-92	66/0	
Bond Strength	-	ASTM F-459	Note 12/Note 12	
Moisture Sensitivity	Surface Mount Only	J-STD-020-A	12/0	

^{*} Preconditioning per JEDEC Std. 22, Method A112/A113

Suplemental Device Characteristics

Master Die: CDAC8831CAP
Wafer Fab: DM5
Pin/Package Type: 14/D
Fab Process: 50HPA07
Fab Technology: CMOS
Die Revision: C

Assembly Site: TAI
Pin/Package Type: 14/D
Lead Composition: Cu
Lead Finish: NIPDAU
Mount Compound: ABL8290

Passivation: Bond: K1/33.3 UM (1.3 MILS)

Metal 1: Ti/AlCu.5/TiN Mold Compound: G700FG
Metal 2: Ti/AlCu.5/TiN Die Thickness: 15 MILS

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