

# Block Diagram

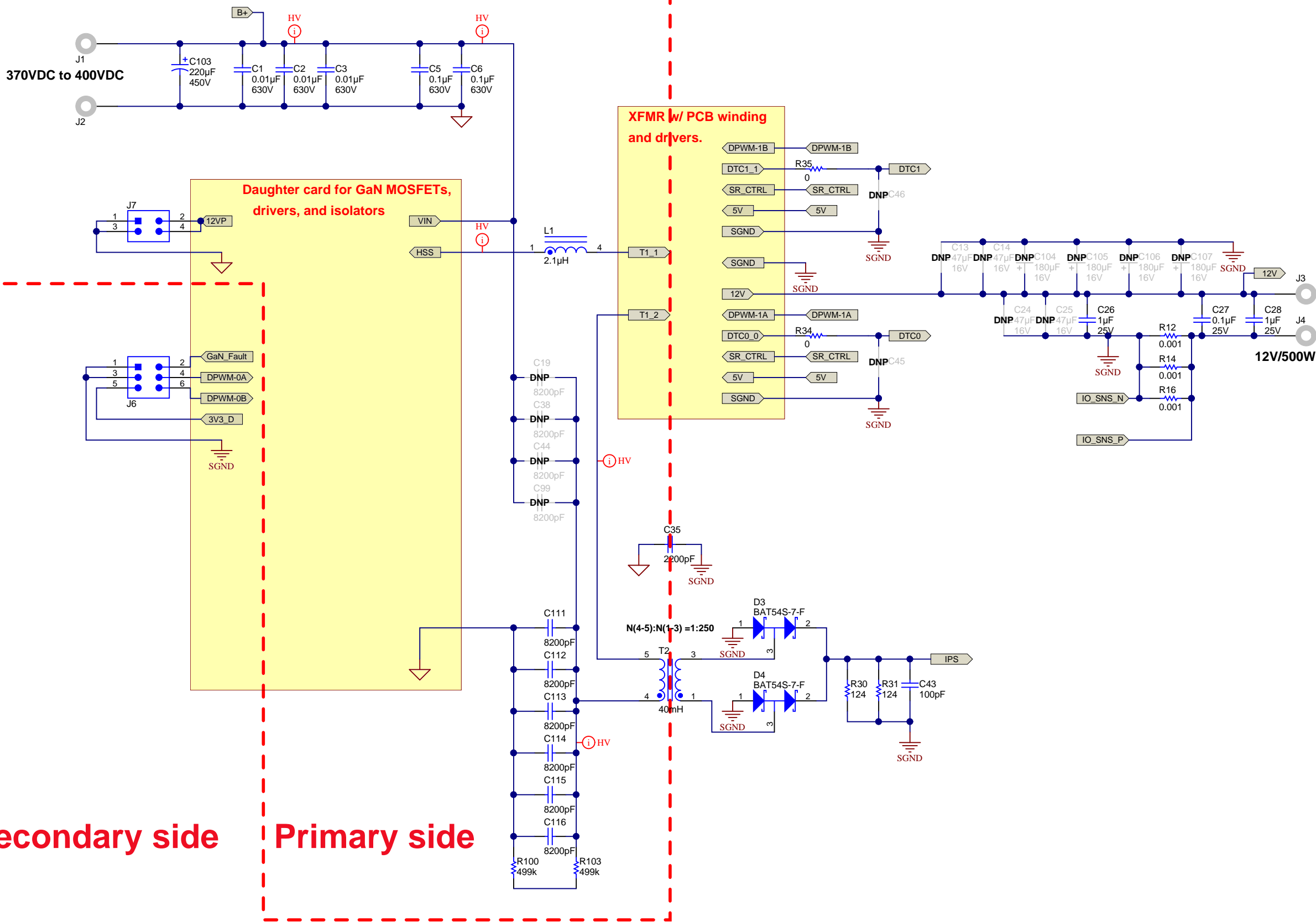
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Orderable: N/A	Designed for: Public Release	Mod. Date: 6/1/2016
TID #: PMP20289	Project Title: 400V to 12V/500W LLC W/ UCD3138A	Rev: A
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 3
Drawn By: Sheng-Yang Yu	File: PMP12047_CoverSheet.SchDoc	Size: B
Engineer: Sheng-Yang Yu	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	© Texas Instruments 2016



Notes:

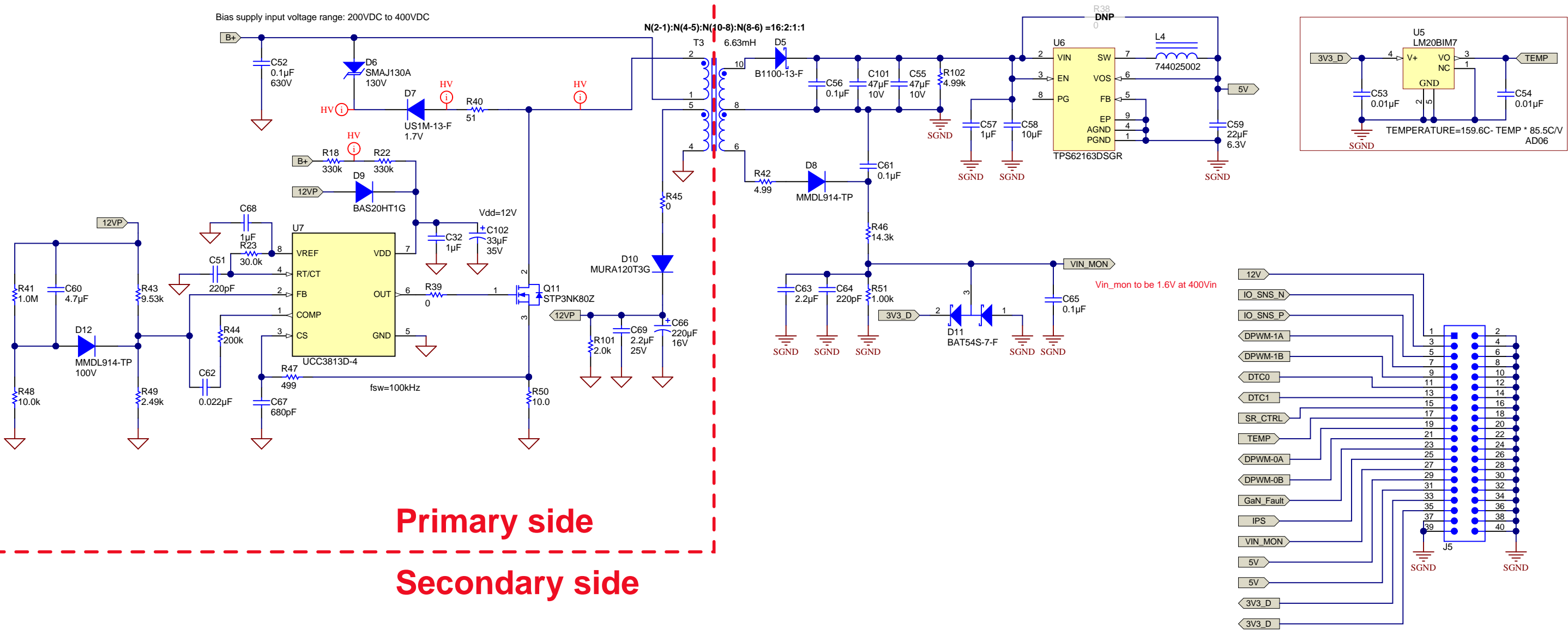
1. Minimum switching frequency: 200kHz
2. Maximum switching frequency: 750kHz
3. IPS over current protection set at 5A.
3. R100, R103, C115 and C116 are add-on capacitors.



Built on PMP12047Rev A PCB

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Built on PMP12047RevA PCB

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