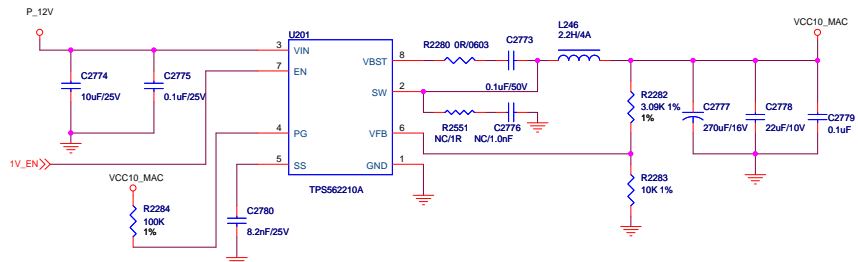
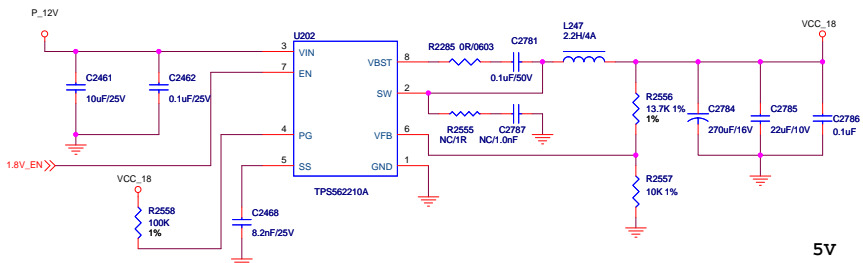


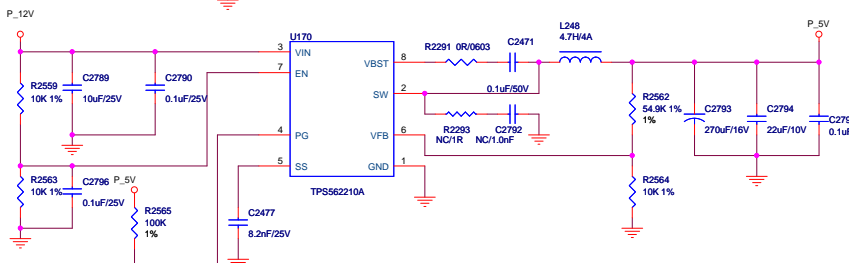
Nephos-1V



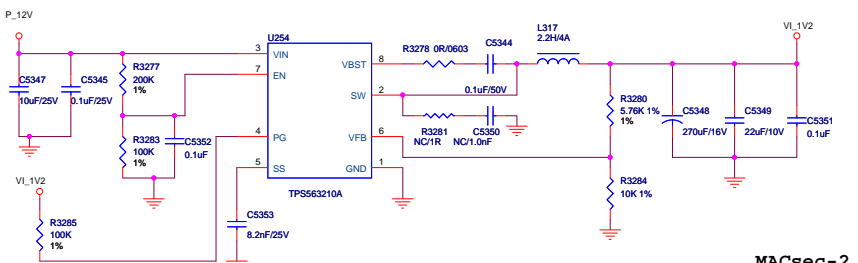
Nephos-1.8V



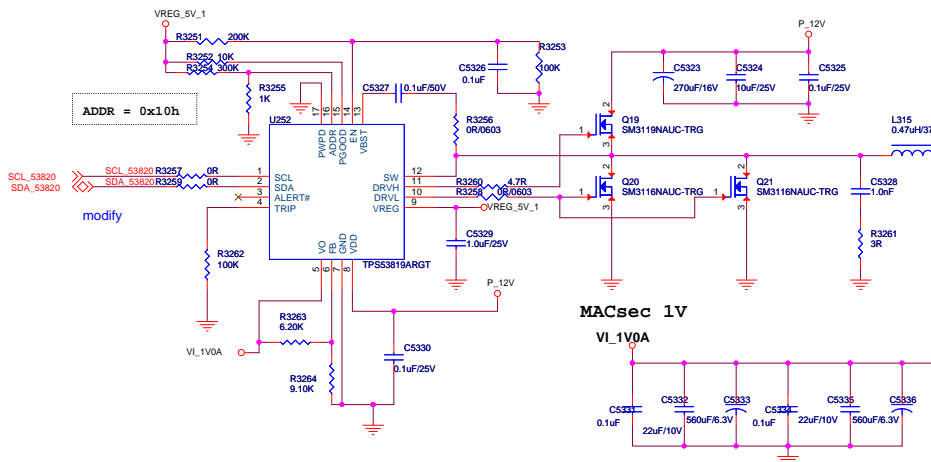
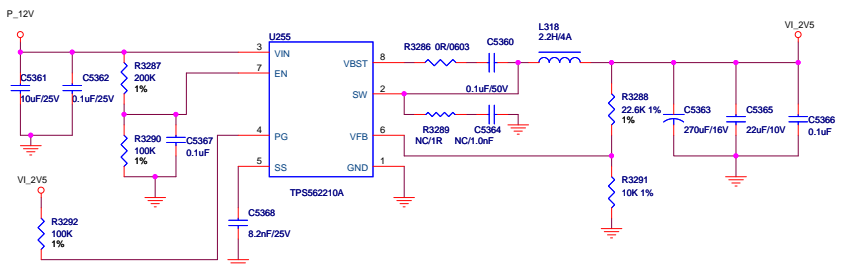
5V



MACsec-1.2V

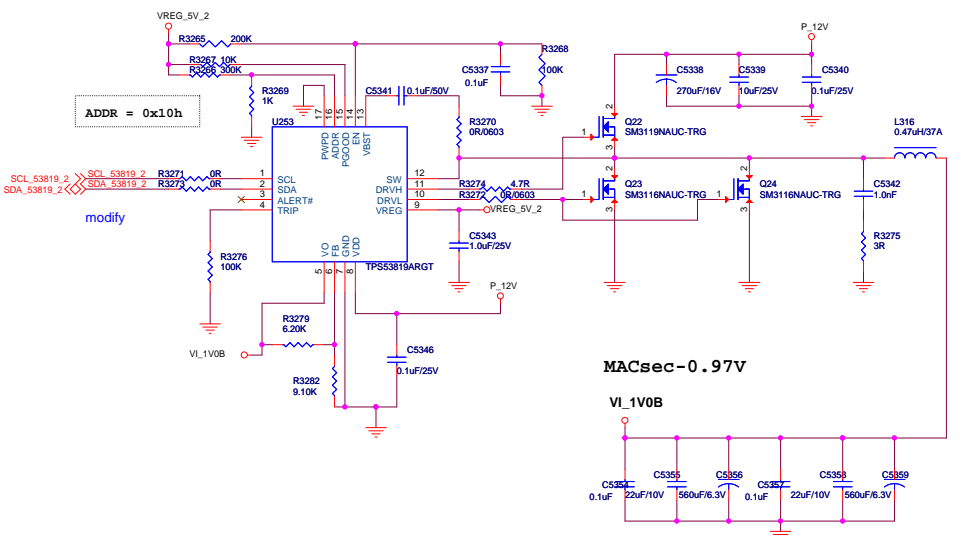


MACsec-2.5V



MACsec 1V

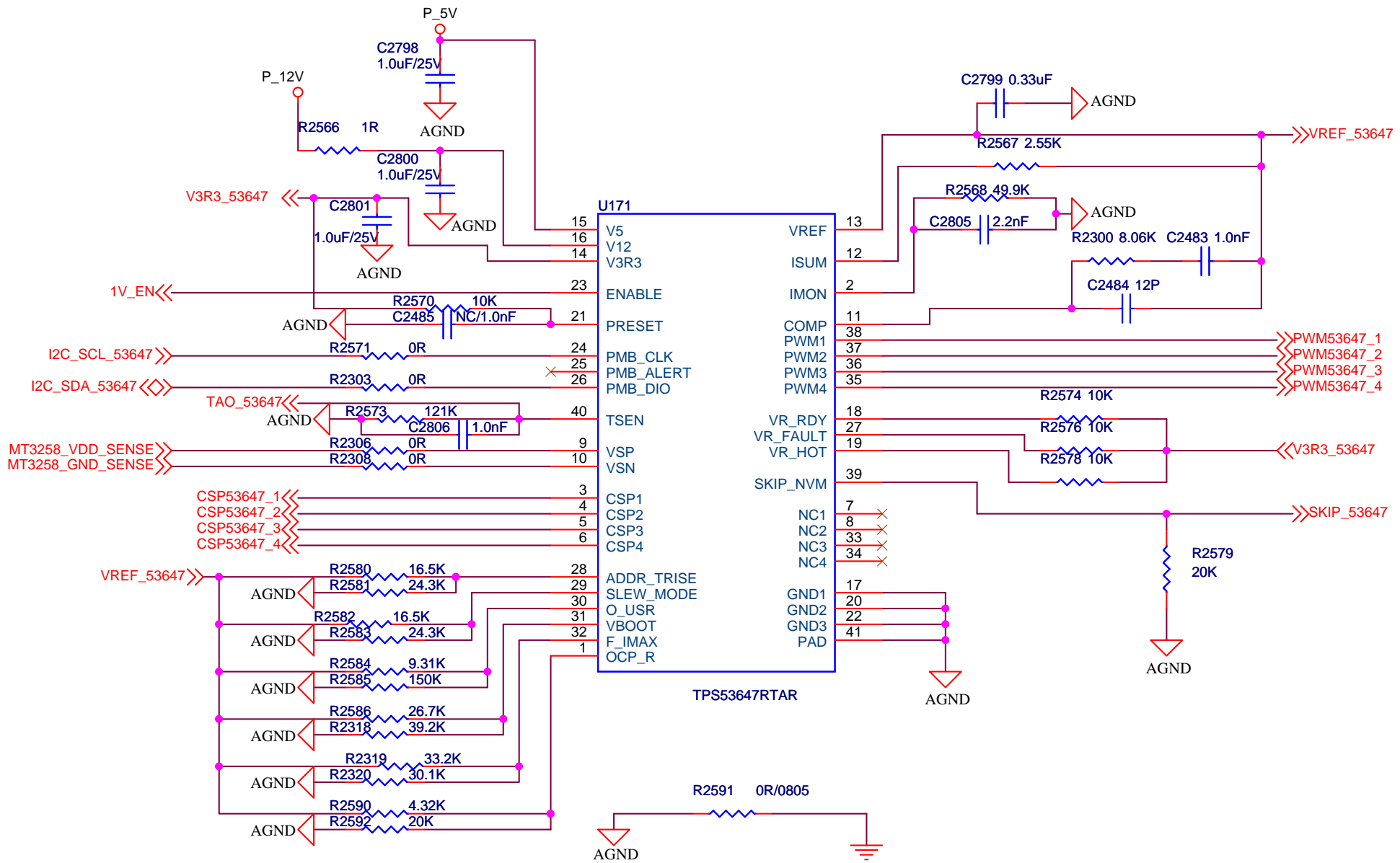
VL\_1V0A



MACsec-0.97V

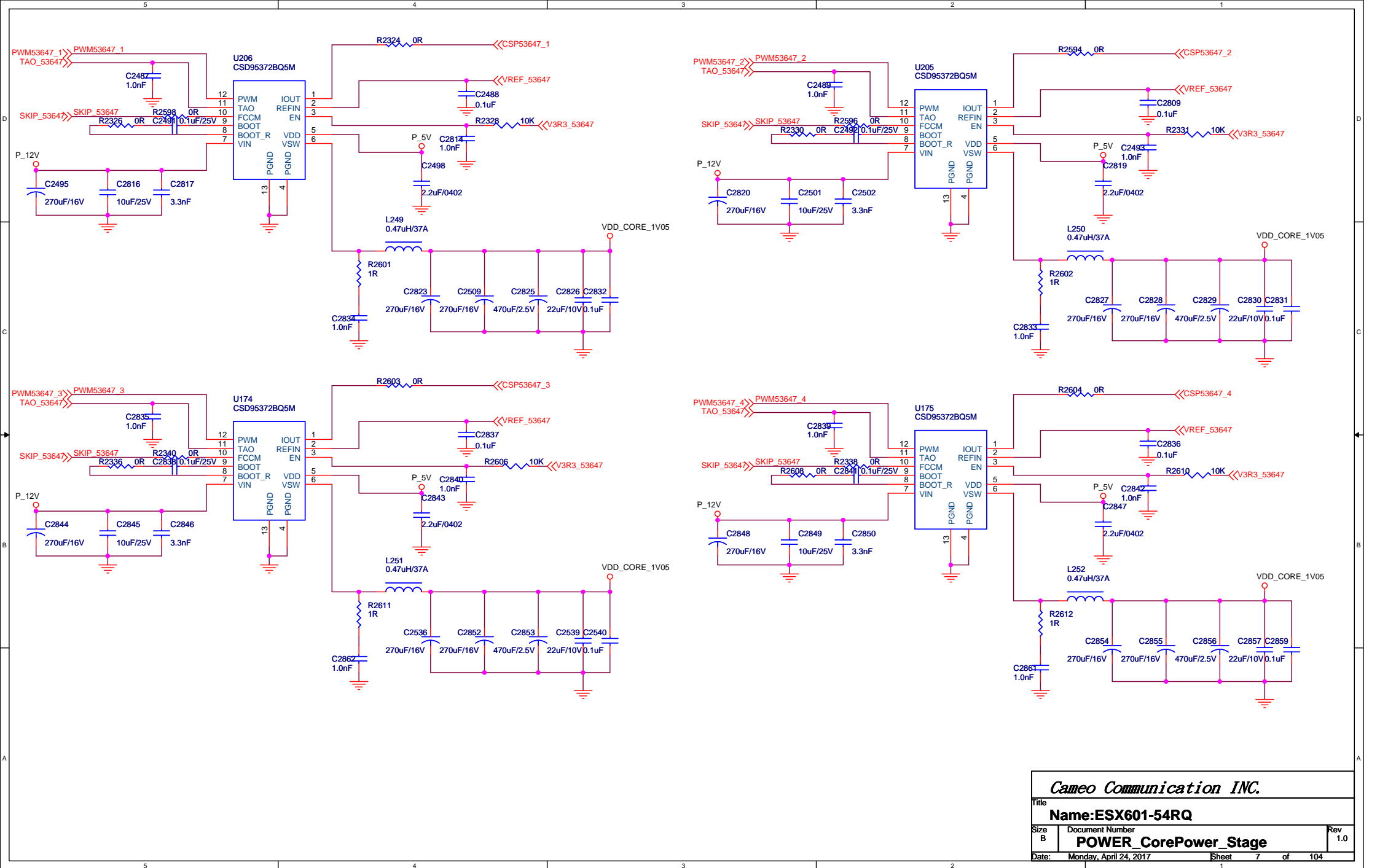
VL\_1V0B

<b>Cameo Communication INC.</b>			
File	<b>Name:ESX601-54RQ</b>		
Size	Document Number	Rev	1.0
C	<b>Other POWER_02</b>		
Date:	Monday, April 24, 2017	Sheet	5 of 104

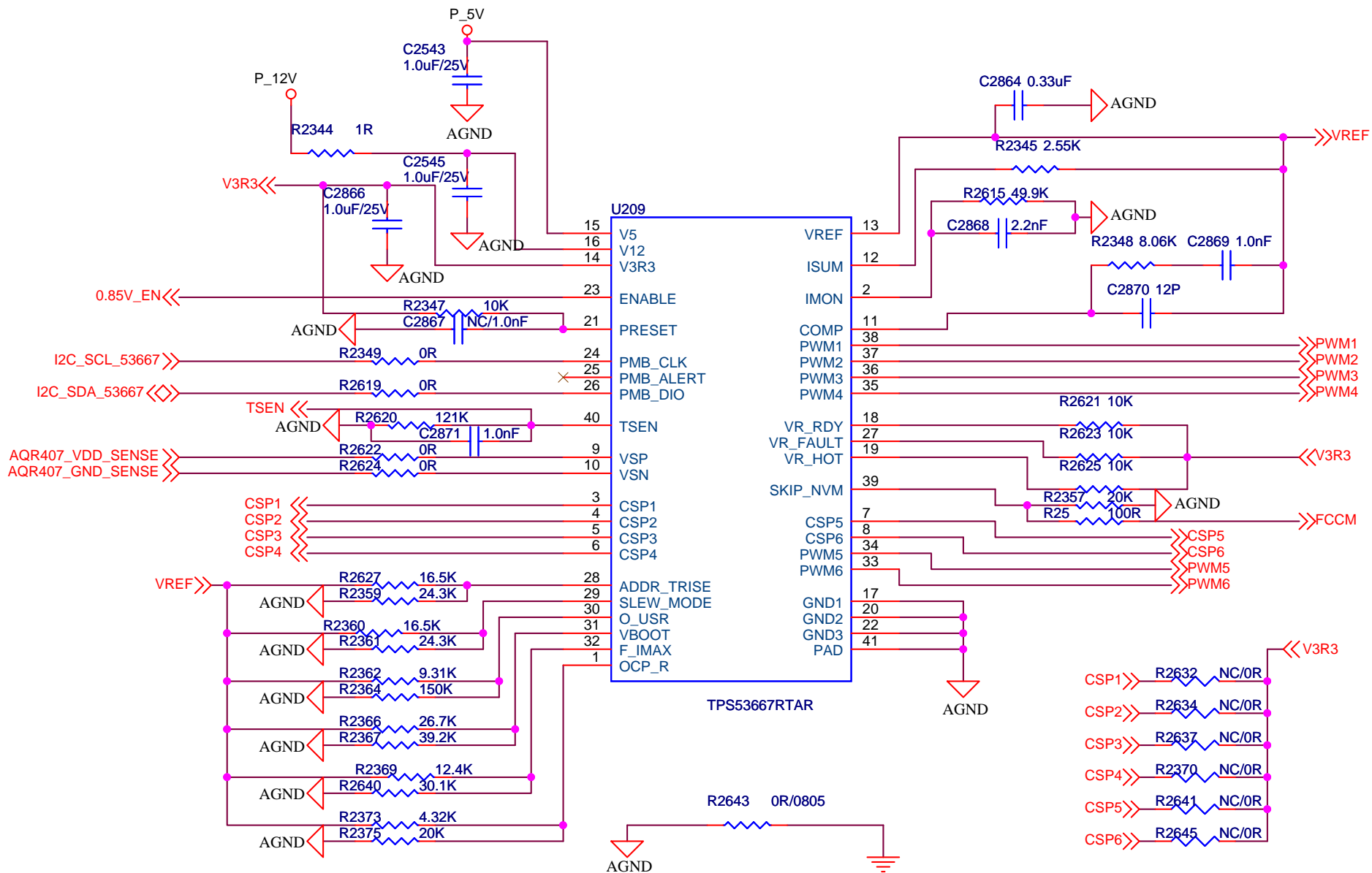


PMBus Address: 1110001 (bin)  
PMBus Address: E1(hex)

<b>Cameo Communication INC.</b>		
Title		
<b>Name: ESX601-54RQ</b>		
Size	Document Number	Rev
A	<b>MAC_POWER_CorePower</b>	1.0
Date:	Monday, April 24, 2017	Sheet 6 of 104



<i>Cameo Communication INC.</i>		
Title		
<b>Name:ESX601-54RQ</b>		
Size	Document Number	Rev
B	<b>POWER CorePower Stage</b>	1.0
Date:	Monday, April 24, 2017	Sheet 7 of 104

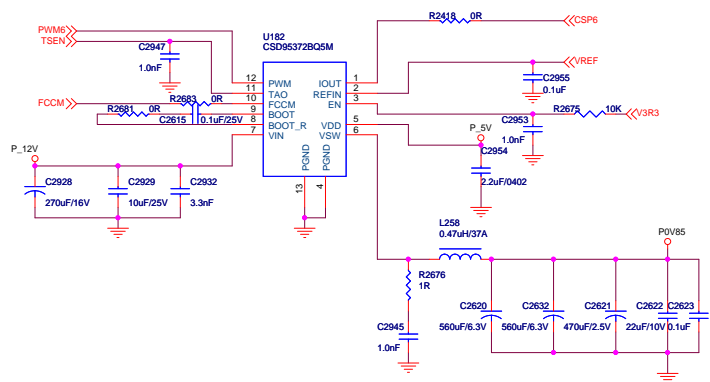
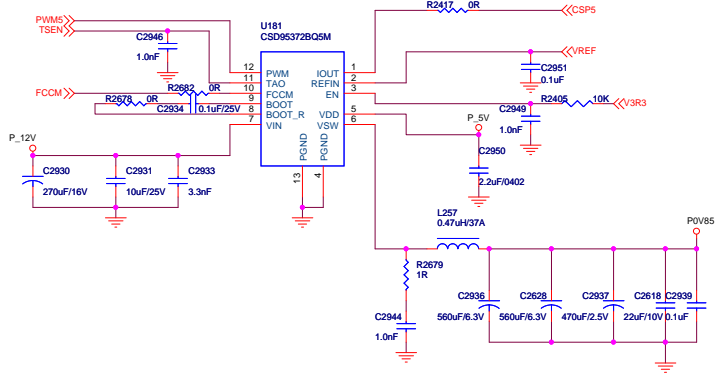
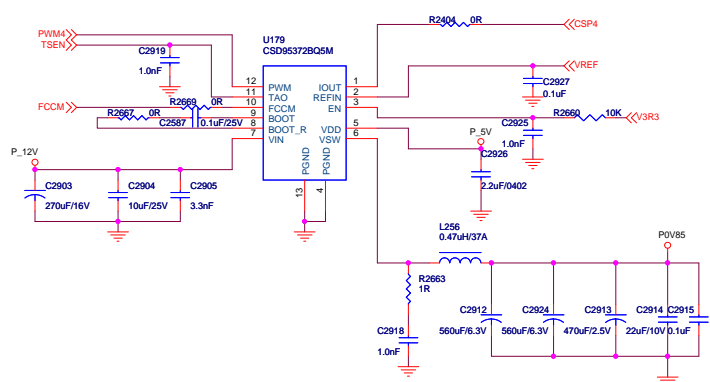
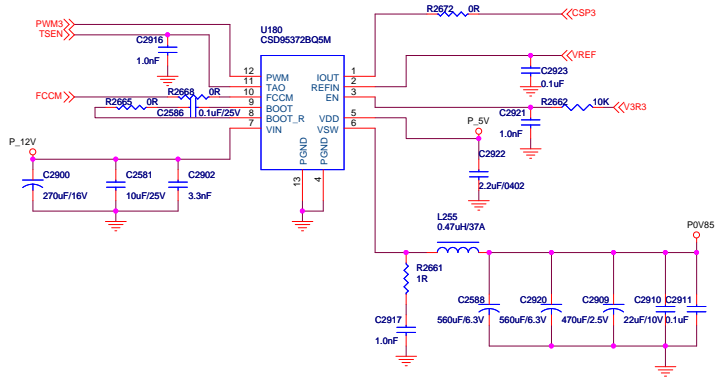
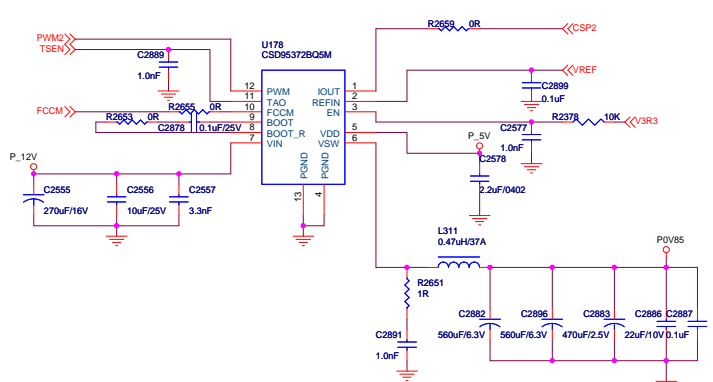
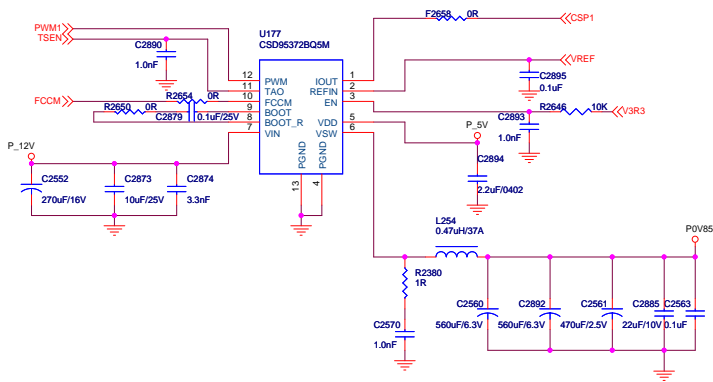


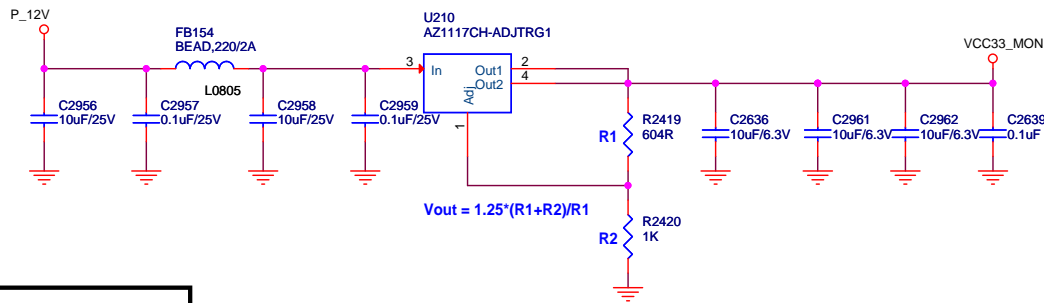
**Need to Check**

PMBus Address: 1110010 (bin)  
PMBus Address: E2(hex)

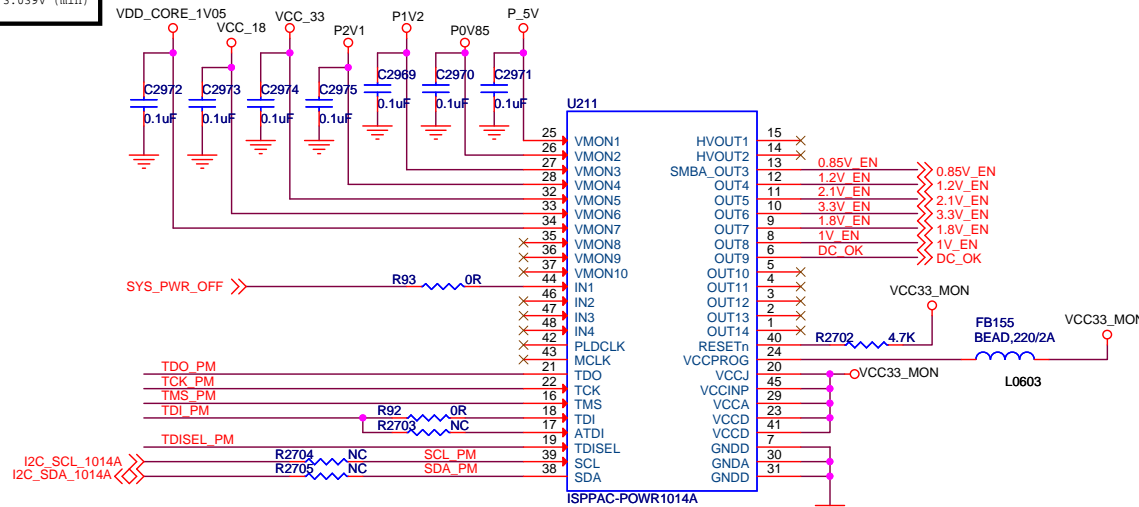
*Cameo Communication INC.*

Title		
<b>Name: ESX601-54RQ</b>		
Size	Document Number	Rev
A	<b>PHY_POWER</b>	1.0
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VMON\_12V, -10% to design  
 under voltage threshold = 1.97V  
 Normal voltage threshold = 3.039V (min)

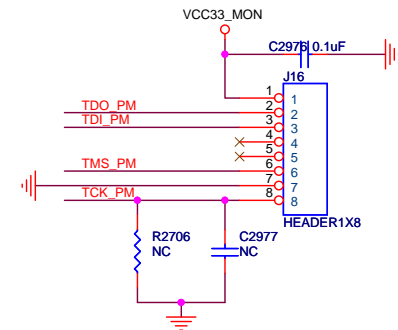
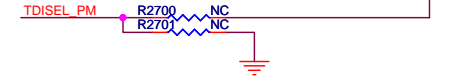
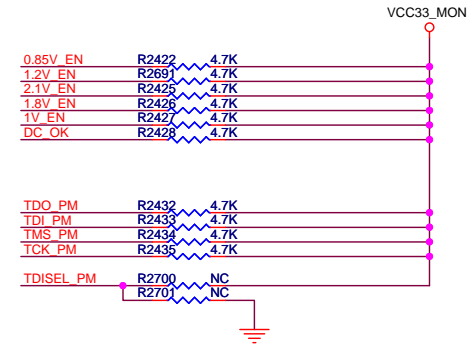
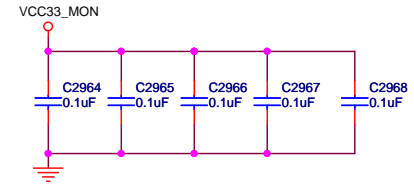


```

undefine ,default ADDR = 0x0h
TDISEL = 1, standard TDI pin is enabled
TDISEL = 0, ATDI is enabled.
  
```

**AQR407 Requirement :**  
 VDD(0.85V) to DVDD1.2(1.2V) to DVDD2.1(2.1V) to DVDD33 (3.3V) <100ms  
 Power sequence :  
 0.85V-->1.2V-->2.1V and 3.3V

**MT3257 Requirement :**  
 DVDD33 (3.3V) to DVDD18 (1.8V) to VDDK(1.05V) and Analog PWR (1V) < 5ms  
 Power sequence :  
 3.3V-->1.8V-->1.05V and 1.0V



<b>Cameo Communication INC.</b>		
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<b>Name:ESX601-54RQ</b>		
Size	Document Number	Rev
B	<b>Power Sequence</b>	1.0
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