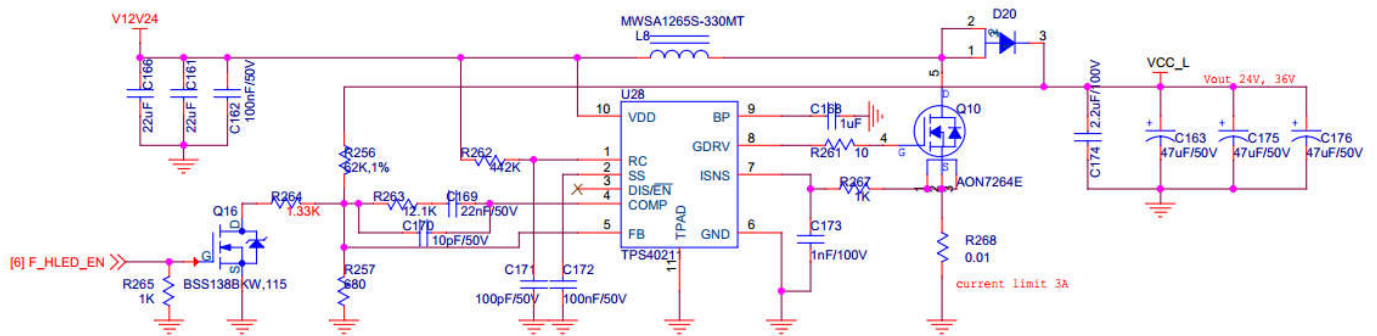
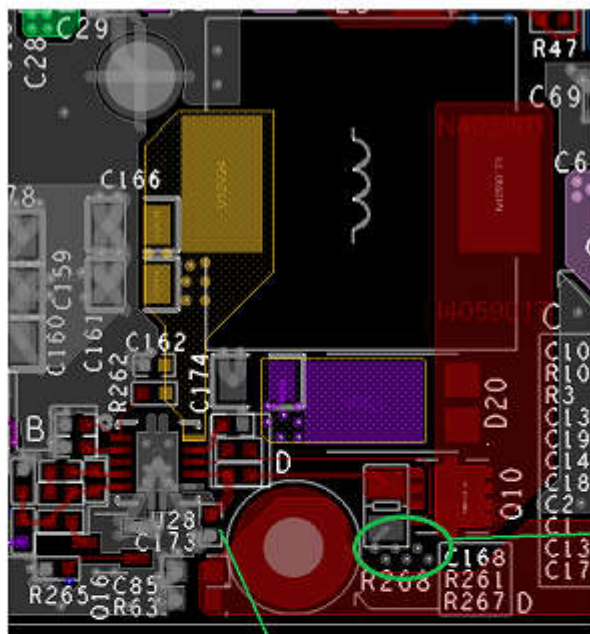


The actual circuit is as follows, and the F_HLED_EN signal has been set to a high level.

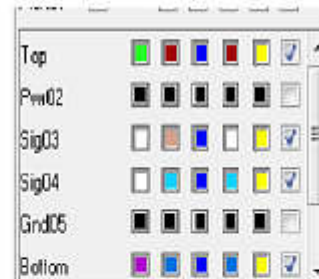


the partial pcb layout of tps40211 as follow:



TOP

stack sequence

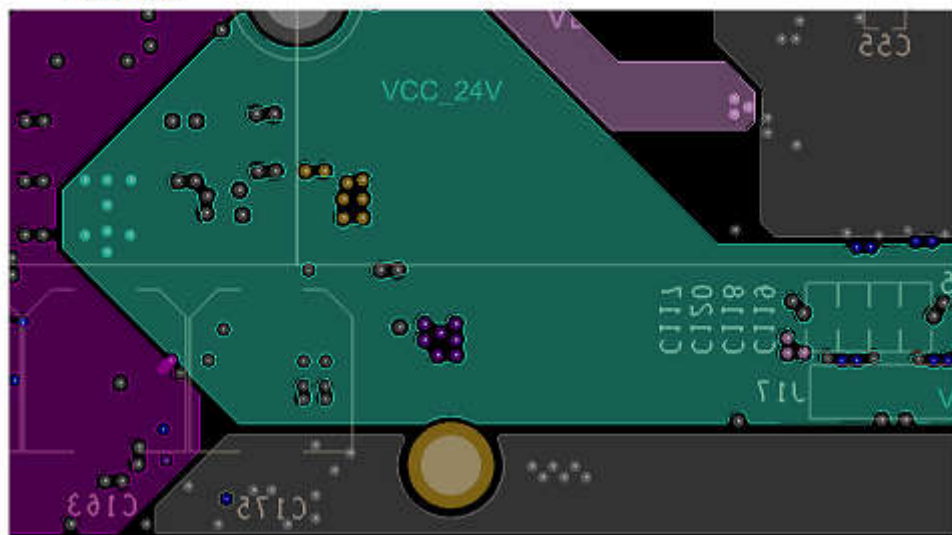


the GND via has been re-soldered

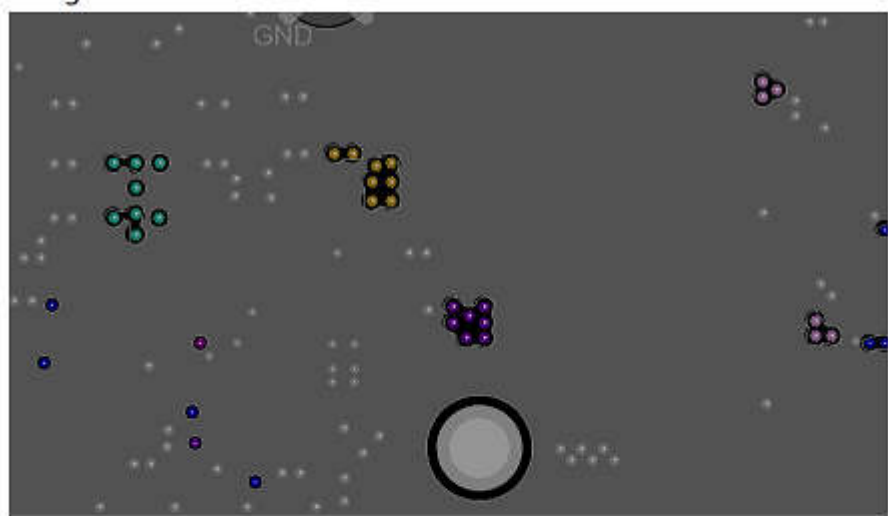
BOTTOM



POWER02



Sig03 and Gnd05



sig04

