

# Type C Redriver.

## <CPU AUX>

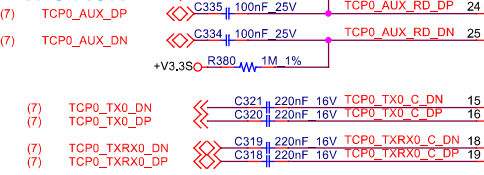
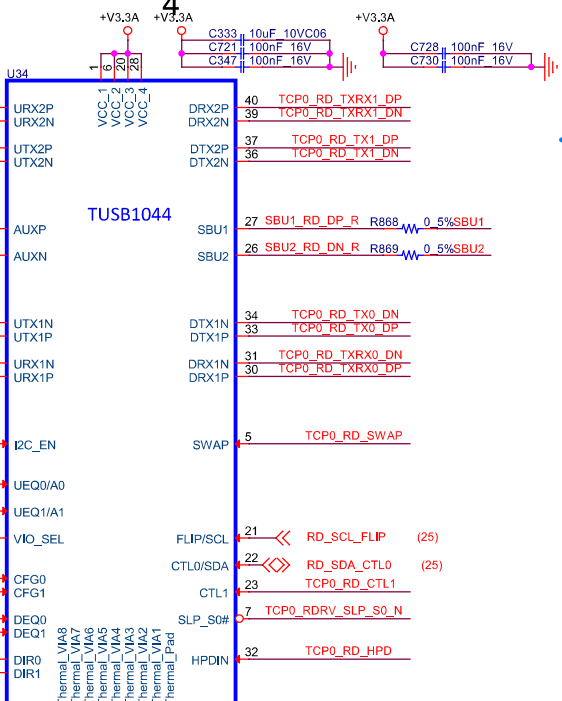
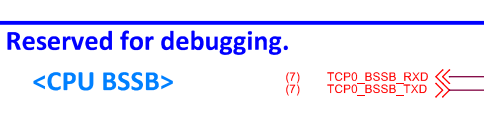
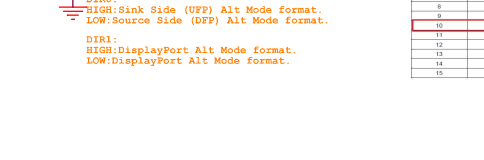
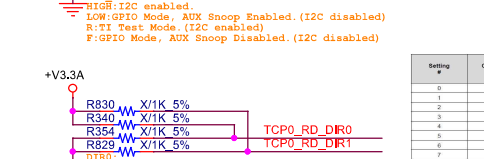
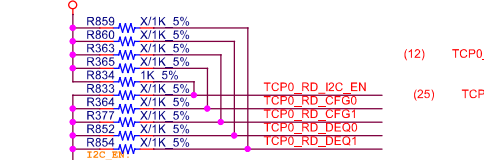
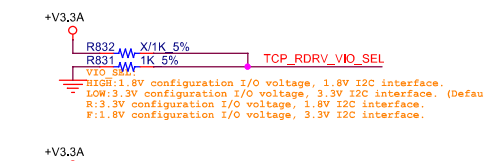
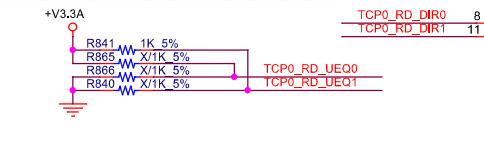


Table 9. I2C Slave Address

US040 Pin Level	US040 Pin Level	Bit 7 (MSB)	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0 (LSB)
D	R	1	0	0	0	0	0	0	0
D	R	1	0	0	0	0	0	0	1
D	R	1	0	0	0	0	0	1	0
D	R	1	0	0	0	0	0	1	1
D	R	1	0	0	0	1	0	0	0
D	R	1	0	0	0	1	0	0	1
D	R	1	0	0	0	1	0	1	0
D	R	1	0	0	0	1	0	1	1
D	R	1	0	0	1	0	0	0	0
D	R	1	0	0	1	0	0	0	1
D	R	1	0	0	1	0	0	1	0
D	R	1	0	0	1	0	0	1	1
D	R	1	0	0	1	1	0	0	0
D	R	1	0	0	1	1	0	0	1
D	R	1	0	0	1	1	0	1	0
D	R	1	0	0	1	1	0	1	1
D	R	1	0	1	0	0	0	0	0
D	R	1	0	1	0	0	0	0	1
D	R	1	0	1	0	0	0	1	0
D	R	1	0	1	0	0	0	1	1
D	R	1	0	1	0	1	0	0	0
D	R	1	0	1	0	1	0	0	1
D	R	1	0	1	0	1	0	1	0
D	R	1	0	1	0	1	0	1	1
D	R	1	0	1	1	0	0	0	0
D	R	1	0	1	1	0	0	0	1
D	R	1	0	1	1	0	0	1	0
D	R	1	0	1	1	0	0	1	1
D	R	1	0	1	1	1	0	0	0
D	R	1	0	1	1	1	0	0	1
D	R	1	0	1	1	1	0	1	0
D	R	1	0	1	1	1	0	1	1
D	R	1	1	0	0	0	0	0	0
D	R	1	1	0	0	0	0	0	1
D	R	1	1	0	0	0	1	0	0
D	R	1	1	0	0	0	1	0	1
D	R	1	1	0	0	1	0	0	0
D	R	1	1	0	0	1	0	0	1
D	R	1	1	0	0	1	0	1	0
D	R	1	1	0	0	1	0	1	1
D	R	1	1	0	1	0	0	0	0
D	R	1	1	0	1	0	0	0	1
D	R	1	1	0	1	0	0	1	0
D	R	1	1	0	1	0	0	1	1
D	R	1	1	0	1	1	0	0	0
D	R	1	1	0	1	1	0	0	1
D	R	1	1	0	1	1	0	1	0
D	R	1	1	0	1	1	0	1	1
D	R	1	1	1	0	0	0	0	0
D	R	1	1	1	0	0	0	0	1
D	R	1	1	1	0	0	1	0	0
D	R	1	1	1	0	0	1	0	1
D	R	1	1	1	0	1	0	0	0
D	R	1	1	1	0	1	0	0	1
D	R	1	1	1	0	1	0	1	0
D	R	1	1	1	0	1	0	1	1
D	R	1	1	1	1	0	0	0	0
D	R	1	1	1	1	0	0	0	1
D	R	1	1	1	1	0	1	0	0
D	R	1	1	1	1	0	1	0	1
D	R	1	1	1	1	1	0	0	0
D	R	1	1	1	1	1	0	0	1
D	R	1	1	1	1	1	0	1	0
D	R	1	1	1	1	1	0	1	1
D	R	1	1	1	1	1	1	0	0
D	R	1	1	1	1	1	1	0	1
D	R	1	1	1	1	1	1	1	0
D	R	1	1	1	1	1	1	1	1



Level	SETTINGS
H	1 kΩ 5% to VCC.
L	1 kΩ 5% to GND.
R	20 kΩ 5% to GND.
F	Float.

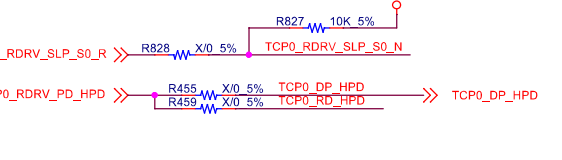


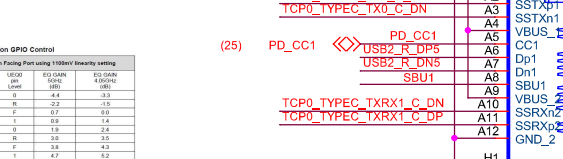
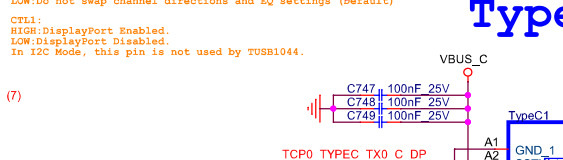
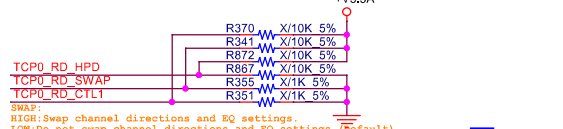
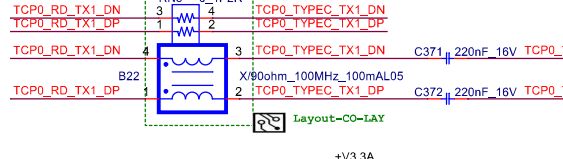
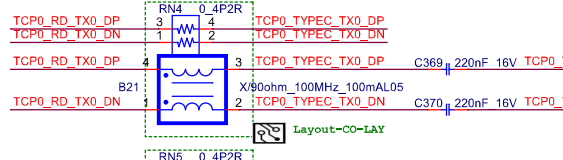
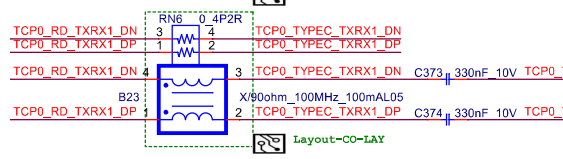
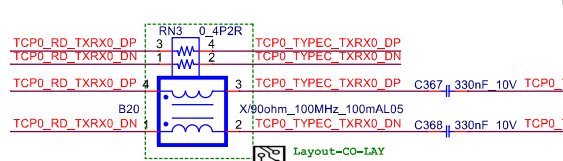
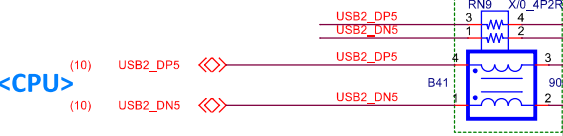
Table 8. VDD Linear Range and DC Gain

Setting #	CPD pin Level	CPD pin Level	Downstream DC Gain (dB)	Downstream VDD Linear Range (mV)	Upstream VDD Linear Range (mV)
0	0	0	1	0	500
1	0	R	0	1	500
2	0	F	0	0	500
3	0	I	1	1	500
4	R	0	0	0	1100
5	R	R	1	0	1100
6	R	F	0	1	1100
7	R	I	2	2	1100
8	F	0	Reserved	Reserved	Reserved
9	F	R	Reserved	Reserved	Reserved
10	F	F	0	0	1300
11	F	I	Reserved	Reserved	Reserved
12	I	0	Reserved	Reserved	Reserved
13	I	R	Reserved	Reserved	Reserved
14	I	F	Reserved	Reserved	Reserved
15	I	I	Reserved	Reserved	Reserved

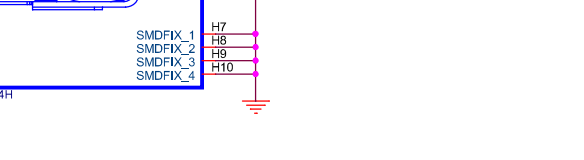
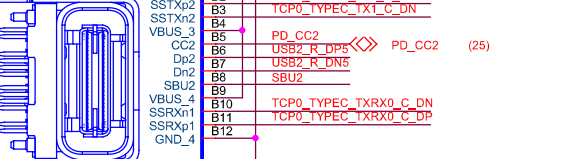
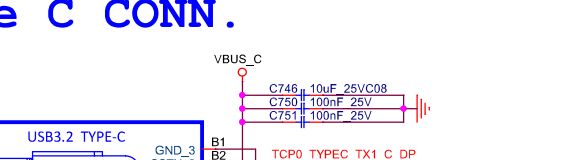
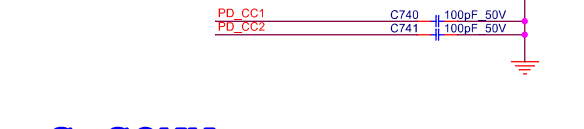
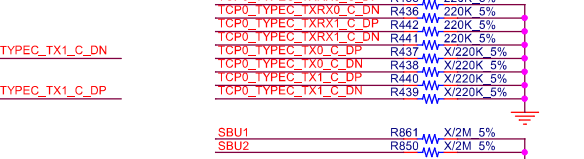
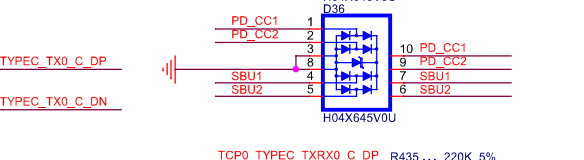
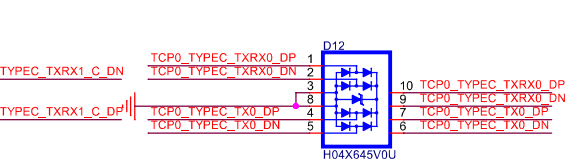
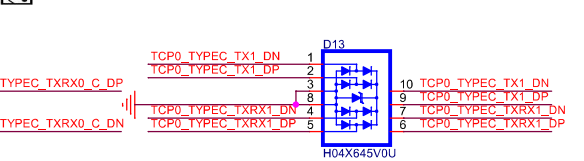
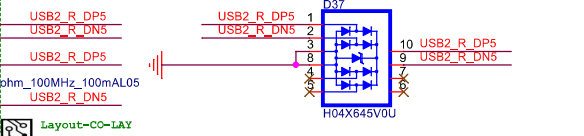
Table 7. TUSB1044 Receiver Equalization GPIO Control

ED	EQ01	EQ02	EQ03	EQ04	EQ05	EQ06	EQ07	EQ08	EQ09	EQ10	EQ11	EQ12	EQ13	EQ14	EQ15
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
4	R	0	0	0	0	0	0	0	0	0	0	0	0	0	0
5	R	0	0	0	0	0	0	0	0	0	0	0	0	0	0
6	R	0	0	0	0	0	0	0	0	0	0	0	0	0	0
7	R	0	0	0	0	0	0	0	0	0	0	0	0	0	0
8	F	0	0	0	0	0	0	0	0	0	0	0	0	0	0
9	F	0	0	0	0	0	0	0	0	0	0	0	0	0	0
10	F	0	0	0	0	0	0	0	0	0	0	0	0	0	0
11	F	0	0	0	0	0	0	0	0	0	0	0	0	0	0
12	I	0	0	0	0	0	0	0	0	0	0	0	0	0	0
13	I	0	0	0	0	0	0	0	0	0	0	0	0	0	0
14	I	0	0	0	0	0	0	0	0	0	0	0	0	0	0
15	I	1	0	0	0	0	0	0	0	0	0	0	0	0	0

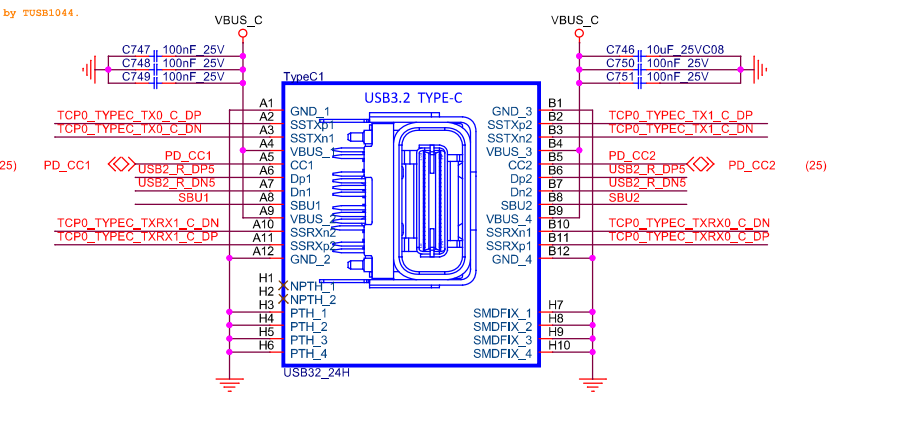
## <CPU>



## 1



## Type C CONN.



Reserved for debugging.

<CPU BSSB>

(7) TCPO\_BSSB\_RXD <- R847 X/0 5% SBU1

(7) TCPO\_BSSB\_TXD <- R839 X/0 5% SBU2

NOTE: Ref CRB 730611 Page 78