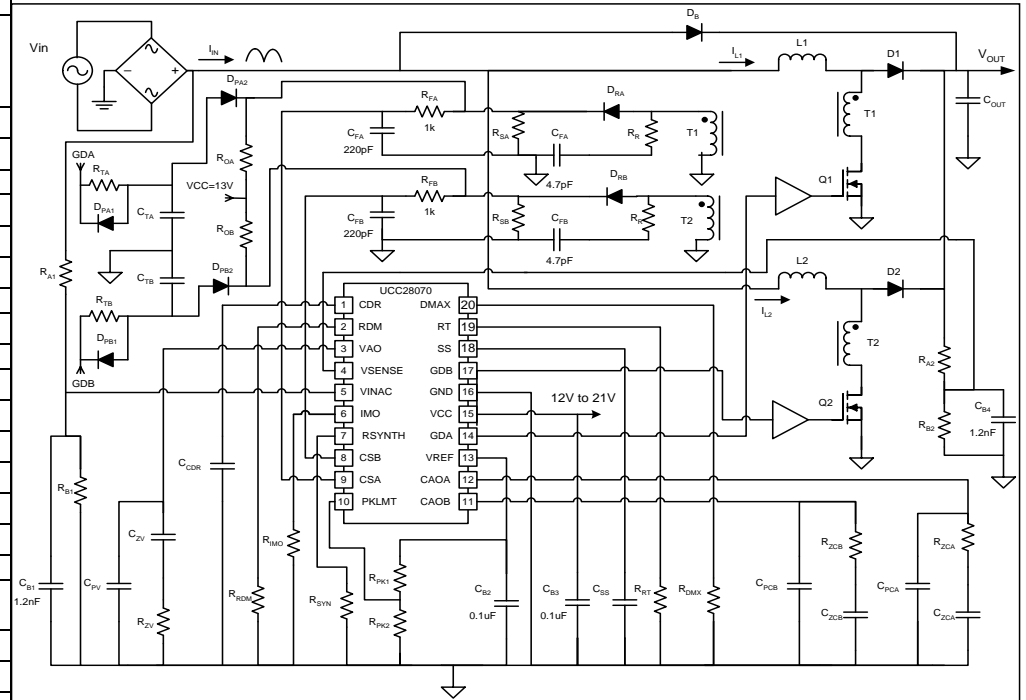
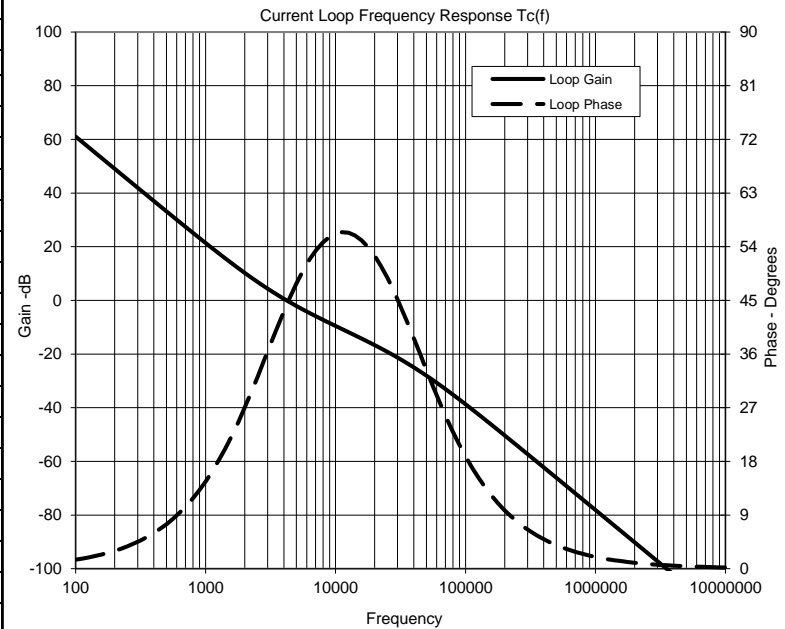
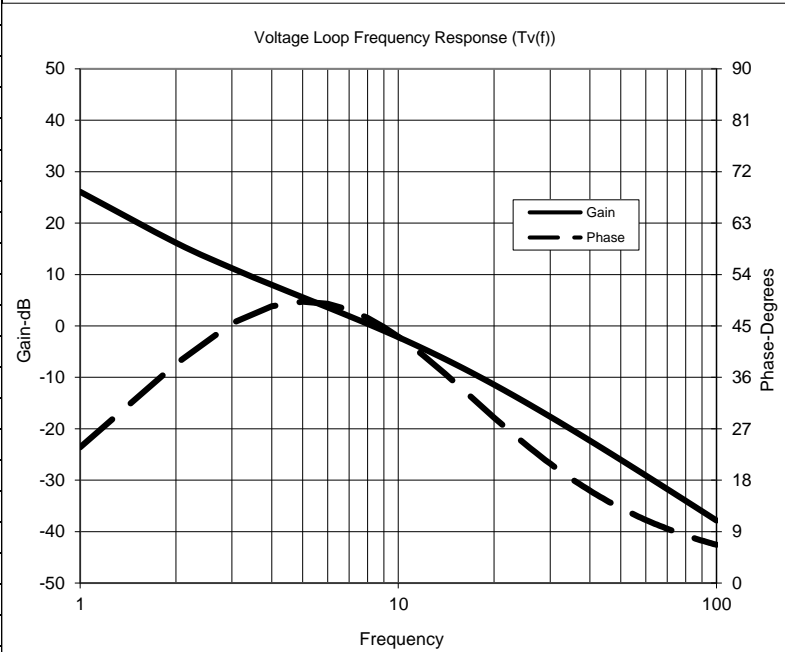


UCC28070 Controller Setup Tool		11/20/2018	
<b>Preliminary</b>			
<b>Notes:</b>			
This design tool is designed to work with the UCC28070 Application Note SLUA479			
This design tool is optimized for a Universal Input and can be used for designs where the input voltage is anywhere between 85V to 265V RMS.			
Please enter design parameters into the		shaded	cells;
Calculated results will be in GRAY			
Design Parameters:	Variable Names		Units
Minimum RMS Input Voltage	V <sub>IN_MIN</sub>	185	V
Maximum RMS Input Voltage	V <sub>IN_MAX</sub>	265	V
Minimum Line Frequency	f <sub>LINE</sub>	47	Hz
Maximum Line Frequency		63	Hz
Maximum Output Power	P <sub>OUT</sub>	3000	W
Full Load Efficiency (Must be less than 0.99)	η	0.93	
Switching Frequency	f <sub>S</sub>	3.75E+04	Hz
Output Voltage	V <sub>OUT</sub>	400	V
Maximum Duty Cycle	D <sub>MAX</sub>	0.90	
Soft Start Time	t <sub>SS</sub>	0.50	s
Optional Frequency Dither Magnitude (Total Δfs)	f <sub>DM</sub>	3.75E+03	Hz
Optional Frequency Dither Rate	f <sub>DR</sub>	1.00E+03	Hz
VCC	VCC	12	V
<b>Component Selection, Trip Points and Calculated Values from the UCC28070 Design Example</b>			
Duty Cycle at the Peak the Peak of Low Line Input	D <sub>PLL</sub>	0.35	
Inductor Ripple Current Cancellation at the Peak of Low Line	K(D <sub>PLL</sub> )	0.47	
Inductor Ripple Current	ΔI <sub>L1</sub>	15.70	
Calculated Boost Inductors L <sub>1_MIN</sub> , L <sub>2_MIN</sub>	L <sub>1_MIN</sub> , L <sub>2_MIN</sub>	1.54E-04	H
Enter the Lowest Inductance Value of the Real Inductor	L <sub>1_MIN</sub> , L <sub>2_MIN</sub>	2.00E-04	H
Enter the Highest Inductance Value of the Real Inductor	L <sub>1_MAX</sub> , L <sub>2_MAX</sub>	2.70E-04	H
Average Inductance Value	L <sub>1_AVG</sub> , L <sub>2_AVG</sub>	2.35E-04	H
Inductor RMS Current	I <sub>L1_RMS</sub> , I <sub>L2_RMS</sub>	9.310	A
Output Capacitance Calculated Based on Holdup Time	C <sub>OUT</sub>	1.82E-03	F
Output Capacitance Selected	C <sub>OUT</sub>	8.20E-04	F
Output Ripple Voltage	V <sub>RIPPLE</sub>	33.3	V
Low Frequency Output Capacitor RMS Current	I <sub>COU_LF</sub>	5.702	A
High Frequency Output Capacitor RMS Current	I <sub>COU_HF</sub>	7.847	A
Peak Diode and FET Current	I <sub>PEAK</sub>	24.217	A
FET RMS Current (Q1 and Q2)	I <sub>DS</sub>	5.815	A
Diode Average Current (D1 and D2)	I <sub>D</sub>	3.750	A
Calculated Current Sense Transformer Turns Ratio	N <sub>CT</sub> =N <sub>S</sub> /N <sub>P</sub>	242	
Enter Current Sense Transformer Turns Ratio	N <sub>CT</sub> =N <sub>S</sub> /N <sub>P</sub>	200	
Minimum Magnetizing Inductance of the Current Sense Transform	L <sub>M</sub>	1.41E-02	H



Voltage Loop and Current Loop Axis Can be Adjusted Based on Individual Need

Select Current Sense Peak Voltage	$V_S$	3.00	V
Calculated Current Sense Resistor	$R_{SA} = R_{SB}$	22.3	ohm
Select Standard Current Sense Resistor	$R_{SA} = R_{SB}$	23	ohm
Calculated Reset Resistor	$R_R$	2.E+02	ohm
Select a Standard Value	$R_R$	3.00E+02	ohm
Calculated Maximum DR Reverse Voltage	$V_R$	36	V
Current Sense Offset Desired	$V_{OFF}$	0.12	V
Program Offset Bias Current to be added to $R_S$	$R_{OA}=R_{OB}$	2.28E+03	ohm
Select a Standard Value	$R_{OA}=R_{OB}$	2.37E+03	ohm
Program Current Sense PWM Ramp Resistor	$R_{TA}=R_{TB}$	1.59E+03	ohm
Select Standard Values	$R_{TA}=R_{TB}$	1.60E+03	ohm
Program Current Sense PWM Ramp Timing Capacitor	$C_{TA}=C_{TB}$	3.86E-07	F
Select Standard Values	$C_{TA}=C_{TB}$	3.80E-07	F
Select High Side Resistor on Peak Current Limit Divider	$R_{PK1}$	4.75E+03	ohm
Calculated Low Side Resistor on Peak Current Limit Divider	$R_{PK2}$	4.75E+03	ohm
Select Low Side Resistor on Peak Current Limit Divider	$R_{PK2}$	6.20E+03	ohm
Calculated Timing Resistor	$R_{RT}$	2.00E+05	ohm
Select Timing Resistor	$R_{RT}$	2.00E+05	ohm
Calculated Programmable Duty Cycle Limit Resistor	$R_{DMX}$	1.60E+05	ohm
Select Programmable Duty Cycle Limit Resistor	$R_{DMX}$	1.80E+05	ohm
Select High Side Resistor for VSENSE Voltage Divider	$R_A$	3.00E+06	ohm
Calculated Low Side Resistor on VSENSE voltage Divider	$R_B$	2.27E+04	ohm
Select Low Side Resistor on VSENSE voltage Divider	$R_B$	2.37E+04	ohm
Calculated Nominal Over Voltage Trip Point	$V_{OVP}$	406	V
Voltage Divider Gain	H	7.84E-03	
Voltage Amplifier Output Impedance at double $f_{LINE}$	$Z_O$	5.25E+03	ohm
Calculated Pole Capacitance for the Voltage Loop	$C_{PV}$	3.22E-07	F
Select a Standard Value	$C_{PV}$	3.20E-07	F
Calculated Voltage Loop Crossover Frequency	$f_{VC}$	11.5	Hz
Calculated Voltage Loop Zero Compensation Resistor	$R_{ZV}$	4.34E+04	ohm
Select a Standard Value	$R_{ZV}$	4.22E+04	ohm
Calculated Voltage Loop Zero Compensation Capacitor	$C_{ZV}$	3.29E-06	F
Select a Standard Value	$C_{ZV}$	2.00E-06	F
Calculated Current Synthesis Programmable Resistor	$R_{SYN}$	1.84E+05	ohm
Select a Standard Value	$R_{SYN}$	1.78E+05	ohm
Voltage Calculation for Selecting Multiplier Resistor	$V_1$	68.563	V
Voltage Calculation for Selecting Multiplier Resistor	$V_2$	4.208	V
Multiplier Resistor	$R_{IMO}$	3.24E+04	ohm
Select a Standard Value	$R_{IMO}$	3.16E+04	ohm
Current Loop Power Stage Gain at Loop Crossover	$G_{PSC}$	2.077	
Current Loop Zero Resistor	$R_{ZC1}=R_{ZC2}$	4.81E+03	ohm
Select a Standard Value	$R_{ZC1}=R_{ZC2}$	4.60E+03	ohm
Current Loop Zero Capacitor	$C_{ZC1}=C_{ZC2}$	8.81E-09	F



Select a Standard Value	$C_{ZC1}=C_{ZC2}$	1.00E-08	F
Current Loop Pole Capacitor	$C_{PC1}=C_{PC2}$	1.76E-09	F
Select a Standard Value	$C_{PC1}=C_{PC2}$	1.00E-09	F
Calculated Soft Start Capacitor (Be sure $C_{SS} > \text{or} = C_{ZV}$ )	$C_{SS}$	2.22222E-06	F
Select a Standard Value (Be sure $C_{SS} > \text{or} = C_{ZV}$ )	$C_{SS}$	2.20E-06	F
Program Dither Magnitude Resistor	$R_{RDM}$	2.50E+05	ohm
Select a Standard Value	$R_{RDM}$	2.50E+05	ohm
Program Dither Rate Capacitor	$C_{CDR}$	1.67E-08	F
Select a Standard Value	$C_{CDR}$	1.50E-08	F
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