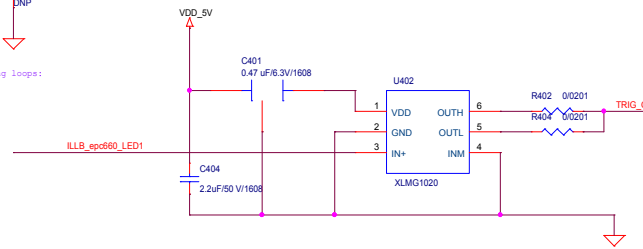


Layout:
Minimize the physical length of the wires in the following loops:
1. U13, C1-U15, G + U15, S-C40 + C40-U13, D1
2. C16-U13, G + U13, C-U15, G + U15, S-C40 + C40-C36
Try to not have vias in the paths.



| | | | |
|-------|----------------------------|---------------|--------|
| Title | | LED_CAPE_REV2 | |
| Size | Document Number | Rev | 2.0c |
| C | <Doc> | | |
| Date | Friday, September 28, 2018 | Sheet | 1 of 1 |