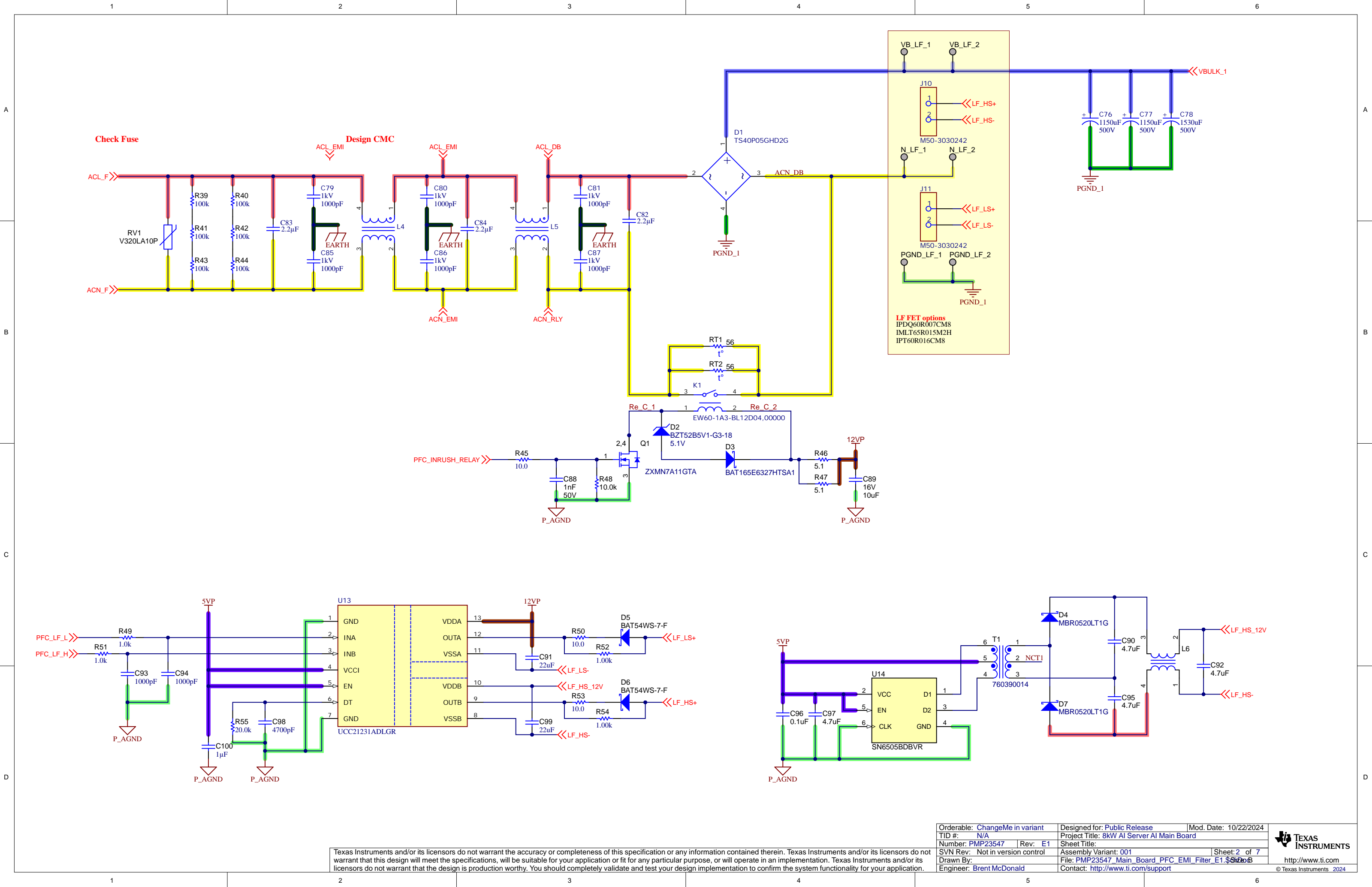


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TID #: N/A	Project Title: 8kW AI Server AI Main Board	
Number: PMP23547	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 7
Drawn By: Brent McDonald	File: PMP23547_Main_Board_PFC_Control_E1.Sch	Size: B
Engineer: Brent McDonald	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



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TID #: N/A	Project Title: 8kW AI Server AI Main Board	
Number: PMP23547	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 7
Drawn By:	File: PMP23547_Main_Board_PFC_EMI_Filter_E1.Sch	Size: 8
Engineer: Brent McDonald	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

A

B

C

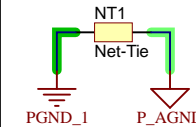
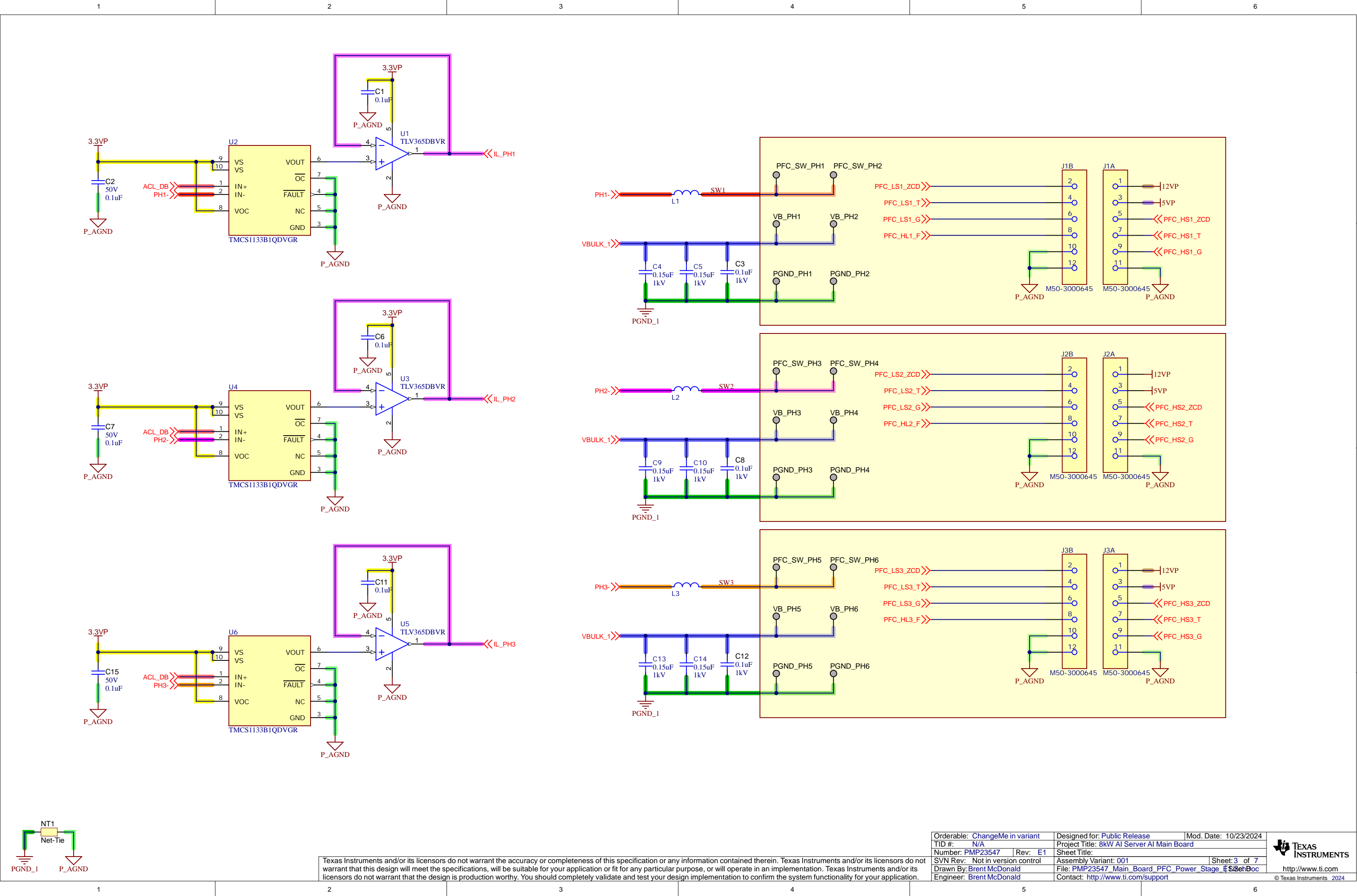
D

A

B

C

D

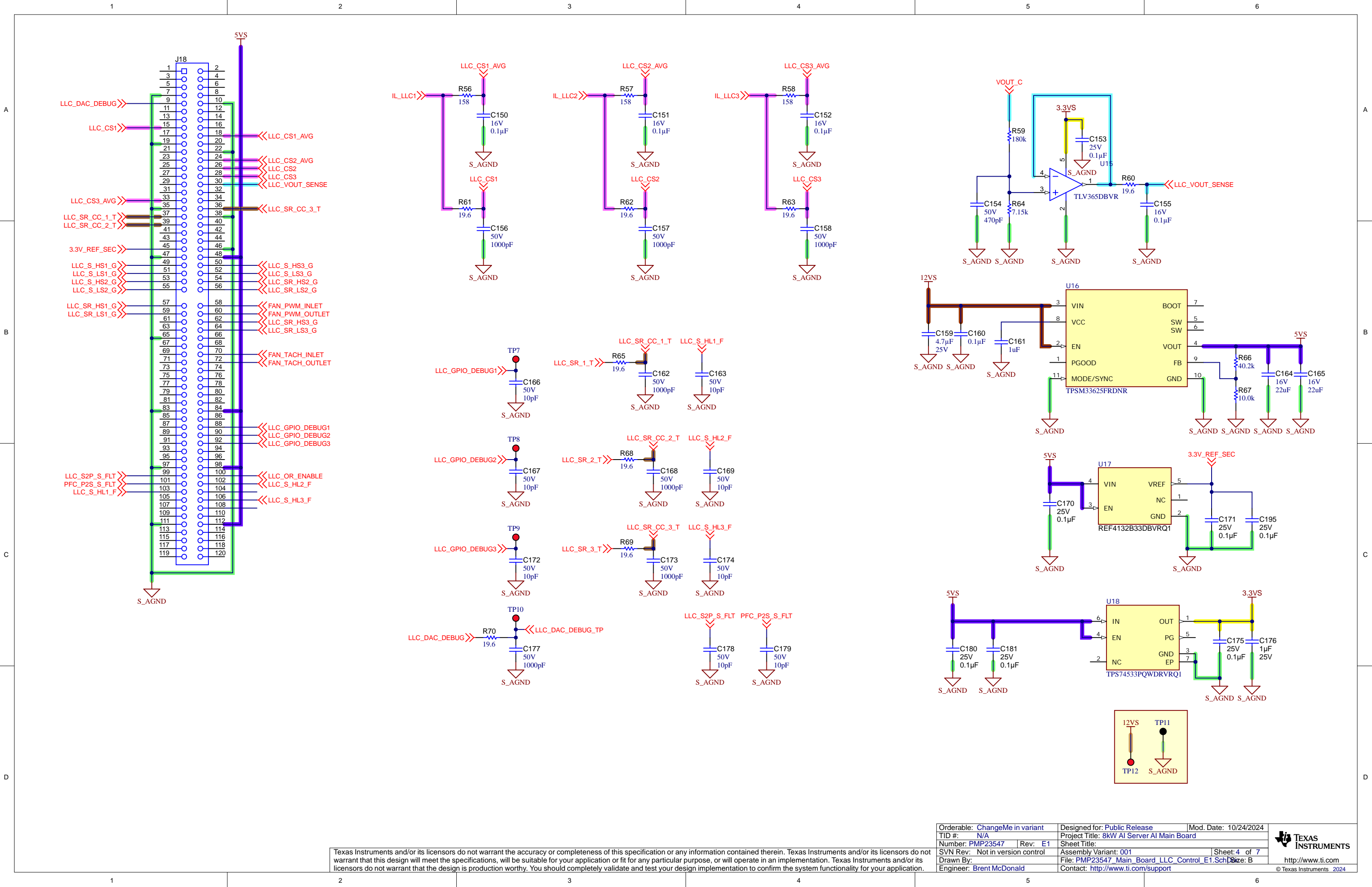


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TID #: N/A	Project Title: 8kW AI Server AI Main Board	
Number: PMP23547	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 7
Drawn By: Brent McDonald	File: PMP23547_Main_Board_PFC_Power_Stage_E1_SchDoc	
Engineer: Brent McDonald	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

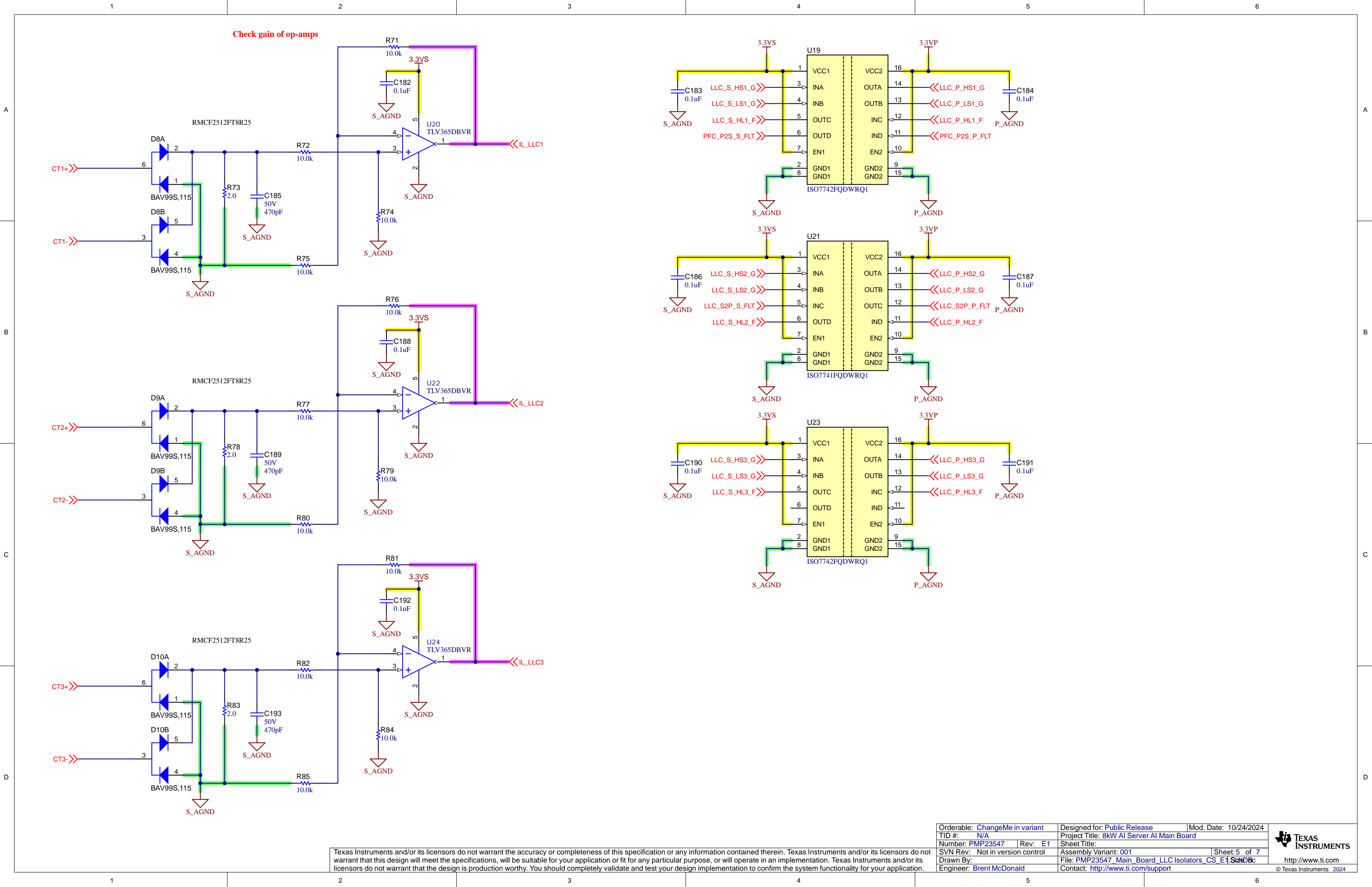
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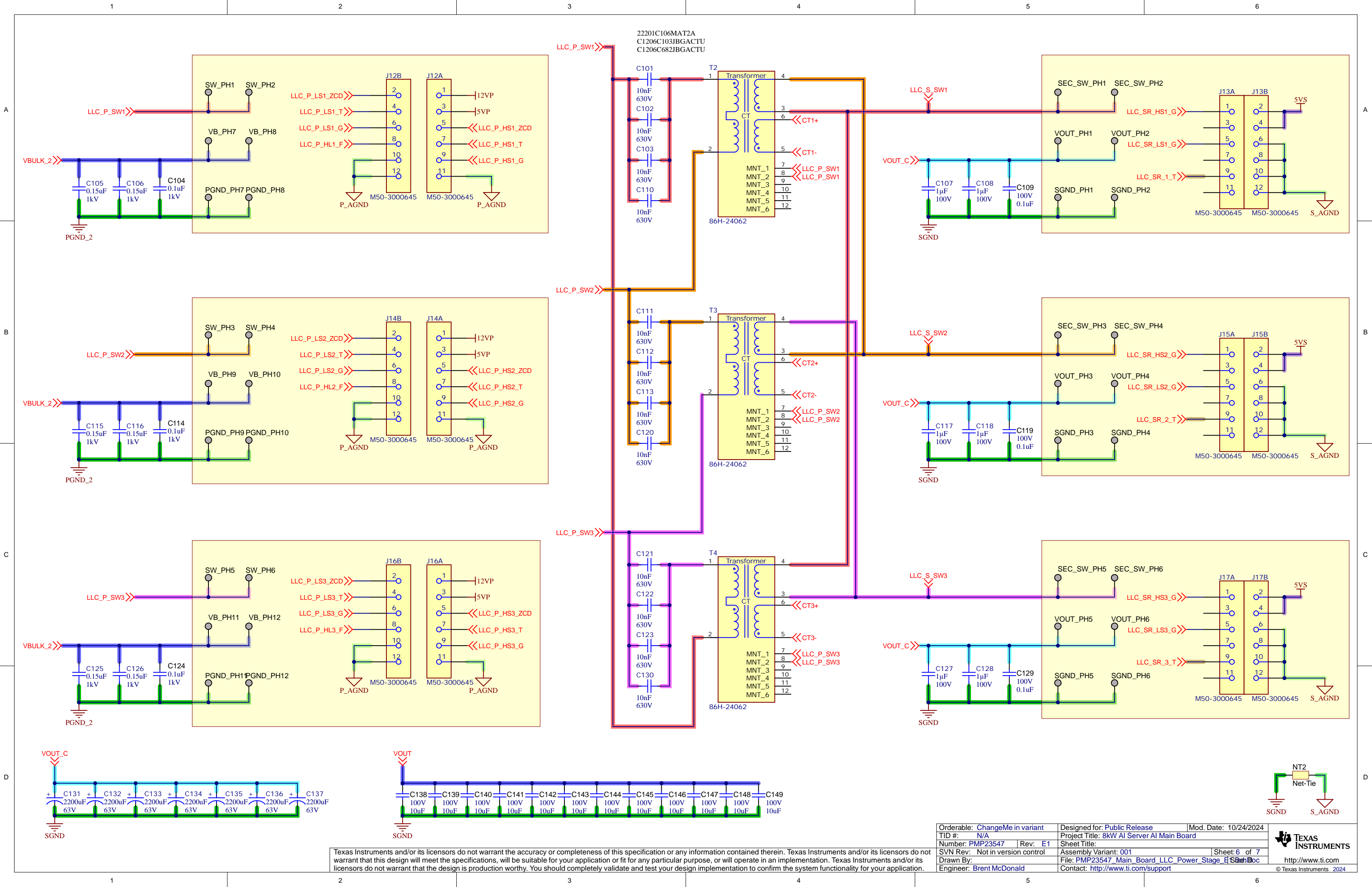
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TID #: N/A	Project Title: <a href="#">8kW AI Server AI Main Board</a>	
Number: <a href="#">PMP23547</a>	Rev: <a href="#">E1</a>	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">4</a> of <a href="#">7</a>
Drawn By:	File: <a href="#">PMP23547_Main_Board_LLC_Control_E1.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Brent McDonald</a>	Contact: <a href="#">http://www.ti.com/support</a>	



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Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 10/24/2024
TID #: N/A	Project Title: <a href="#">8kW AI Server AI Main Board</a>	
Number: <a href="#">PMP23547</a>	Rev: <a href="#">E1</a>	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">5</a> of <a href="#">7</a>
Drawn By:	File: <a href="#">PMP23547_Main_Board_LLC Isolators_CS E1.SchDoc</a>	Source:
Engineer: <a href="#">Brent McDonald</a>	Contact: <a href="#">http://www.ti.com/support</a>	



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 10/24/2024
TID #: N/A	Project Title: 8kW AI Server AI Main Board	
Number: PMP23547	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet 6 of 7
Drawn By:	File: PMP23547_Main_Board_LLC_Power_Stage_ESD.doc	
Engineer: Brent McDonald	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



PCB Number: PMP23547  
PCB Rev: E1

PCB  
LOGO  
Texas Instruments



PCB  
LOGO  
FCC disclaimer

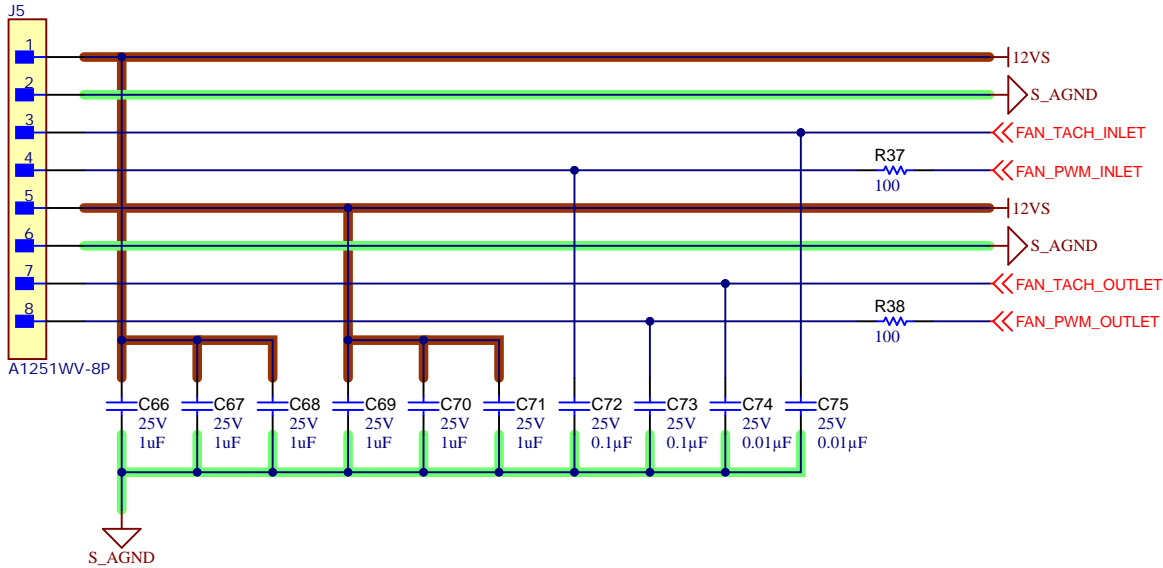
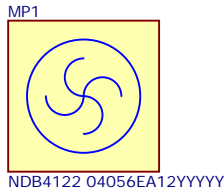
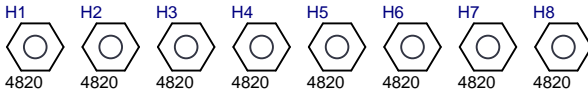
PCB  
LOGO  
WEEE logo

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

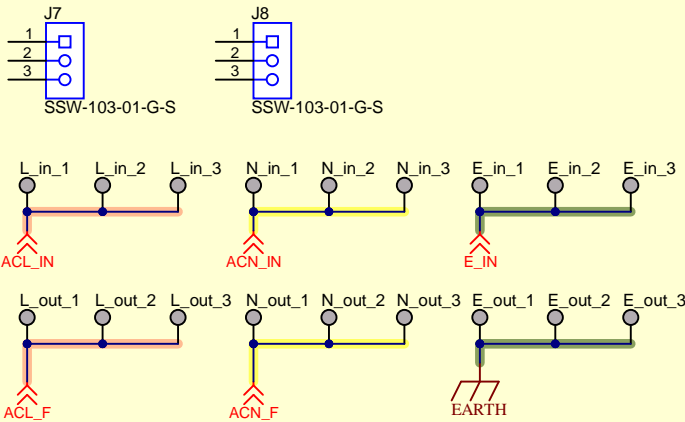
ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

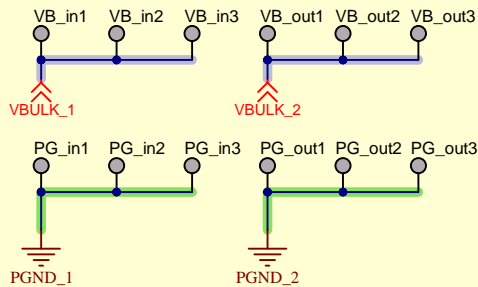
ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



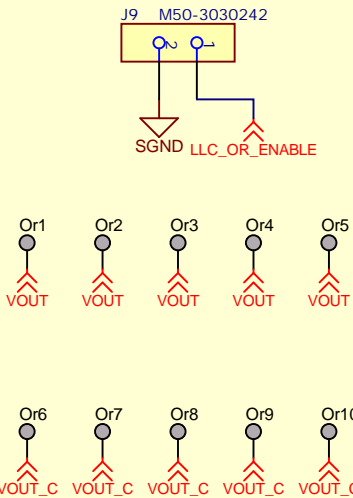
## AC Power Board



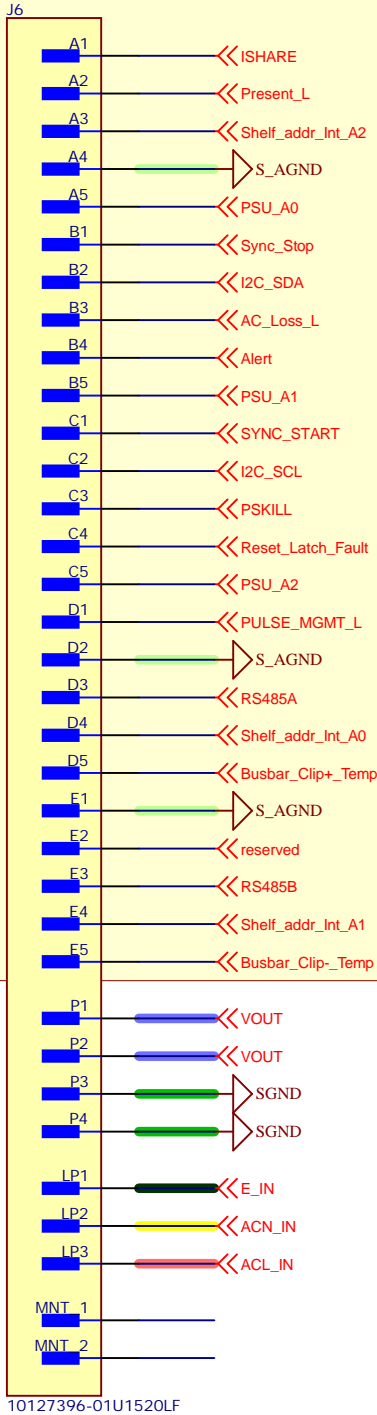
## DC Power Board



## Oring Card



## Not Implemented



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TID #: N/A	Project Title: 8kW AI Server AI Main Board	
Number: <a href="#">PMP23547</a>	Rev: <a href="#">E1</a>	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 7
Drawn By: <a href="#">Brent McDonald</a>	File: <a href="#">PMP23547_Main_Board_Hardware_E1.SchDoc</a>	Size: B
Engineer: <a href="#">Brent McDonald</a>	Contact: <a href="#">http://www.ti.com/support</a>	