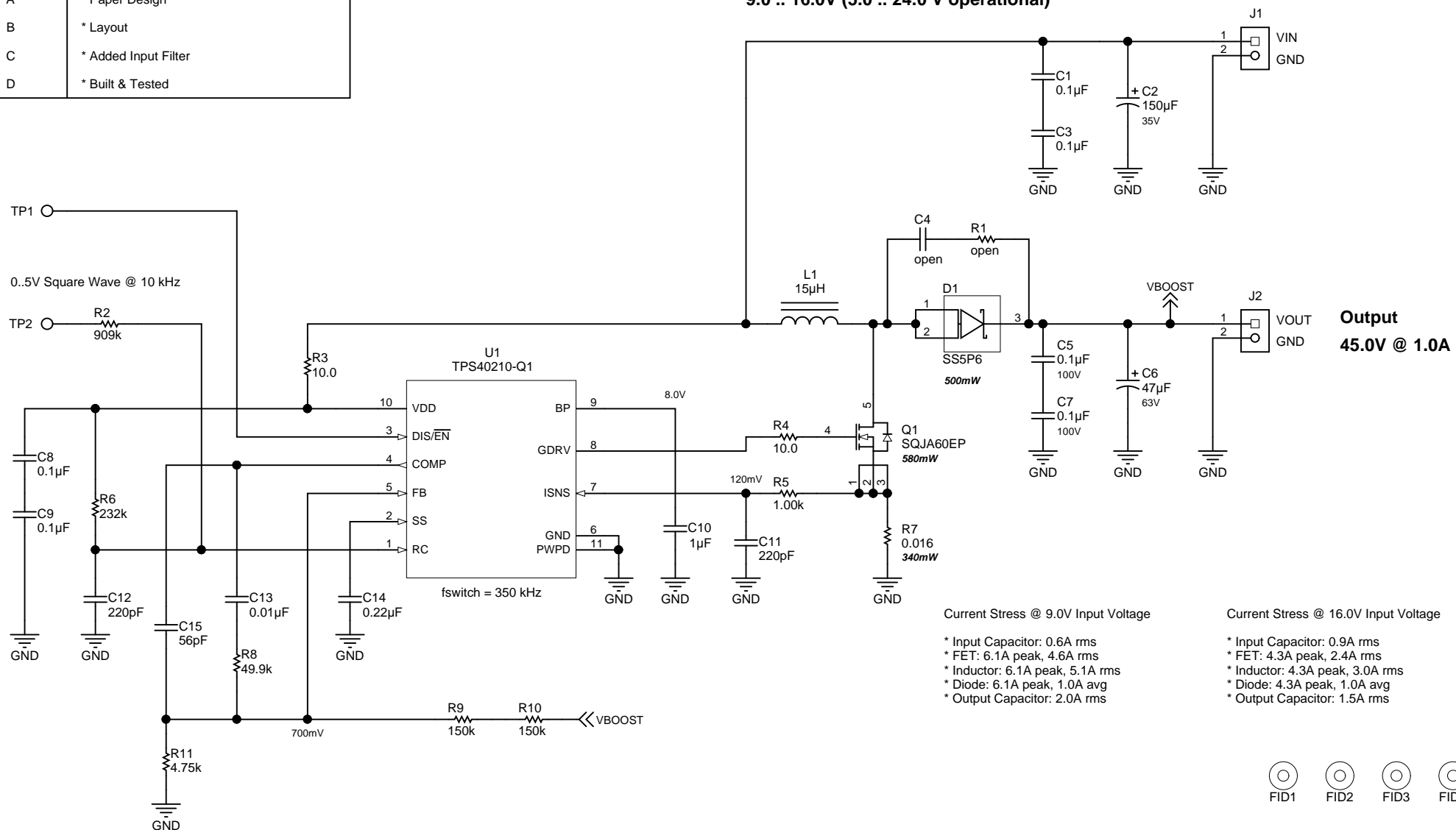


Revision History

Revision	Notes
A	* Paper Design
B	* Layout
C	* Added Input Filter
D	* Built & Tested

Input

9.0 .. 16.0V (5.0 .. 24.0 V operational)



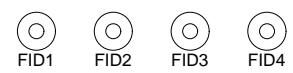
Output
45.0V @ 1.0A

Current Stress @ 9.0V Input Voltage

- * Input Capacitor: 0.6A rms
- * FET: 6.1A peak, 4.6A rms
- * Inductor: 6.1A peak, 5.1A rms
- * Diode: 6.1A peak, 1.0A avg
- * Output Capacitor: 2.0A rms

Current Stress @ 16.0V Input Voltage

- * Input Capacitor: 0.9A rms
- * FET: 4.3A peak, 2.4A rms
- * Inductor: 4.3A peak, 3.0A rms
- * Diode: 4.3A peak, 1.0A avg
- * Output Capacitor: 1.5A rms



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 11/15/2017
TID #: TID	Project Title: Automotive Booster	
Number: PMP30352	Rev: D	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:	File: PMP30352RevD.SchDoc	Size: A4
Engineer: Matthias Ulmann	Contact: http://www.ti.com/support	

TEXAS INSTRUMENTS
http://www.ti.com
© Texas Instruments 2017