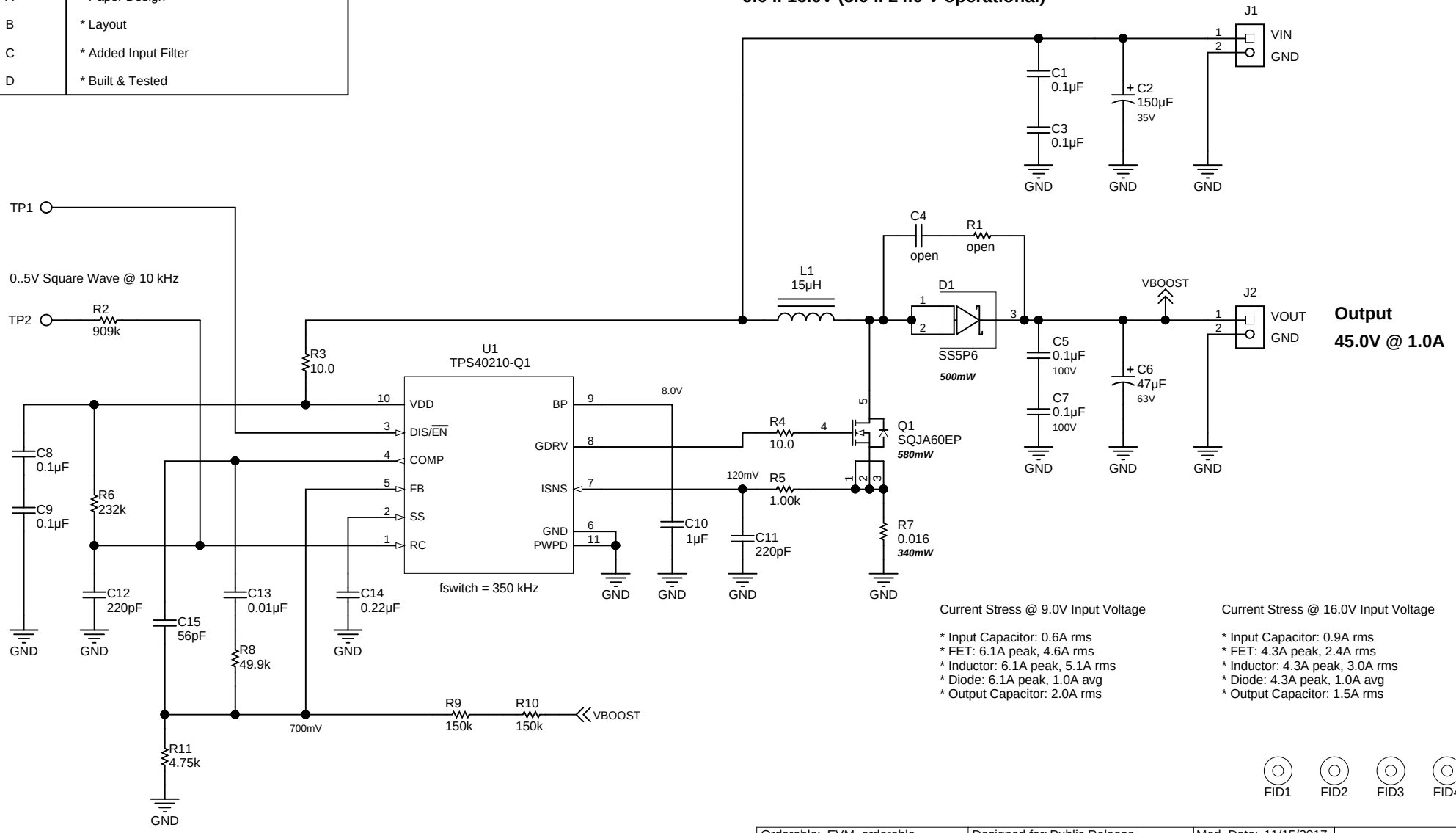
Revision History	
Revision	Notes
A	* Paper Design
B	* Layout
C	* Added Input Filter
D	* Built & Tested

Input

9.0 .. 16.0V (5.0 .. 24.0 V operational)



Output

45.0V @ 1.0A

Current Stress @ 9.0V Input Voltage

* Input Capacitor: 0.6A rms
* FET: 6.1A peak, 4.6A rms
* Inductor: 6.1A peak, 5.1A rms
* Diode: 6.1A peak, 1.0A avg
* Output Capacitor: 2.0A rms

Current Stress @ 16.0V Input Voltage

* Input Capacitor: 0.9A rms
* FET: 4.3A peak, 2.4A rms
* Inductor: 4.3A peak, 3.0A rms
* Diode: 4.3A peak, 1.0A avg
* Output Capacitor: 1.5A rms

Orderable: EVM_orderable		Designed for: Public Release	Mod. Date: 11/15/2017
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SVN Rev: Version control disabled		Assembly Variant: [No Variations]	Sheet: 1 of 1
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Engineer: Matthias Ulmann		Contact: http://www.ti.com/support	

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