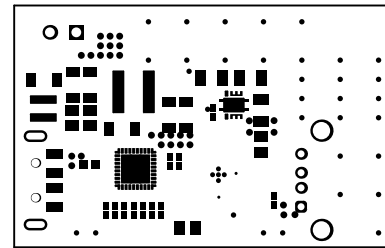
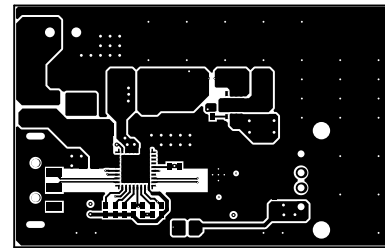


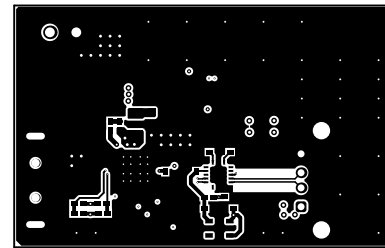
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 9/26/2019 12:29:44 PM	TEXAS INSTRUMENTS	



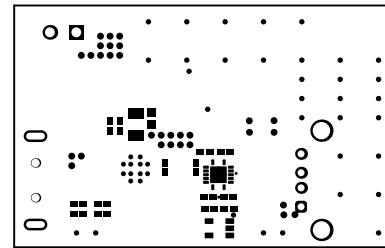
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 9/26/2019 12:29:45 PM	TEXAS INSTRUMENTS	



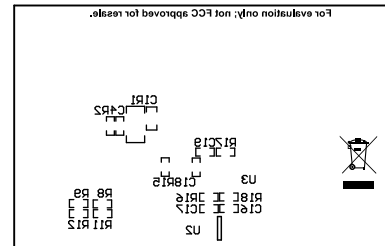
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 9/26/2019 12:29:47 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 9/26/2019 12:29:48 PM	TEXAS INSTRUMENTS	



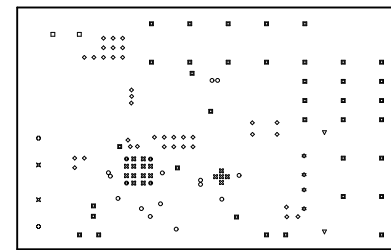
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 9/26/2019 12:29:50 PM	TEXAS INSTRUMENTS	



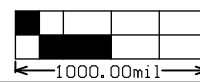
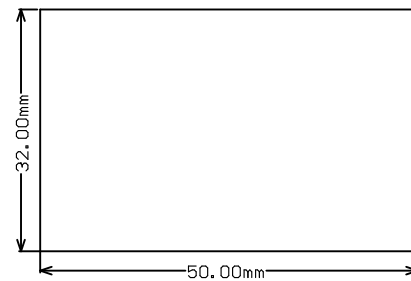
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 9/26/2019 12:29:51 PM	TEXAS INSTRUMENTS	

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
✕	2	43.31mil (1.100mm)	NPTH	Round	Top Layer - Bottom Layer	
○	14	6.00mil (0.152mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	17	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
⊙	4	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer	
■	37	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
◇	36	15.00mil (0.381mm)	PTH	Round	Top Layer - Bottom Layer	
⊛	4	35.22mil (0.920mm)	PTH	Round	Top Layer - Bottom Layer	
□	2	51.18mil (1.300mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	90.55mil (2.300mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	2	35.43mil (0.900mm)	PTH	Slot	Top Layer - Bottom Layer	
120 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 9/26/2019 12:29:53 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP40544	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 9/26/2019 12:29:55 PM	TEXAS INSTRUMENTS	