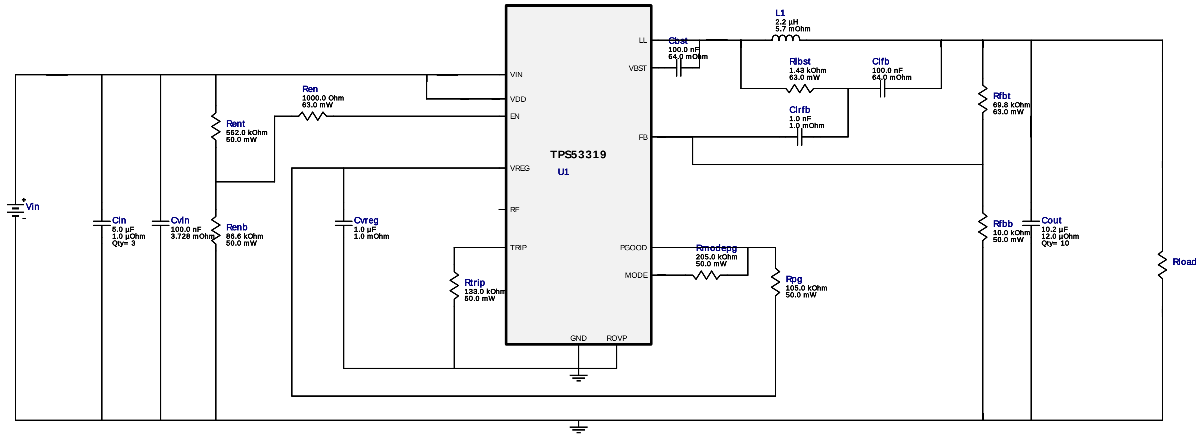
WEBENCH ® Design Report

VinMin = 12.0V VinMax = 12.0V Vout = 5.0V Iout = 10.0A	Device = TPS53319DQPR Topology = Buck Created = 2020-09-21 00:42:15.718 BOM Cost = NA BOM Count = 29 Total Pd = 2.0W

Design : 12845 TPS53319DQPR

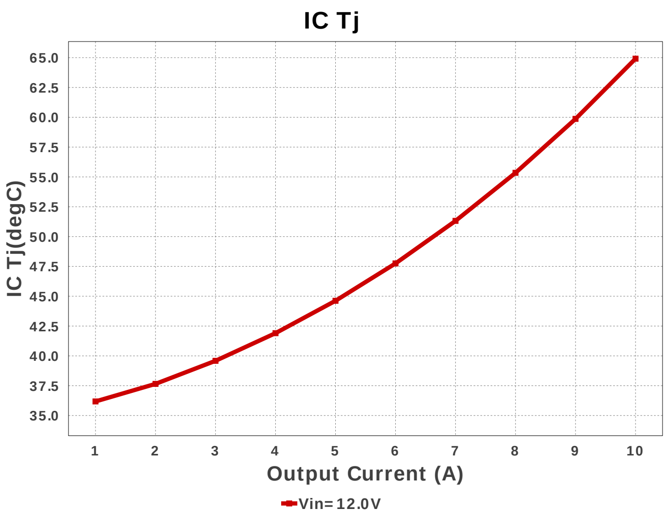
TPS53319DQPR 12V-12V to 5.00V @ 10A

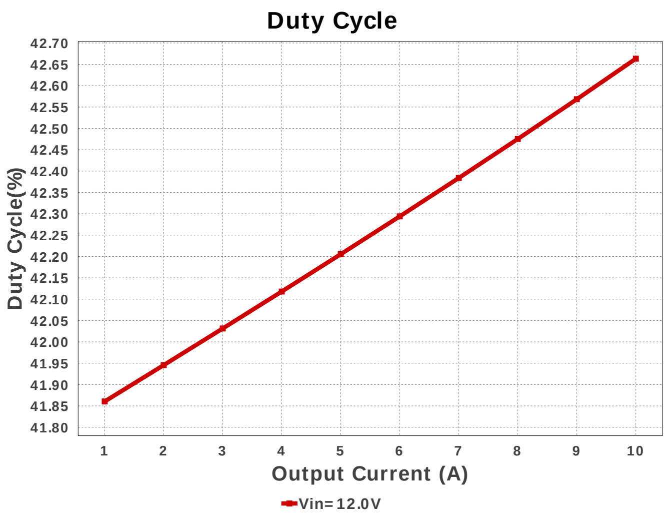


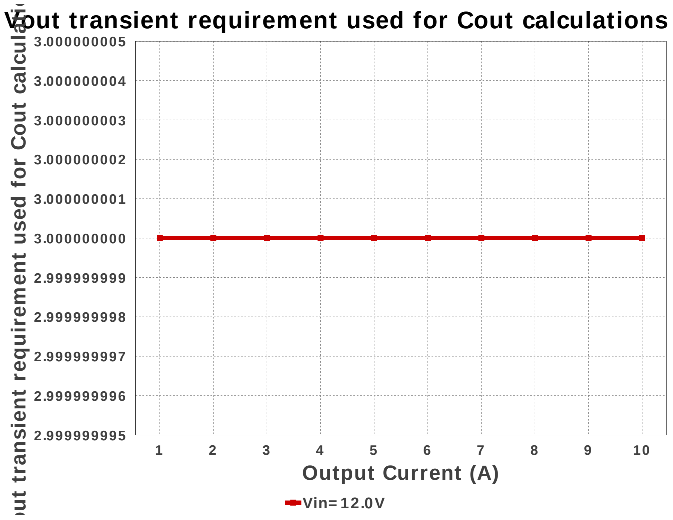
Electrical BOM

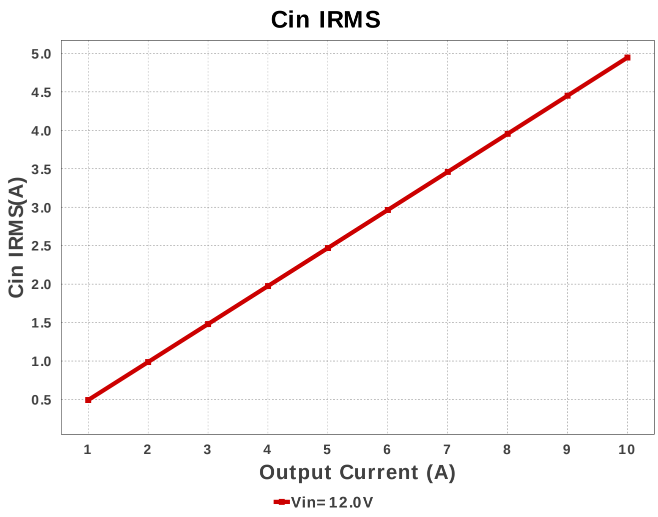
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbst	Kemet	C0805C104M5RACTU Series= X7R	Cap= 100.0 nF ESR= 64.0 mOhm VDC= 50.0 V IRMS= 1.64 A	1	$0.01	0805 7 mm²
Cin	CUSTOM	CUSTOM Series= X7R	Cap= 5.0 uF ESR= 1.0 uOhm VDC= 25.0 V IRMS= 6.635 A	3	NA	2220_280 0 mm²
Clfb	Kemet	C0805C104M5RACTU Series= X7R	Cap= 100.0 nF ESR= 64.0 mOhm VDC= 50.0 V IRMS= 1.64 A	1	$0.01	0805 7 mm²
Clrfb	Yageo	CC0805KRX7R9BB102 Series= X7R	Cap= 1.0 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	$0.01	0805 7 mm²
Cout	CUSTOM	CUSTOM Series= X5R	Cap= 10.2 uF ESR= 12.0 uOhm VDC= 10.0 V IRMS= 4.2862 A	10	NA	1206_190 0 mm²
Cvin	CUSTOM	CUSTOM Series= X5R	Cap= 100.0 nF ESR= 3.728 mOhm VDC= 35.0 V IRMS= 2.69359 A	1	NA	0603 0 mm²
Cvreg	Taiyo Yuden	TMK212B7105KG-T Series= X7R	Cap= 1.0 uF ESR= 1.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	$0.02	0805 7 mm²
L1	Coilcraft	XAL7070-222MEB	L= 2.2 µH 5.7 mOhm	1	$1.19	XAL7070 87 mm²
Ren	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²

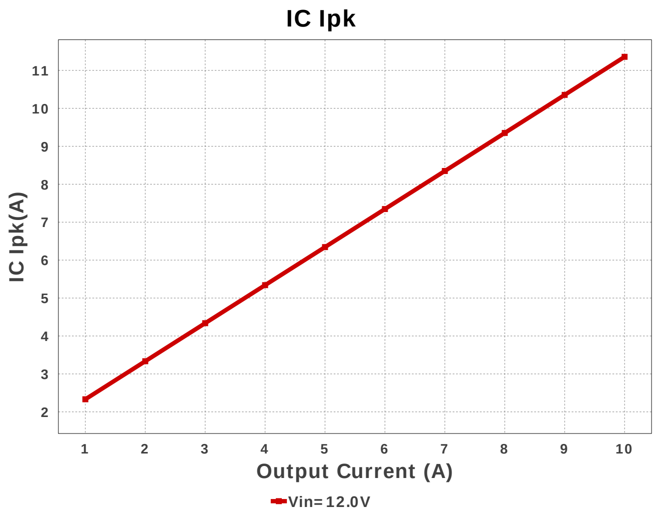
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Renb	Yageo	RC0201FR-0786K6L Series= ?	Res= 86.6 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rent	Yageo	RC0201FR-7D562KL Series= ?	Res= 562.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rfbb	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rfbt	Vishay-Dale	CRCW040269K8FKED Series= CRCW..e3	Res= 69.8 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²
Rlbst	Vishay-Dale	CRCW04021K43FKED Series= CRCW..e3	Res= 1.43 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²
Rmodepg	Yageo	RC0201FR-07205KL Series= ?	Res= 205.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rpg	Yageo	RC0201FR-07105KL Series= ?	Res= 105.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rtrip	Yageo	RC0201FR-07133KL Series= ?	Res= 133.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
U1	Texas Instruments	TPS53319DQPR	Switcher	1	$1.55	DQP0022A 56 mm²

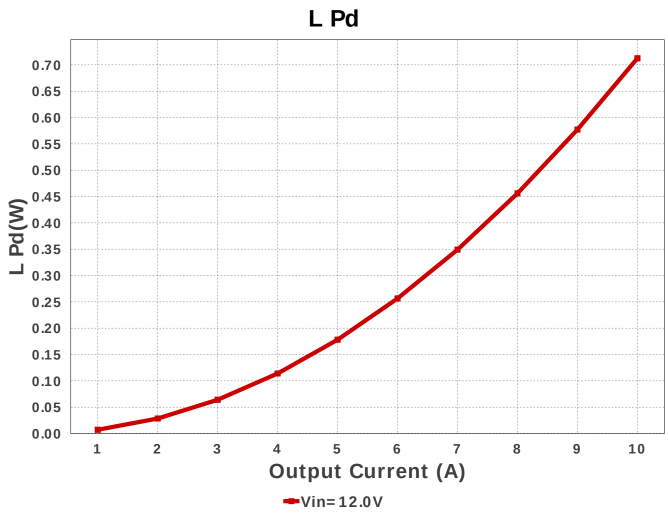


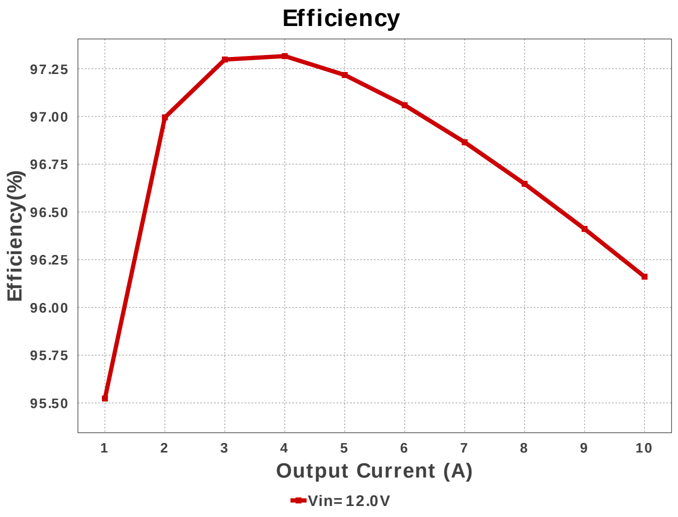


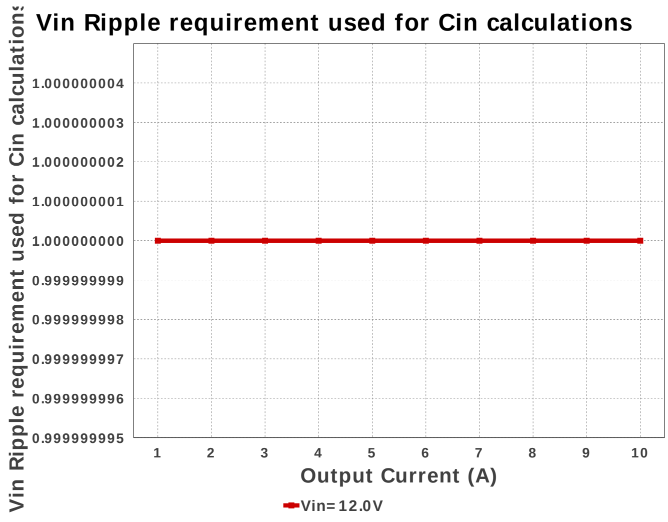

Vout transient requirement used for Cout calculations
3.000000005
3.000000004
3.000000003
3.000000002
3.000000001
3.000000000
2.999999999
2.999999998
2.999999997
2.999999996
2.999999995
1 2 3 4 5 6 7 8 9 10
Output Current (A)
Vin=12.0V

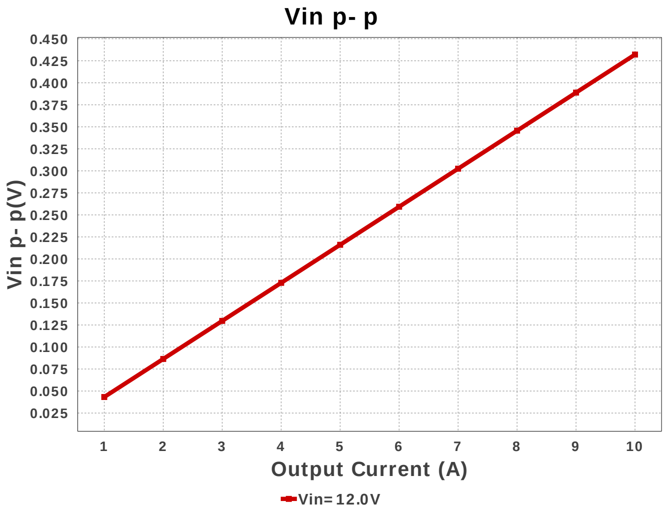

Cin IRMS
Cin IRMS(A)
5.0
4.5
4.0
3.5
3.0
2.5
2.0
1.5
1.0
0.5
1 2 3 4 5 6 7 8 9 10
Output Current (A)
Vin=12.0V

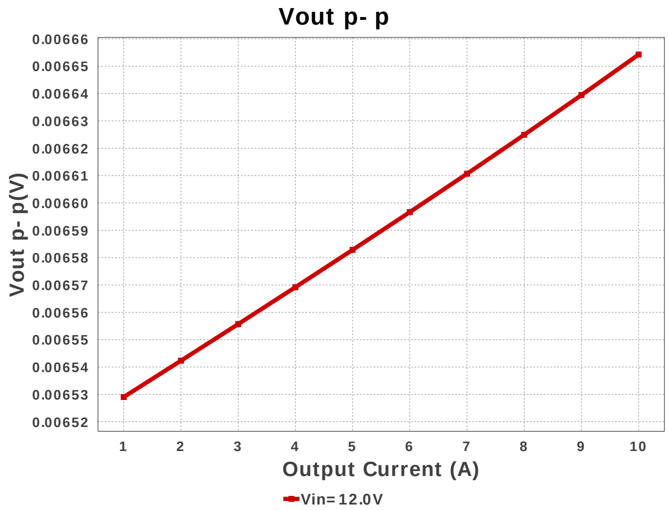

IC Ipk
IC Ipk(A)
11
10
9
8
7
6
5
4
3
2
1 2 3 4 5 6 7 8 9 10
Output Current (A)
Vin=12.0V

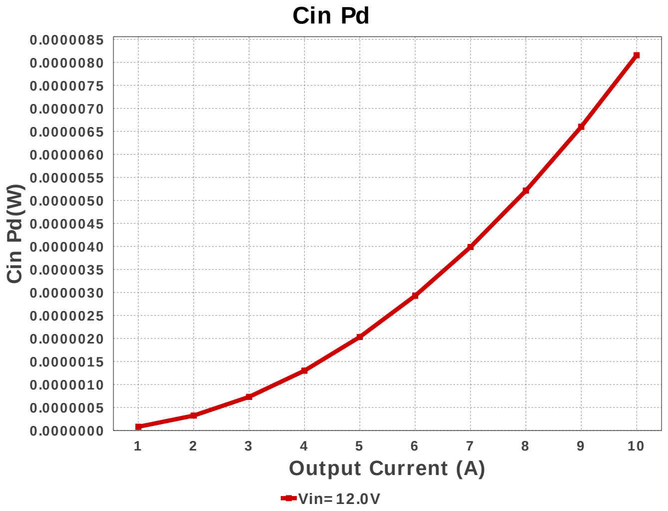

L Pd
L Pd(W)
0.70
0.65
0.60
0.55
0.50
0.45
0.40
0.35
0.30
0.25
0.20
0.15
0.10
0.05
0.00
1 2 3 4 5 6 7 8 9 10
Output Current (A)
Vin=12.0V

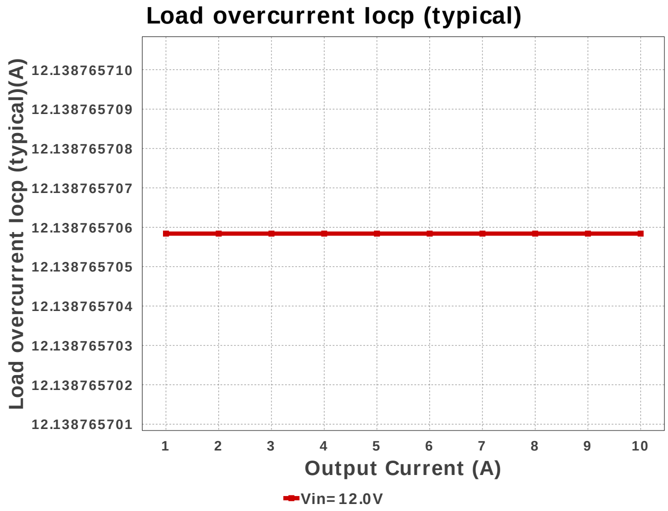

Efficiency
Efficiency(%)
97.25
97.00
96.75
96.50
96.25
96.00
95.75
95.50
1 2 3 4 5 6 7 8 9 10
Output Current (A)
Vin=12.0V

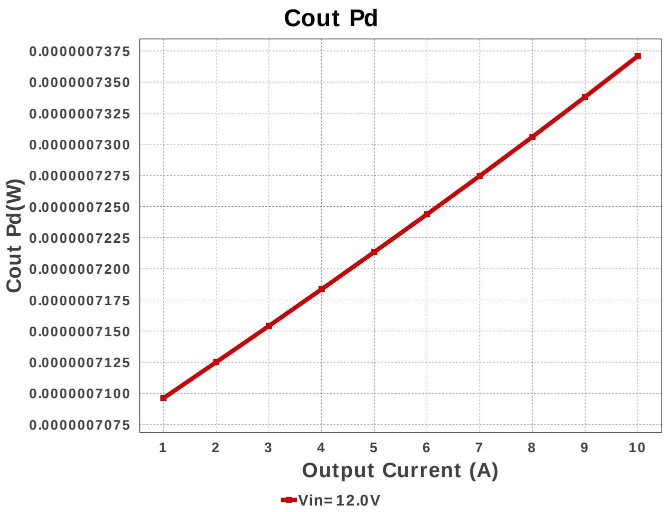

Vin Ripple requirement used for Cin calculations
1.000000004
1.000000003
1.000000002
1.000000001
1.000000000
0.999999999
0.999999998
0.999999997
0.999999996
0.999999995
1 2 3 4 5 6 7 8 9 10
Output Current (A)
Vin=12.0V

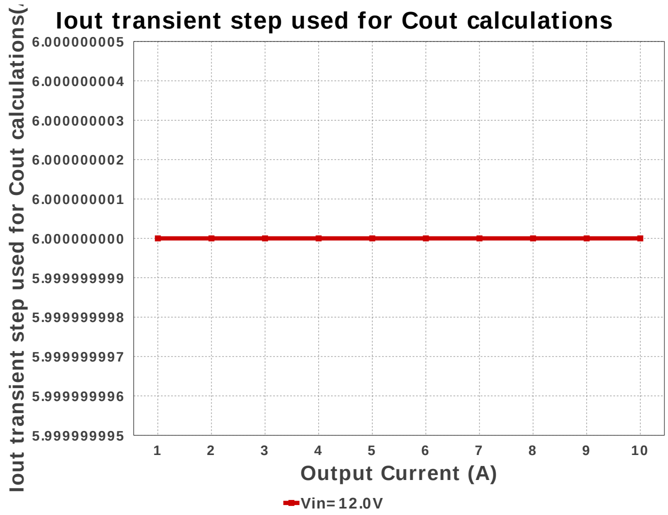

Vin p- p
Vin p- p(V)
Output Current (A)
Vin=12.0V

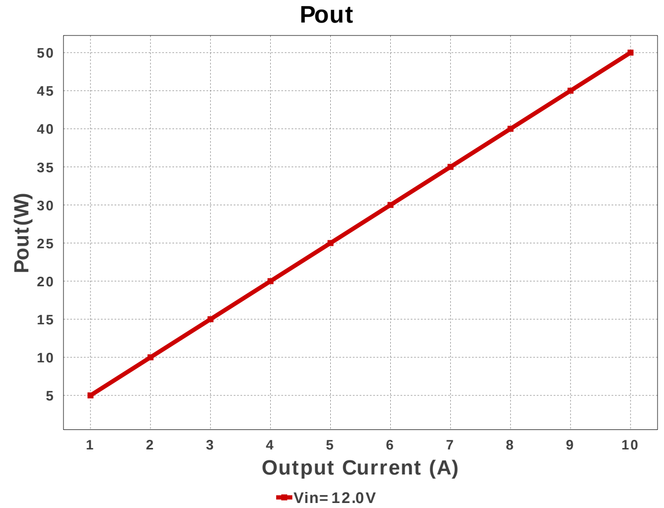

Vout p- p
Vout p- p(V)
Output Current (A)
Vin=12.0V

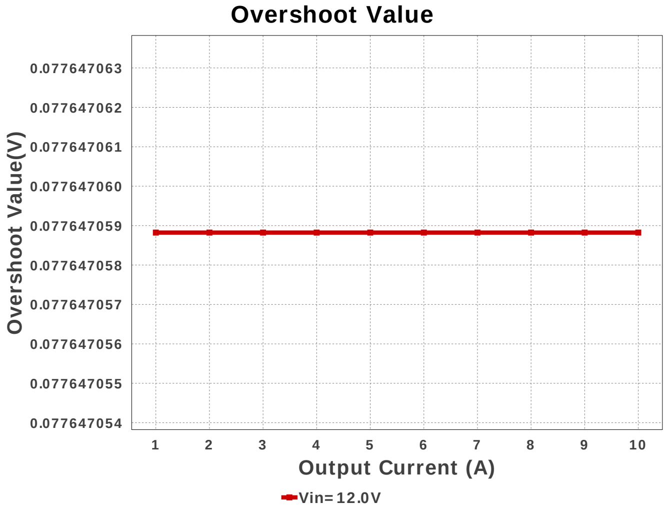

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=12.0V

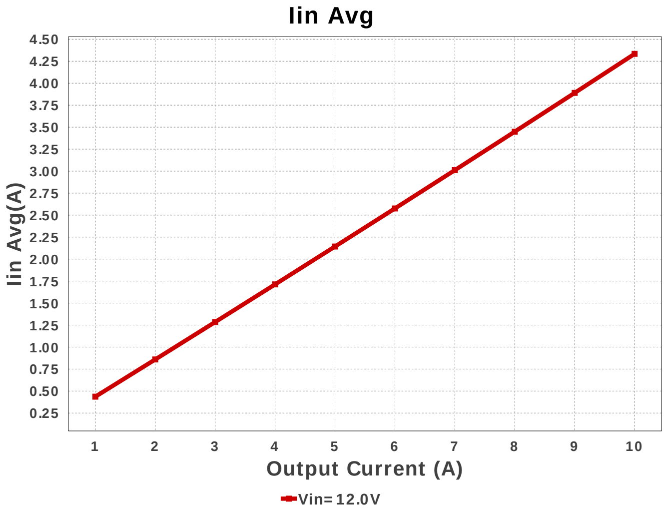

Load overcurrent Iocp (typical)
Load overcurrent Iocp (typical)(A)
Output Current (A)
Vin=12.0V

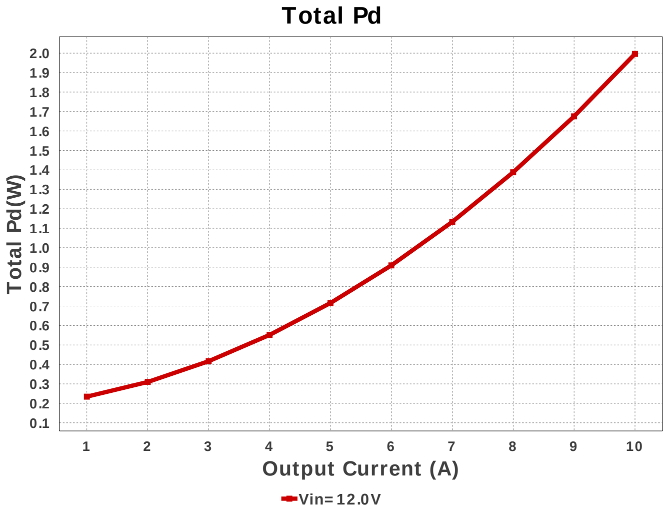

Cout Pd
Cout Pd(W)
Output Current (A)
Vin=12.0V

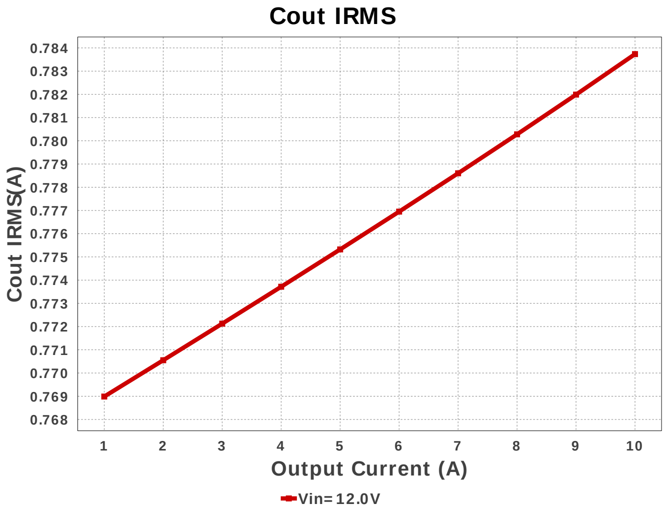

Iout transient step used for Cout calculations
Iout transient step used for Cout calculations(
Output Current (A)
Vin=12.0V

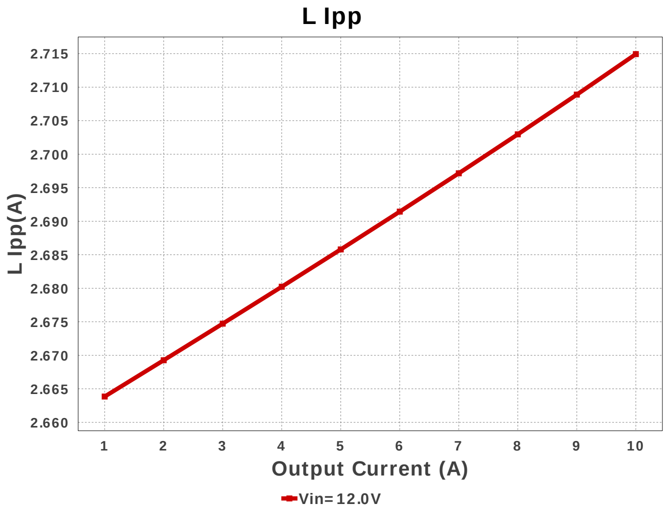

Pout
Pout(W)
Output Current (A)
Vin=12.0V

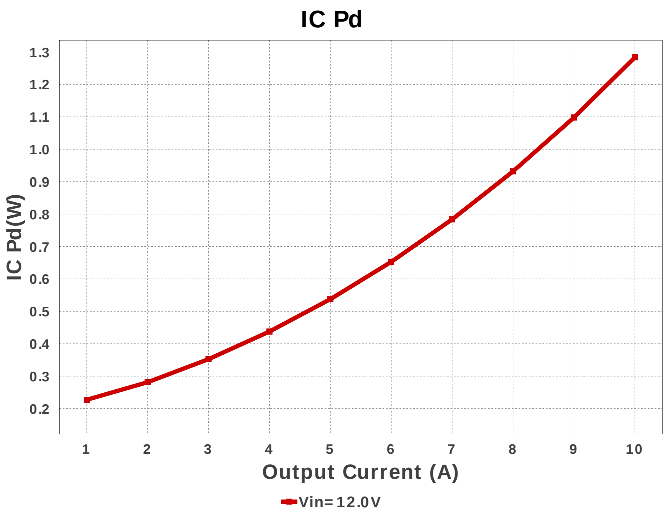

Overshoot Value
Overshoot Value(V)
Output Current (A)
Vin=12.0V

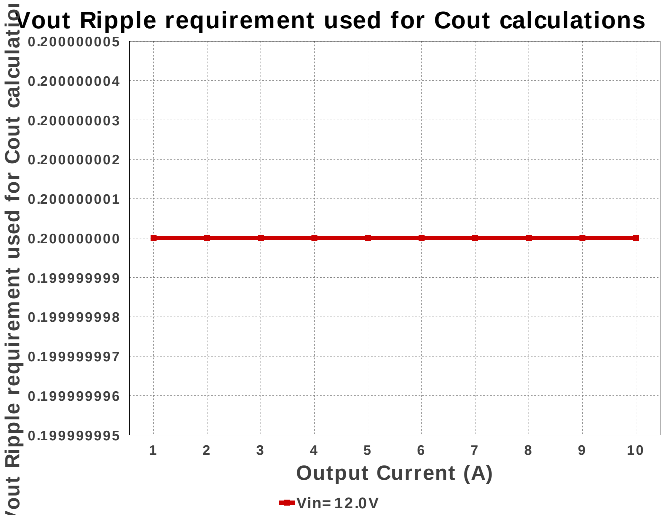

Iin Avg
Iin Avg(A)
Output Current (A)
Vin=12.0V

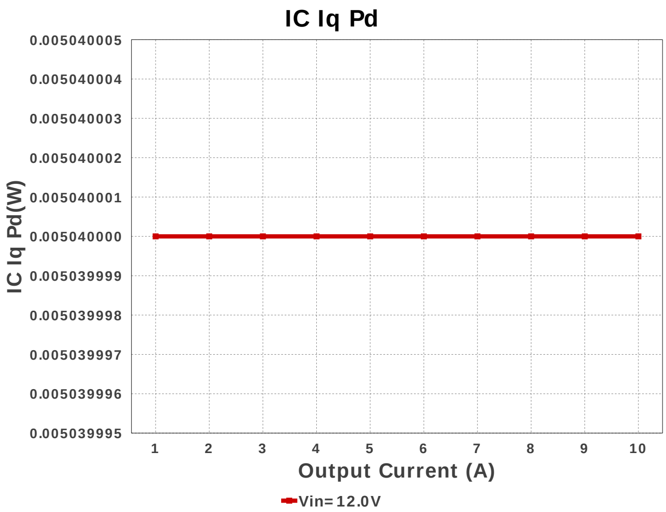

Total Pd
Total Pd(W)
Output Current (A)
Vin=12.0V

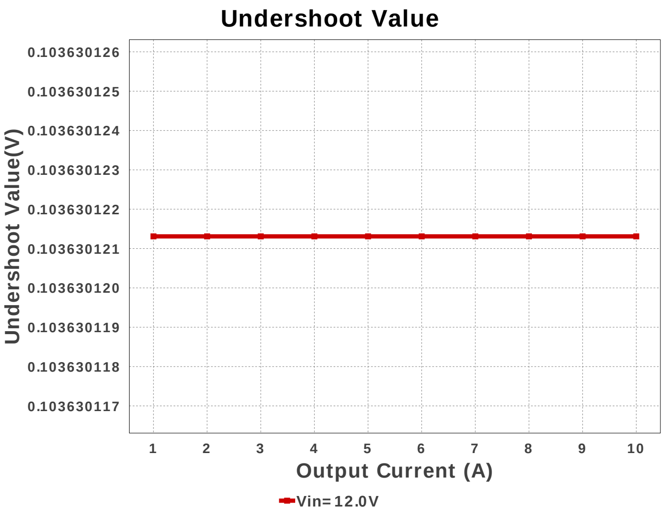

Cout IRMS
Cout IRMS(A)
Output Current (A)
Vin=12.0V


L Ipp
L Ipp(A)
Output Current (A)
Vin=12.0V









Operating Values

#	Name	Value	Category	Description
1.	BOM Count	29		Total Design BOM count
2.	Total BOM	NA		Total BOM Cost
3.	Cin IRMS	4.946 A	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	8.154 µW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	783.734 mA	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	737.09 nW	Capacitor	Output capacitor power dissipation
7.	IC Ipk	11.357 A	IC	Peak switch current in IC
8.	IC Iq Pd	5.04 mW	IC	IC Iq Pd
9.	IC Pd	1.284 W	IC	IC power dissipation
10.	IC Tj	64.913 degC	IC	IC junction temperature
11.	ICThetaJA	27.2 degC/W	IC	IC junction-to-ambient thermal resistance
12.	Iin Avg	4.333 A	IC	Average input current
13.	L Ipp	2.715 A	Inductor	Peak-to-peak inductor ripple current
14.	L Pd	712.5 mW	Inductor	Inductor power dissipation
15.	Cin Pd	8.154 µW	Power	Input capacitor power dissipation
16.	Cout Pd	737.09 nW	Power	Output capacitor power dissipation
17.	IC Pd	1.284 W	Power	IC power dissipation
18.	L Pd	712.5 mW	Power	Inductor power dissipation
19.	Total Pd	1.996 W	Power	Total Power Dissipation
20.	Duty Cycle	42.663 %	System Information	Duty cycle
21.	Efficiency	96.161 %	System Information	Steady state efficiency
22.	FootPrint	467.0 mm^2	System Information	Total Foot Print Area of BOM components
23.	Frequency	500.0 kHz	System Information	Switching frequency
24.	Iout	10.0 A	System Information	Iout operating point
25.	Iout transient step used for Cout calculations	6.0 A	System Information	Custom Transient current step requirement that was used for Cout selection (A).

#	Name	Value	Category	Description
26.	Load overcurrent Iocp (typical)	12.139 A	System Information	Over current protection threshold
27.	Mode	CCM	System Information	Conduction Mode
28.	Overshoot Value	77.647 mV	System Information	Theoretical Vout Overshoot Value
29.	Pout	50.0 W	System Information	Total output power
30.	Undershoot Value	103.63 mV	System Information	Theoretical Vout Undershoot Value
31.	Vin	12.0 V	System Information	Vin operating point
32.	Vin Ripple requirement used for Cin calculations	1.0 %	System Information	Custom maximum input ripple requirement that was used for Cin selection(% of Minimum Vin).
33.	Vin p-p	432.099 mV	System Information	Peak-to-peak input voltage
34.	Vout	5.0 V	System Information	Operational Output Voltage
35.	Vout Actual	4.981 V	System Information	Vout Actual calculated based on selected voltage divider resistors
36.	Vout Ripple requirement used for Cout calculations	200.0 m%	System Information	Custom maximum output ripple requirement that was used for Cout selection(% of Vout).
37.	Vout Tolerance	2.745 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
38.	Vout p-p	6.654 mV	System Information	Peak-to-peak output ripple voltage
39.	Vout transient requirement used for Cout calculations	3.0 %	System Information	Custom Transient voltage change requirement that was used for Cout selection (% of Vout).

Design Inputs

Name	Value	Description
Iout	10.0	Maximum Output Current
VinMax	12.0	Maximum input voltage
VinMin	12.0	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	TPS53319	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

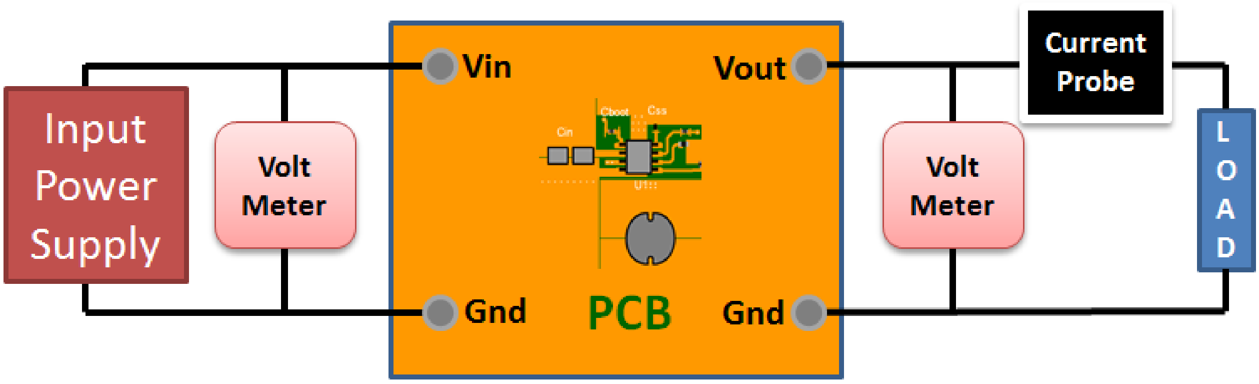
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 12.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

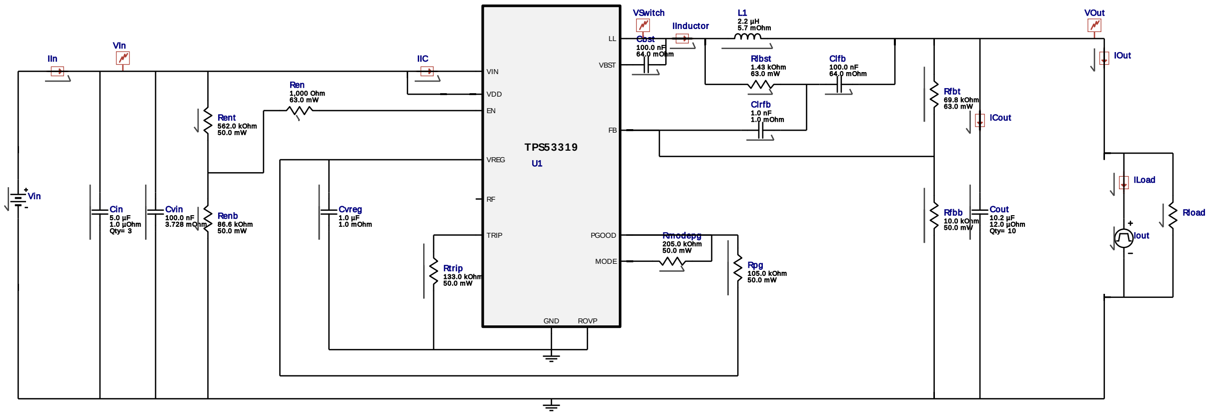


WEBENCH® Electrical Simulation Report

Design Id = 12845

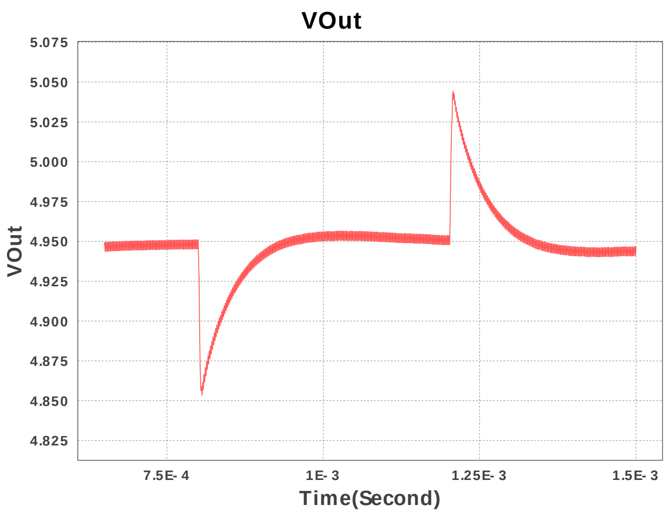
sim_id = 1

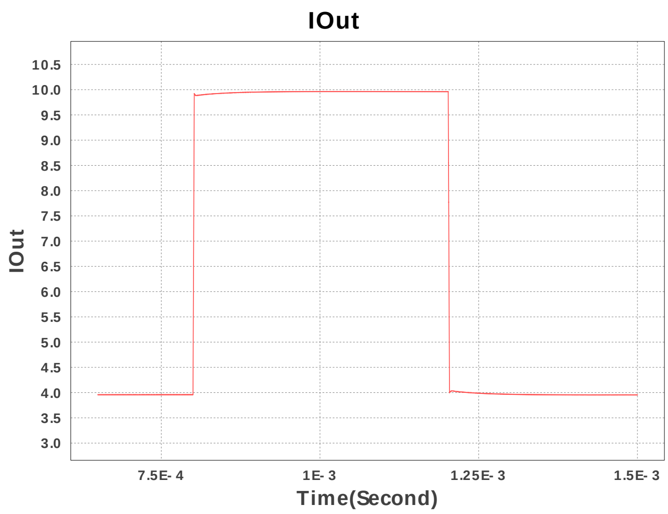
Simulation Type = Load Transient



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	Iout	signal type	Signal Type	PULSE
		I1	Initial Load Current	0.0 A
		I2	Minimum Load Current	6.0 A
		Td	Initial Time Delay	8.0E-4 s
		Tr	Rise Time	2.0E-6 s
		Tf	Fall Time	2.0E-6 s
		Pw	Pulse Width	4.0E-4 s
2.	Rload	R	Load Resistance	1.25





Design Assistance

1. Master key : 1B0CCE6EBD53FA1F[v1]

2. **TPS53319** Product Folder : http://www.ti.com/product/TPS53319 : contains the data sheet and other resources.