

♀
**** 05/17/16 09:20:23 ***** PSpice 16.6.0 (October 2012) ***** ID# 0 *****

** Profile: "sim_TLV342-a3" [D:\WSimetrix\Modely_Lenka\TLV342_16-06-2016\tlv342-pspicefiles\sim_tlv342Wa3.sim]

**** CIRCUIT DESCRIPTION

** Creating circuit file "a3.cir"
** WARNING: THIS AUTOMATICALLY GENERATED FILE MAY BE OVERWRITTEN BY SUBSEQUENT SIMULATIONS

*Libraries:
* Profile Libraries :
* Local Libraries :
.LIB "..\..\..\tlv342.lib"
* From [PSPICE NETLIST] section of C:\Users\fillermiroslav\cdssetup\OrCAD_PSpice\16.6.0\PSpice.ini file:
.lib "nom.lib"

*Analysis directives:
.DC LIN V_V5 0 10 1
.OPTIONS ADVCONV
.PROBE64 V(alias(*)) I(alias(*)) W(alias(*)) D(alias(*)) NOISE(alias(*))
.INC "..\sim_TLV342.net"

**** INCLUDING sim_TLV342.net ****
* source TLV342
X_U1 N00723 N00723 N00372 0 N00408 VOUT2 VOUT2 N04428 TLV342
R_R1 N01086 0 270 TC=0,0
C_C1 N01086 0 20p TC=0,0
R_R2 VOUT2 N01618 1k TC=0,0
C_C2 VOUT2 N01618 20p TC=0,0
V_V3 N01618 0 2.5
V_V4 N00408 0 AC 1
+SIN 2.5 1 1000 0 0 0
X_M1 N04436 N04432 N04428 N04436 N04436 BSP129 PARAMS:
X_U2 N04432 0 AD1580/AD
R_R3 N04432 N04428 6.8k TC=0,0
V_V5 N04436 0 10
R_R4 N00372 N04432 100k TC=0,0
R_R7 N00723 N01086 51 TC=0,0

**** RESUMING a3.cir ****
.END

♀
**** 05/17/16 09:20:23 ***** PSpice 16.6.0 (October 2012) ***** ID# 0 *****

** Profile: "sim_TLV342-a3" [D:\WSimetrix\Modely_Lenka\TLV342_16-06-2016\tlv342-pspicefiles\sim_tlv342Wa3.sim]

**** Diode MODEL PARAMETERS

RecoveryCircuit_DIDEAL
 IS 10.000000E-15
 N .01

OutputCir_VOHVOL_DIDEAL
 IS 10.000000E-15
 N 1.000000E-03

OutputCir_Isc_DIDEAL Vinrange_DIDEAL
 IS 10.000000E-15 10.000000E-15
 N 100.000000E-06 .01

X_U1.X1.XI18.X1.DVNF
 IS 100.000000E-18
 KF 10.000000E-12

X_U1.X1.XI17.X1.DVN
 IS 100.000000E-18
 KF 100.000000E-12

X_U1.X2.XI18.X1.DVNF
 IS 100.000000E-18
 KF 10.000000E-12

X_U1.X2.XI17.X1.DVN
 IS 100.000000E-18
 KF 100.000000E-12

♀
 **** 05/17/16 09:20:23 ***** PSpice 16.6.0 (October 2012) ***** ID# 0 *****

** Profile: "sim_TLV342-a3" [D:\WSimetrix\WModely_Lenka\TLV342_16-06-2016\tlv342-pspicefiles\sim_tlv342Wa3.sim]

*** BJT MODEL PARAMETERS

	X_U2.qn1	X_U2.qn2	X_U2.qp1
	NPN	NPN	PNP
LEVEL	1	1	1
IS	100.000000E-18	100.000000E-18	100.000000E-18
BF	100	100	50.000000E+03
NF	1	1	1
VAF	100	100	100
BR	1	1	1
NR	1	1	1
ISS	0	0	0

RE	0	0	0
RC	0	0	0
CJE	0	0	0
VJE	.75	.75	.75
CJC	0	0	0
VJC	.75	.75	.75
MJC	.33	.33	.33
XCJC	1	1	1
CJS	0	0	0
VJS	.75	.75	.75
KF	0	0	0
AF	1	1	1
CN	2.42	2.42	2.2
D	.87	.87	.52

Starting pseudo-transient algorithm.

INTERNAL ERROR -- Overflow, Convert

ABORTING SIMULATION

**** 05/17/16 09:20:23 ***** PSpice 16.6.0 (October 2012) ***** ID# 0 *****

** Profile: "sim_TLV342-a3" [D:\WSimatrix\WModely_Lenka\TLV342_16-06-2016\tlv342-pspicefiles\sim_tlv342Wa3.sim]

**** JOB STATISTICS SUMMARY

Node counts:

Top level (NUNODS)	=	10
External (NCNODS)	=	222
Total (NUMNOD)	=	222

Total device count (NUMEL)	=	401
Capacitors (C)	=	56
Diodes (D)	=	28
VCVS (E)	=	70
CCCS (F)	=	1
VCCS (G)	=	31
CCVS (H)	=	6
Current Sources (I)	=	12
Inductors (L)	=	12
BJTs (Q)	=	3
Resistors (R)	=	110
Voltage Sources (V)	=	60

Number of subcircuits (X)	=	74
---------------------------	---	----

Matrix statistics:

Matrix size (NSTOP)	=	370
Initial no. elements (NTTAR)	=	1085
No. elements w/ fillin (NTTBR)	=	1704
No. fillins (IFILL)	=	619
No. overflows (NTTOV)	=	527
No. LU operations (IOPS)	=	2094
Percent sparsity (PERSPA)	=	98.755

Analysis statistics:

No. total time points (NUMTTP) = 6
No. rejected time points (NUMRTP) = 6
No. iterations (NUMNIT) = 900

Load Threads = 8

Runtime statistics:

	Seconds	Iterations	Stopped At
Matrix load	= .47		
Matrix solution	= .08	10	
Readin	= .05		
General setup	= 0.00		
CMI setup	= 0.00		
Setup	= 0.00		
DC sweep	= 0.00	0	
Bias point	= 0.00	0	
Default algorithm	= -.39	-1050	
GMIN stepping	= .22	1050	100.00m
Supply stepping	= .17	0	3.87%
AC and noise	= 0.00	0	
Total transient analysis	= 0.00		
Output	= 0.00		
Overhead	= .45		
Total job time (using Solver 1)	= .62		

♀