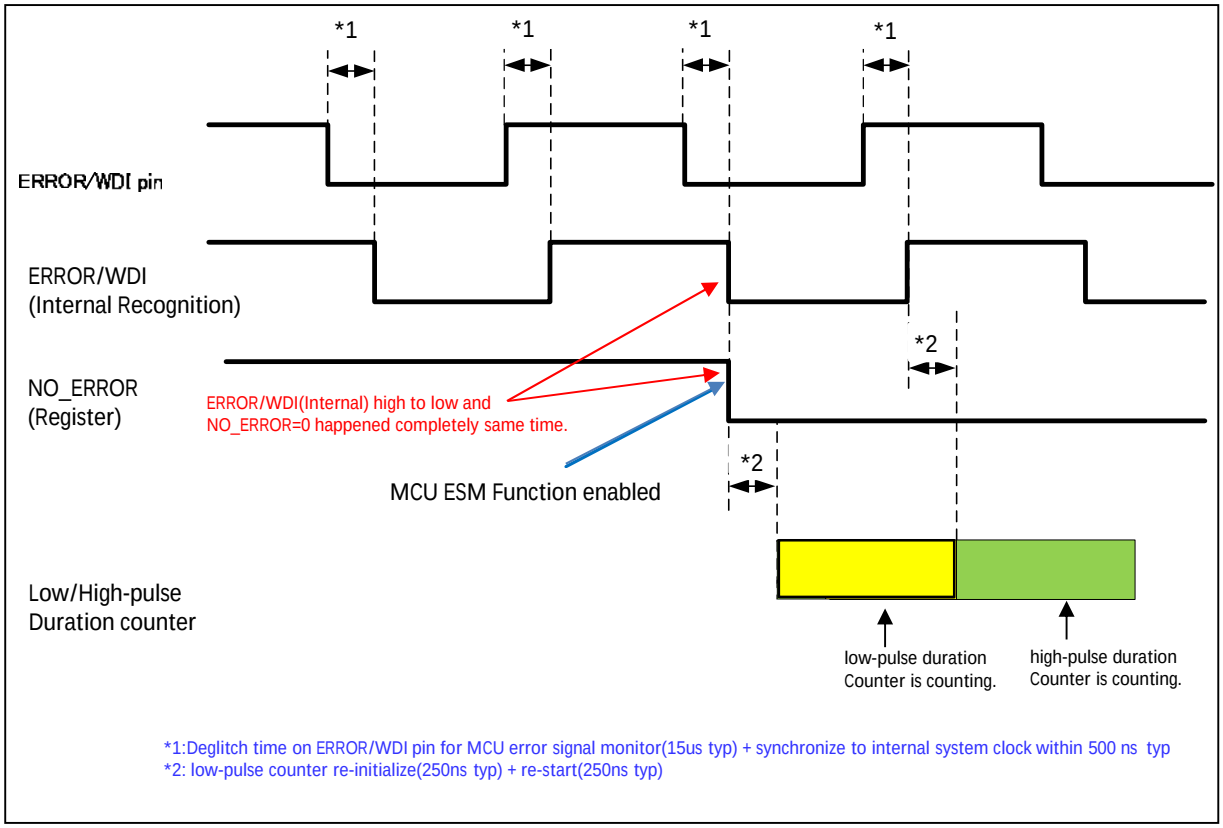
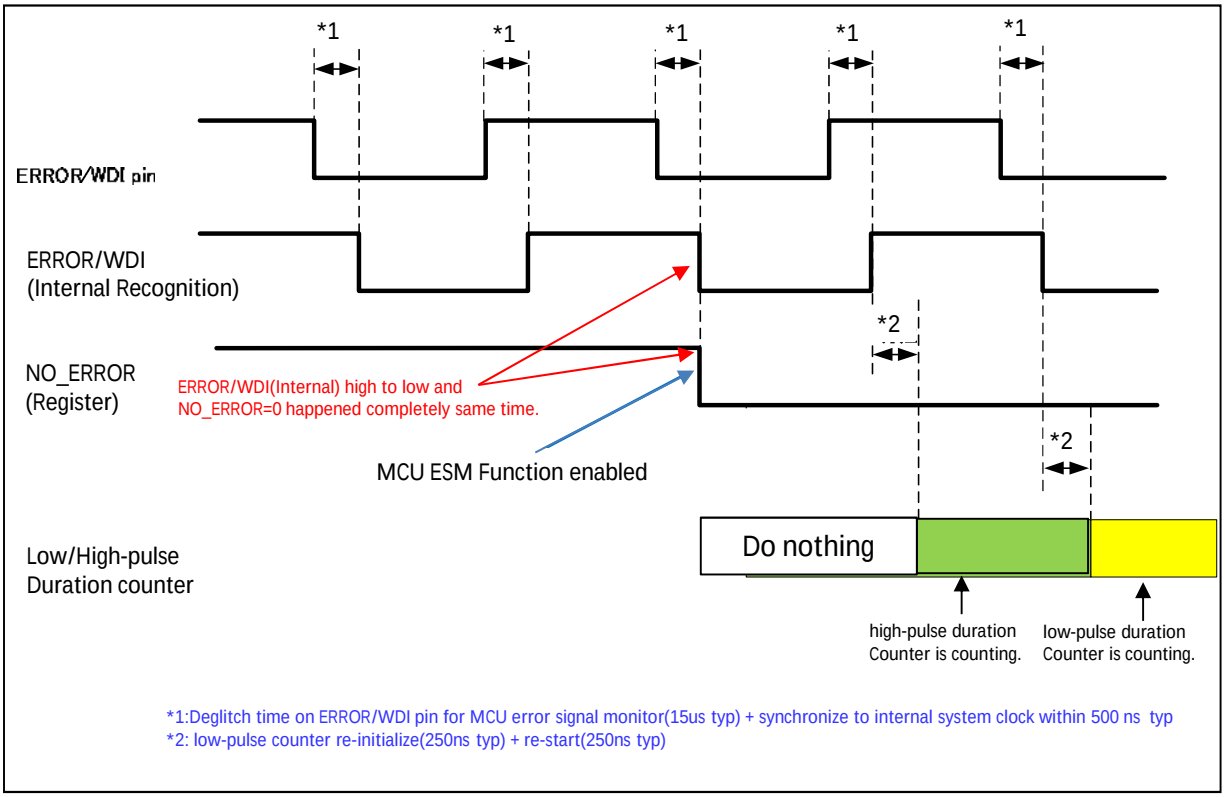
Q1: Pattern 1 (In case of device caught internal ERROR/WDI falling edge)



*1:Deglitch time on ERROR/WDI pin for MCU error signal monitor(15us typ) + synchronize to internal system clock within 500 ns typ
*2: low-pulse counter re-initialize(250ns typ) + re-start(250ns typ)

Q1: Pattern 2 (In case of device didn't catch internal ERROR/WDI falling edge)



*1:Deglitch time on ERROR/WDI pin for MCU error signal monitor(15us typ) + synchronize to internal system clock within 500 ns typ
*2: low-pulse counter re-initialize(250ns typ) + re-start(250ns typ)