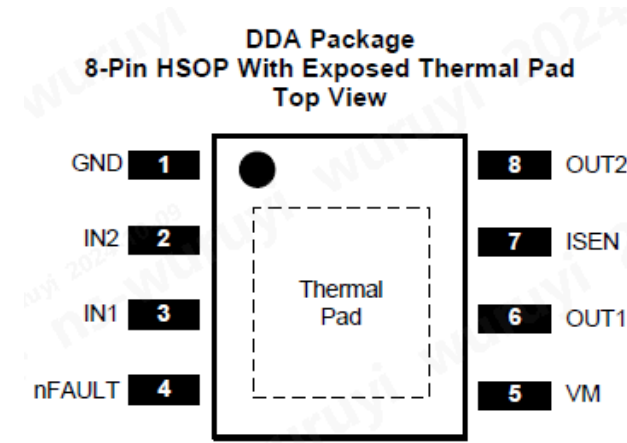


一、芯片规格



Device Information⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
DRV8872-Q1	HSOP (8)	4.90 mm × 6.00 mm

二、输出逻辑

产品中使用的芯片输入输出为 IN2&OUT1，输出逻辑如下：

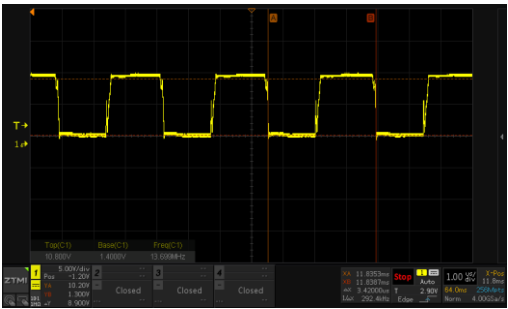
Table 1. H-Bridge Control

IN1	IN2	OUT1	OUT2	DESCRIPTION
0	0	High-Z	High-Z	Coast; H-bridge disabled to High-Z (sleep entered after 1 ms)
0	1	L	H	Reverse (current OUT2 → OUT1)
1	0	H	L	Forward (current OUT1 → OUT2)
1	1	L	L	Brake; low-side slow decay

三、输出信号波形

根据输入输出逻辑测试 8872 输出稳态时的 PWM 信号：

1、PWM 脉宽

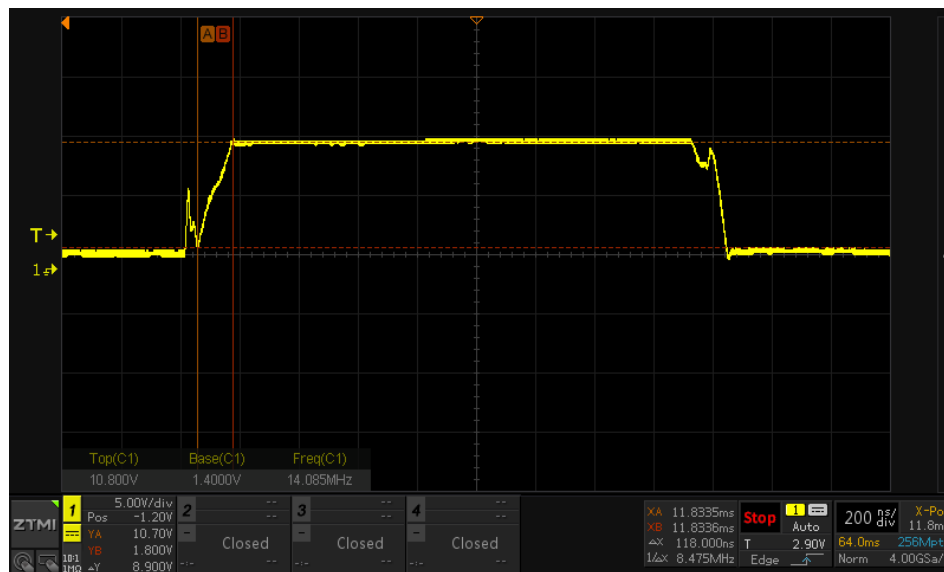


T=300Khz



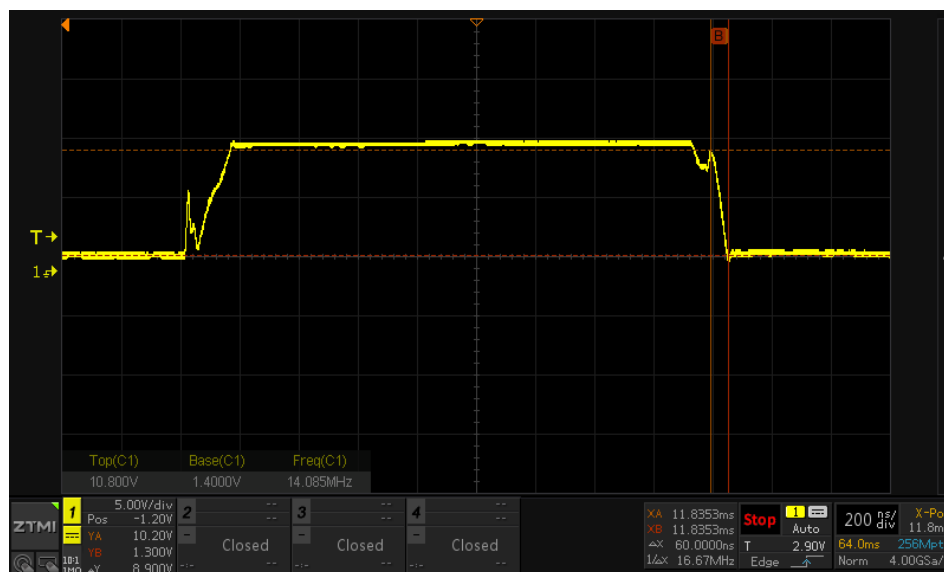
t_{pulse}=1.57us

2、OUT 上升时间



$t_r=118\text{ns}$

3、OUT 下降时间



$t_f=60\text{ns}$