# **About DRV8426E**

#### About behavior for short-circuit(1)

Customers are conducting short-circuit tests on suppose that the motor is short-circuited. I got the waveform at the time of short circuit from customer.

Please comment on this behavior.



Ch1:AOUT1, Ch2:/FAULT,

Ch3:AOUT2, Ch4:Short current

 When short-circuited, it looks like it is clamped at about 7.4A.

Clamped before Itrip's Blanking time(Typ1.0us)

<Question>

Is there a factor that limits the current except for Itrip function?

### About behavior for short-circuit(2)



Ch1:AOUT1, Ch2:/FAULT, Ch3:AOUT2, Ch4:Short current The same short-circuit test as slide 2, but unlike slide 2, nFault asserts after lowering without clamping with 1.8us (locp deglitch time), more than 7A current.

I recognize Table 7-6 as a condition of nFault assertion.

Table 7-6. Fault Condition Summary						
FAULT	CONDITION	ERROR REPORT	H-BRIDGE	CHARGE PUMP	LOGIC	RECOVERY
VM undervoltage (UVLO)	VM < V <sub>UVLO</sub>	nFAULT	Disabled	Disabled	Reset (V <sub>DVDD</sub> < 3.9 V)	Automatic: VM > V <sub>UVLO</sub>
VCP undervoltage (CPUV)	VCP < V <sub>CPUV</sub>	nFAULT	Disabled	Operating	Operating	VCP > V <sub>CPUV</sub>
Overcurrent (OCP)	I <sub>OUT</sub> > I <sub>OCP</sub>	nFAULT	Disabled	Operating	Operating	Latched
Thermal Shutdown (OTSD)	T <sub>J</sub> > T <sub>TSD</sub>	nFAULT	Disabled	Disabled	Operating	Latched

## **Supplement to slide 3**



- As shown in Waveform 3, the VM is not UVLO.
- As shown in waveform 4, Vcp has no problem(Vcp=30V).
- Regarding OTSD, I confirmed it with a customer, but it has not risen as much as OTSD.
- The cause of OCP was confirmed in waveform 5, but it was about 1.0us at 3A. Much smaller than deglitch time for locp.

#### <Question>

Although there is no factor to assert nFault, Is there any factor?

The current is dropping before the Itrip Blanking time, is there any factor that clamps on the device?

If you need our customer's circuit, we will send you it using private message on E2E.