



#### ■ Question 1

When I turned FET (at prior to PVDD) to be OFF→ON at stationary behavior (DRV8305 is activated and motor is rotating), DRV8305 was broken and may be internal short break. During FET=OFF, EN = 5V and PWM supply was stopped.

The other hands, after FET = OFF, when I turned EN=OFF and turned FET/EN to be ON at same time, DRV8305 was not broken and acted normal.

Could you teach the reason why DRV8305 was broken?

Are some Absolute Maximum Ratings the reason?

#### ■ Question 2

The 24V supply has possibility to decrease some voltage.

Are there situations to break DRV8305 other Absolute Maximum Ratings?

#### ■ Question 3

Like Question 1 that the relationship between PVDD and EN, are there break modes with control timing?