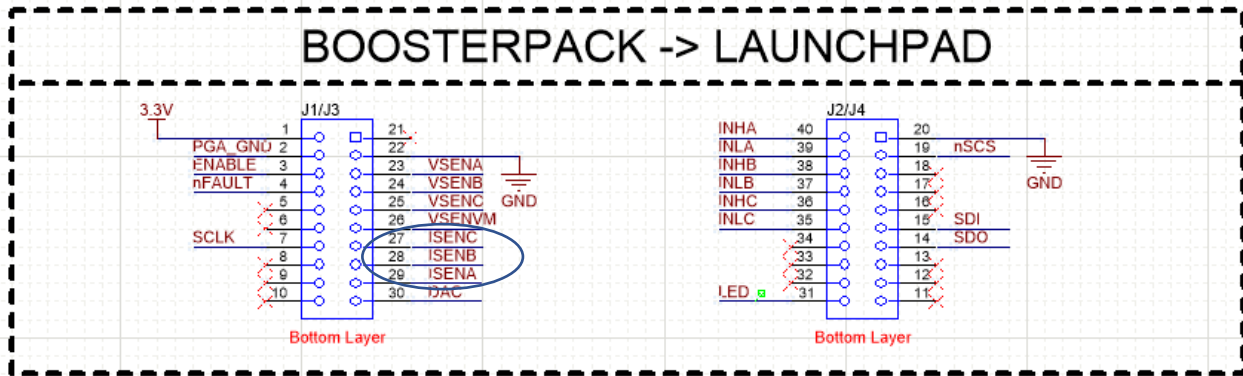


DRV8320RS BOOSTXL connections



From the image above, we see that ISENC is J3.27, ISENB is J3.28, and ISENA is J3.29. These correspond to the LAUNCHXL-F280049C pin map shown below:

LAUNCHXL-F280049C pin map

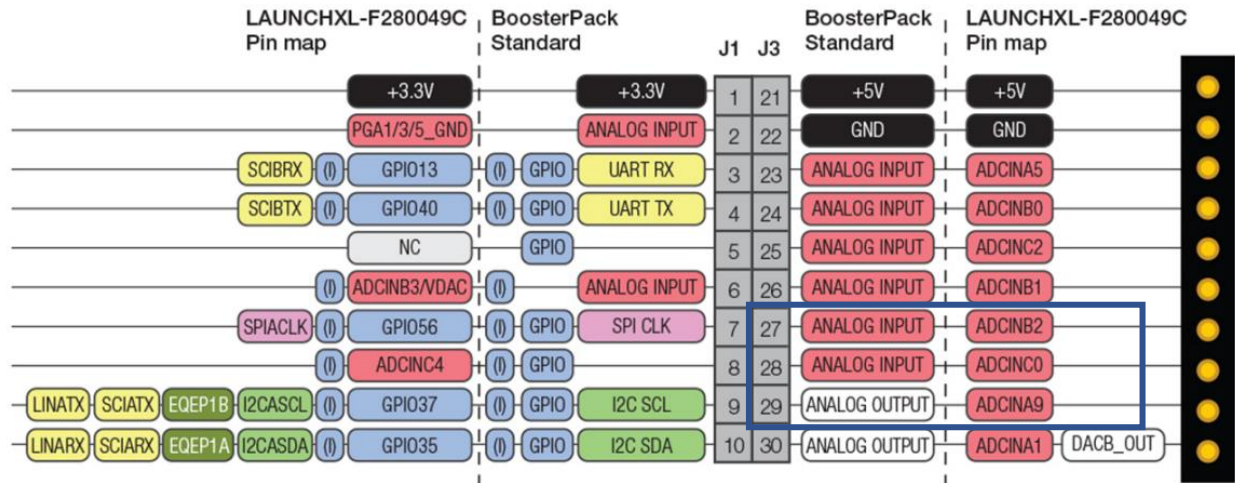


Table 3. PGA Signals and Associated Connections

BoosterPack Site	Pin Position	PGA Signal	ADC Input Signal	Note
1	J3.27	PGA1_IN	ADCINB2	Populate RC filter if required
	J3.28	PGA3_IN	ADCINC0	Populate RC filter if required
	J3.29	PGA5_IN	ADCINA9	Populate RC filter if required

As shown in the table above, J3.27 is PGA1_IN (also called ADCINB2), and should be connected to ISENC, J3.28 is PGA3_IN (also called ADCINC0) and should be connected to ISENB, and J3.29 is PGA5_IN (also called ADCINA9) and should be connected to ISENA.