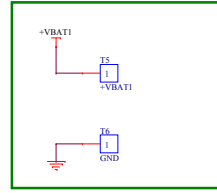
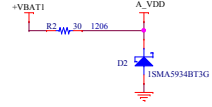
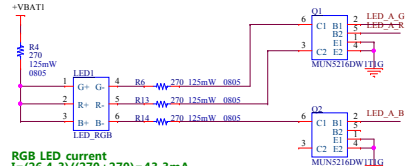
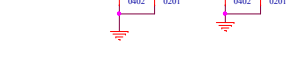
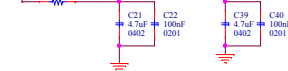
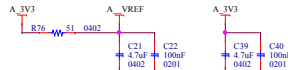
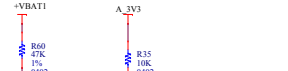
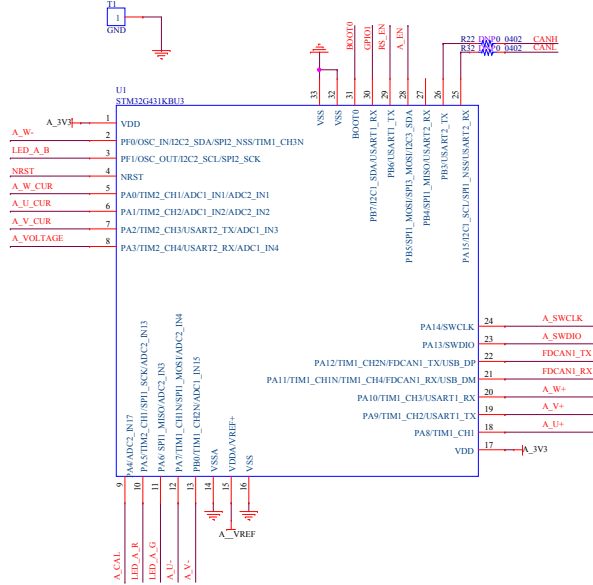
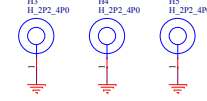
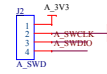


RGB LED current
 $I = (26.4 - 3) / (270 + 270) = 43.3\text{mA}$



Power cord from Power Board

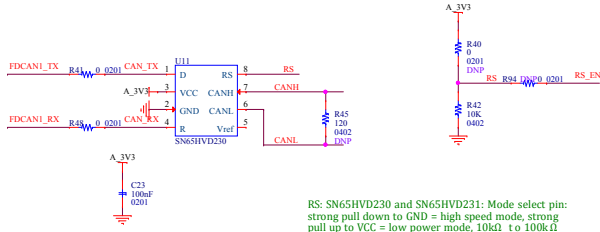
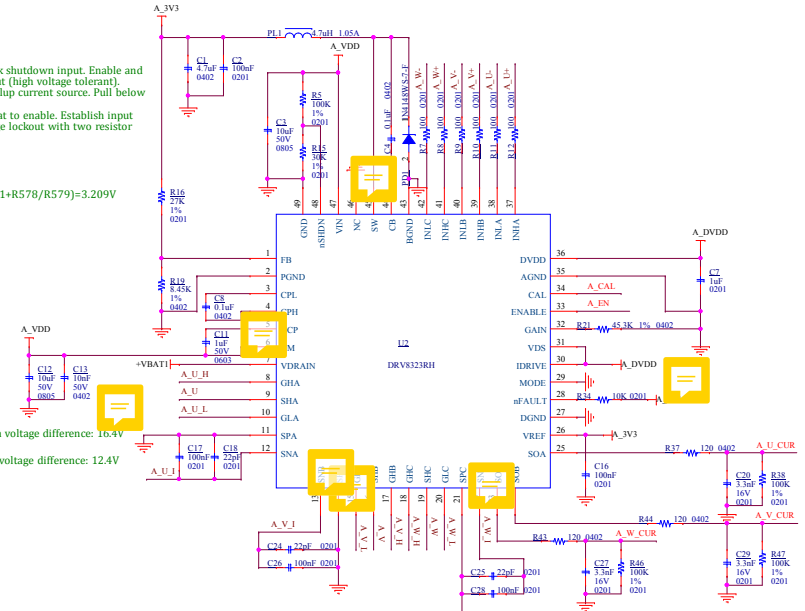


nSHDN: Buck shutdown input. Enable and disable input (high voltage tolerant). Internal pullup current source. Pull below 1.25V to disable. Float to enable. Establish input undervoltage lockout with two resistor divider.

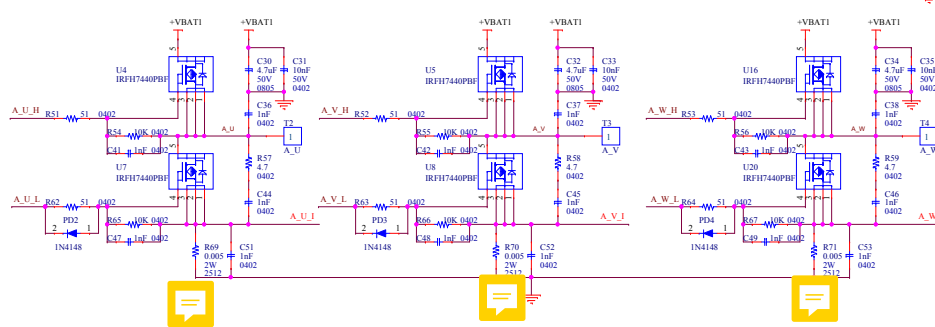
$$V_o = 0.765V \cdot (1 + R57/R59) = 3.209V$$

CPL and CPH Maximum voltage difference: 16.4V

VM and VCP Maximum voltage difference: 12.4V



RS: SN65HVD230 and SN65HVD231: Mode select pin: strong pull down to GND = high speed mode, strong pull up to VCC = low power mode, 10kΩ t to 100kΩ pull down to GND = slope control mode



5

4

3

2

1

D

D

C

C

B

B

A

A

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/10/22	Deciphered Date	2014/10/22	Title	Version Change List (P. I. R. List)
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				C	Yuneeec R8501-H-V10 MB Board
Date: Monday, December 07, 2020				Sheet	2 of 2

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3

2

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