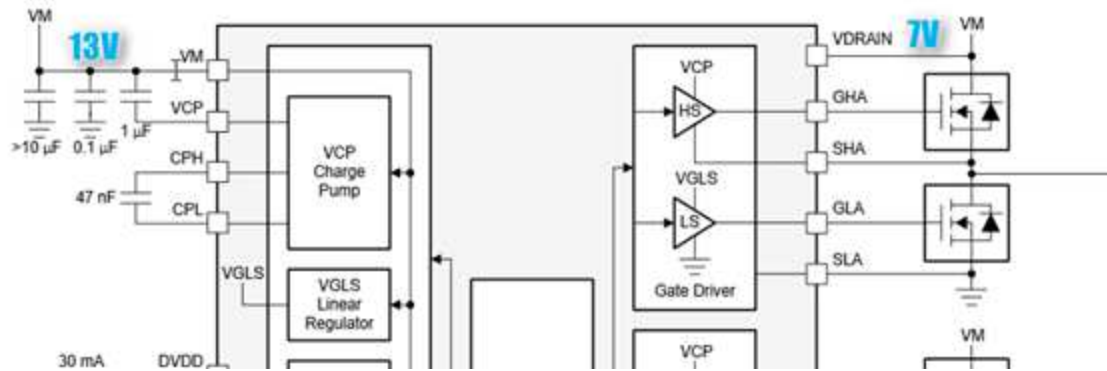


8.2 Functional Block Diagram



Electrical Characteristics (continued)

at $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, $V_{VM} = 6$ to 60 V (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
GATE DRIVERS (GHx, GLx)						
$V_{GSH}^{(1)}$	High-side gate drive voltage with respect to SHx	$V_{VM} = 13$ V, $I_{VCP} = 0$ to 25 mA	8.4	11	12.5	V
		$V_{VM} = 10$ V, $I_{VCP} = 0$ to 20 mA	6.3	9	10	
		$V_{VM} = 8$ V, $I_{VCP} = 0$ to 15 mA	5.4	7	8	
		$V_{VM} = 6$ V, $I_{VCP} = 0$ to 10 mA	4	5	6	
$V_{GSL}^{(1)}$	Low-side gate drive voltage with respect to PGND	$V_{VM} = 12$ V, $I_{VGLS} = 0$ to 25 mA	9	11	12	V
		$V_{VM} = 10$ V, $I_{VGLS} = 0$ to 20 mA	7.5	9	10	
		$V_{VM} = 8$ V, $I_{VGLS} = 0$ to 15 mA	5.5	7	8	
		$V_{VM} = 6$ V, $I_{VGLS} = 0$ to 10 mA	4	5	6	