



- NOTES:
- This circuit is needed at each BST_x to OUT_x connection
 - 1) 24k resistor R1 is used to maintain the charge on the bootstrap capacitor, and limit the current through the zener when OUT_x is low
 - 2) Zener D2 is used to prevent BST_x to OUT_x from exceeding 14V
 - 3) Optional but recommended Schottky D1 is used to prevent current flow from BST_x to PVDD+12V when GVDD_x is powered and PVDD+12V is not.
 - 4) The capacitor value C1 and resistor value can be adjusted with experimentation
 - 5) Consider adding a switch on PVDD+12V to disable the path when the DRV8432 is inactive