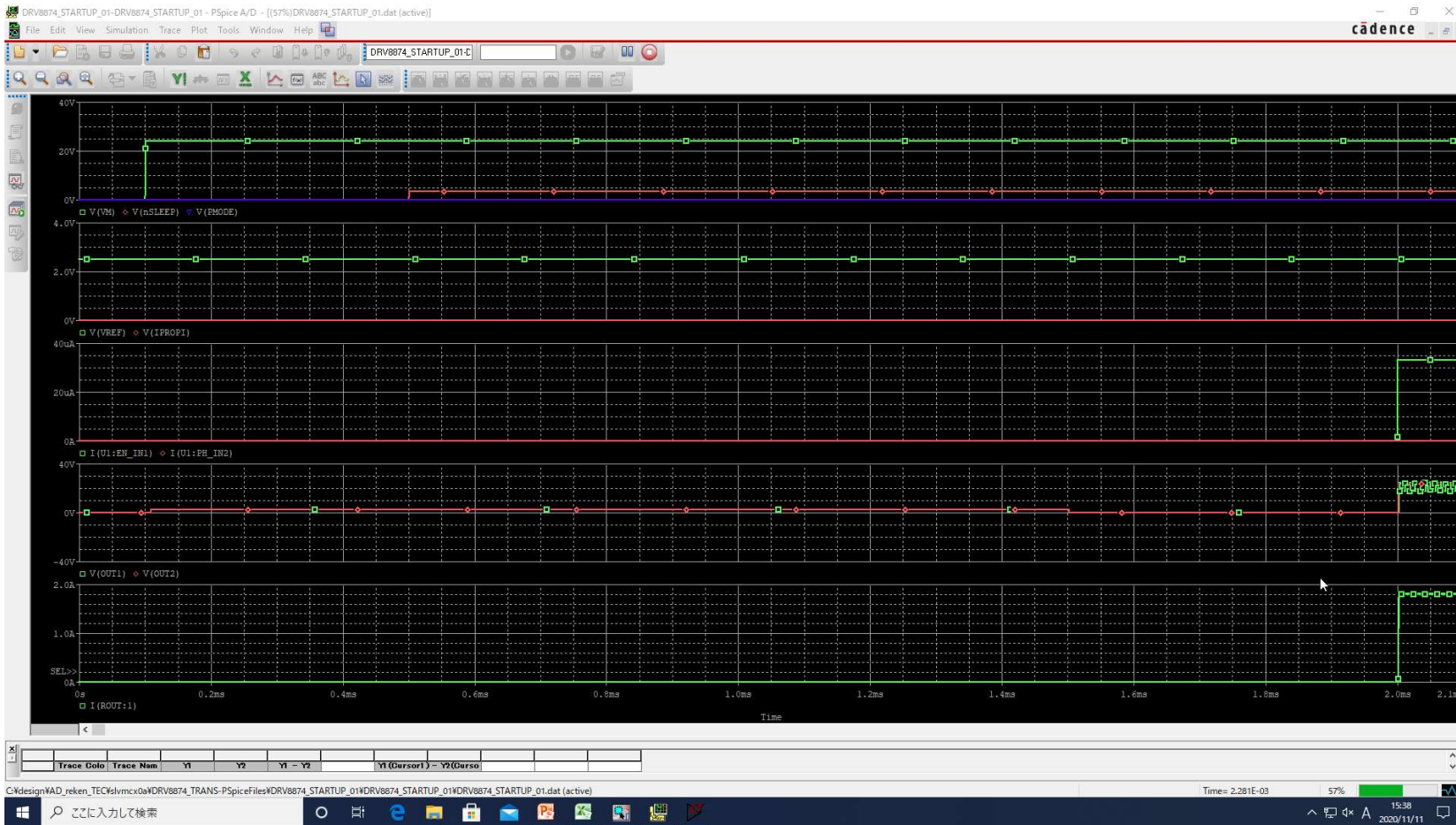


OUT2 is connected using Inductors to OUT1

Operation MODE
PMODE : PH/EN Control Mode
IMODE : Fixed Off Time
Itrip : 1A

INFO(ORNET-1156): PSpice netlist generation complete
INFO(ORCAP-2191): Creating PSpice Netlist
INFO(ORNET-1041): Writing PSpice Flat Netlist C:\design\AD_reken_TEC\slvmc\0a\DRV8874_TRANS-PSpiceFiles\DRV8874_STARTUP_01\DRV8874_STARTUP_01.net
INFO(ORNET-1156): PSpice netlist generation complete

OUT2 is connected using Inductors to OUT1/ Result



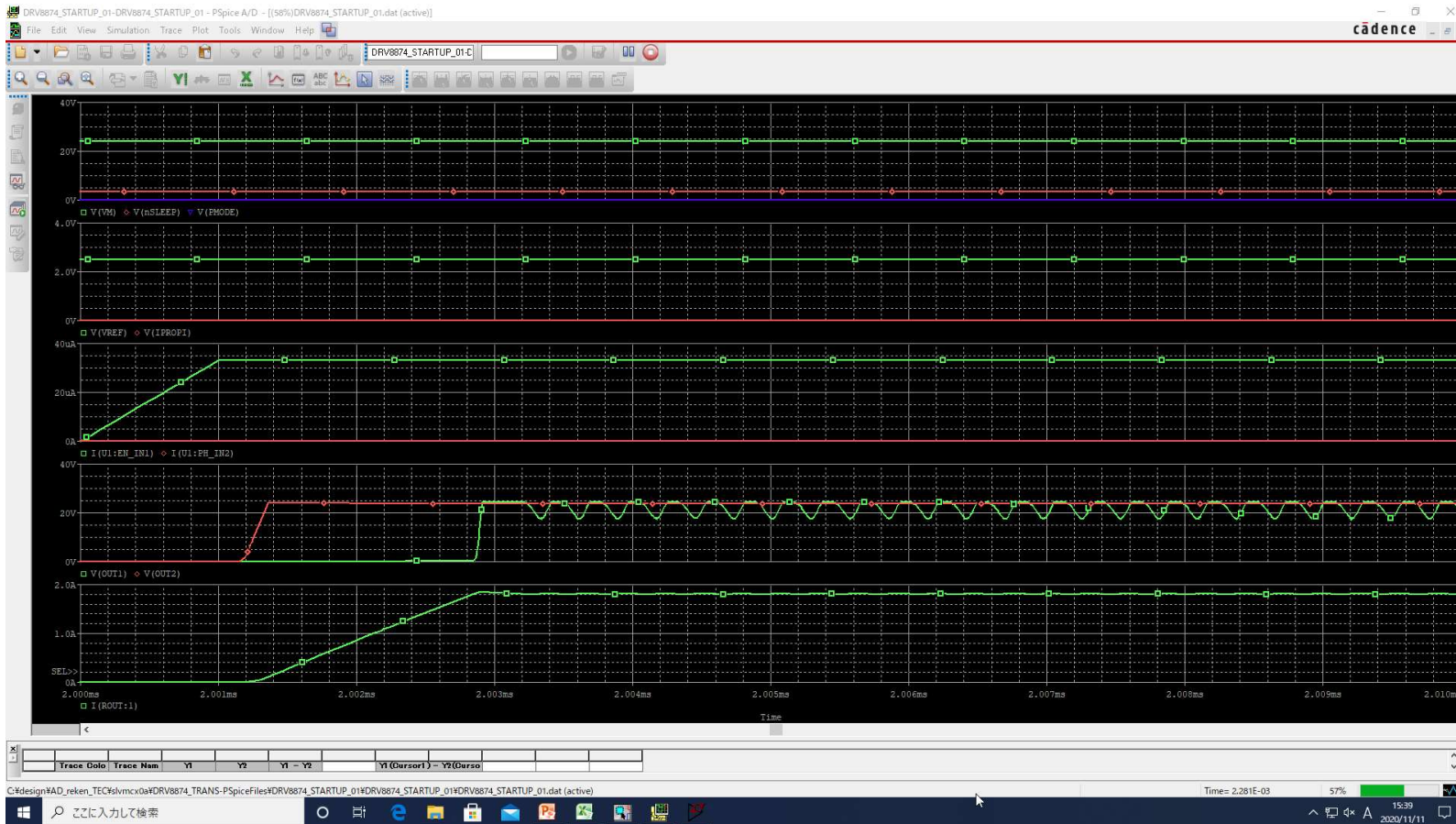
《Operating MODE》

PMODE : PH/EN Control Mode
IMODE : Fixed Off Time
Itrip : 1A

《Comments》

Wave form : 0ms ~ 2.1ms.
PH/EN is operated from 2ms.

OUT2 is connected using Inductors to OUT1/ Result



《Operating MODE》

PMODE : PH/EN Control Mode
IMODE : Fixed Off Time
Itrip : 1A

《Comments》

Wave form : 0ms ~ 2.1ms.
PH/EN is operated from 2ms.

Itrip setting is 1A.
but the current(from out2 to out1)
is about 1.8A.

the current(from out2 to out1) is
different from Itrip setting.

《Questions》

1. Is this simulation result is correct? Would you tell me the problems about the design.
2. Why is it different from the current(from out2 to out1) and Itrip setting ?