

Delay after FAULTn assertion

Notebook: Automatic Hen Door Opener

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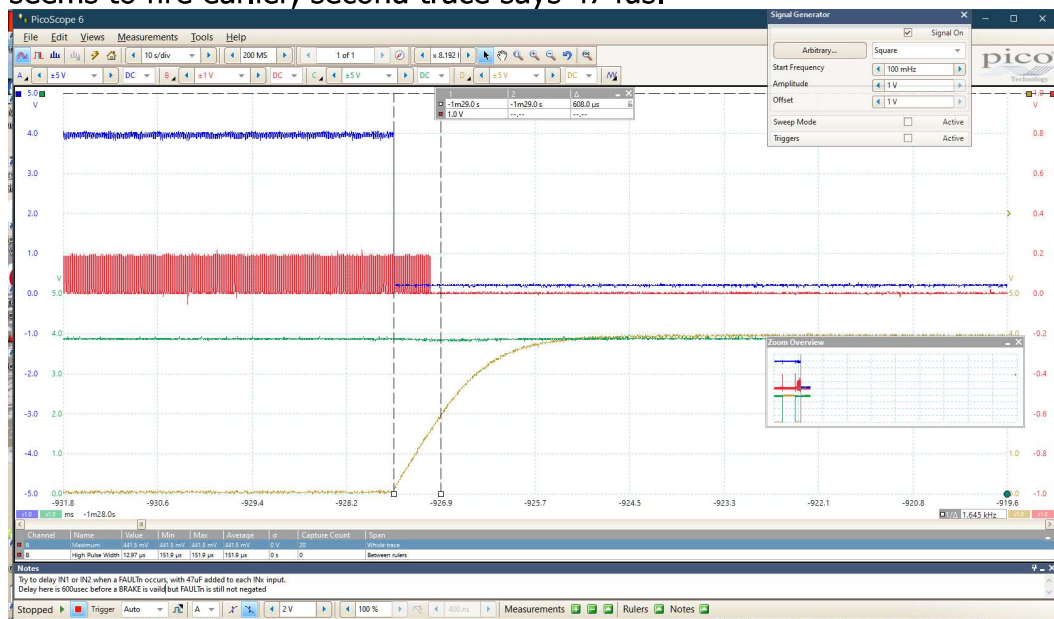
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Delay after FAULTn assertion

Adding to delay IN1 or IN2 when a FAULTn occurs, with 47uF added to each INx input.

Delay here is 600usec before a Vin should be valid (0.5 x Vcc) but it seems to fire earlier, second trace says 474us.



This second trace also has 47uF added to preceding NAND gates inputs, but this makes no difference as FAULTn is one signal forcing IN1 and IN2 HIGH.

