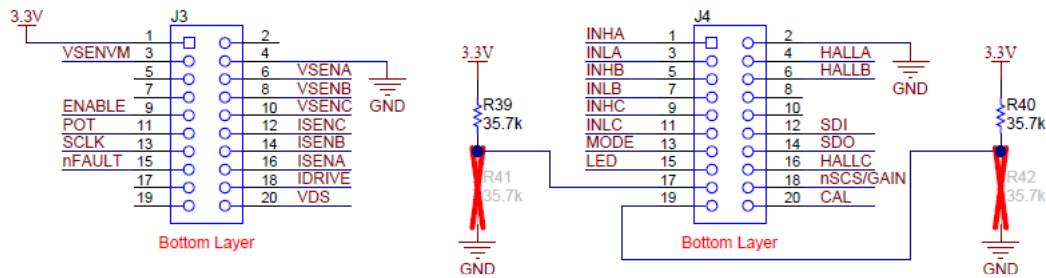


BOOSTERPACK-DRV8323RS Headers



LAUNCHXL-F28049C Headers



BoosterPack Headers Site 1 (Top)

Break some connections below between boosterpack-drv8323rs and launchxl-f280049c headers.

launchxl-f280049c			boosterpack-drv8323rs	
J1-2	PGA135_GND	X	J3-3	VSENVN
J1-8	ADCINC4	X	J3-15	nFAULT
J3-29	ADCINA9-P5I	X	J3-18	IDRIVE
J3-30	ADCINA1	X	J3-20	VDS

Use wires to connect the two-pin of launchxl-f280049c, or the pin of launchxl-f280049c to the pin of boosterpack-drv8323rs

J1-4/GPIO40 (launchxl-f280049c) connect to **J1-5** (launchxl-f280049c)

J1-8/ADCINC4 (launchxl-f280049c) connect to **J1-9/GPIO37_BP** (launchxl-f280049c)

J4-12/GPIO23 (launchxl-f280049c) connect to **J4-19/GPIO57** (launchxl-f280049c)

J3-29/ ADCINA9-P5I (launchxl-f280049c) connect to **J3-3/VSENVN** (boosterpack-drv8323rs)

Set the switch **S8 (UART SELECT)** on launchxl-f280049c to **HIGH (28/29)** site

Set the switch **S6 (28/29 ROUTE)** on launchxl-f280049c to **HIGH (BP)** site