

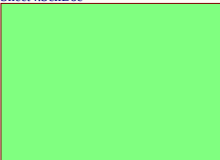
U_Sheet2
Sheet2.SchDoc



U_Sheet2
Sheet3.SchDoc



U_Sheet4
Sheet4.SchDoc



BLOCK DIAGRAM

REVISION HISTORY

DATE	VERSION	COMMENTS
11/01/25	Rev.A	Preliminary Release

Title : OEPL_PTU_DR_0225_V100			OPTIMIZED ELECTROTECH	
Size: B	Code: PTU	Revision: Rev.A	Unit# 107, brigade IRV Center Nallurhalli, Whitefield Bangaluru 560066	
Date: 18-03-2025	Time: 15:10:39	Sheet 1 of 4		
Drawn by : Tomin		Checked by : Kiran	Approved by : Dharin	



CON3

MH1

1 P BOUT2

2 P BOUT1

3 P AOUT2

4 P AOUT1

MH2

105311-1104

GND

CON2

MH1

1

2

3

4

MH2

105311-1104

T BOUT2

T BOUT1

T AOUT2

T AOUT1

GND

Diagram of the 501331-1407 connector. The connector is a 14-pin header labeled 'CON5' and '501331-1407'. The connections are as follows:

- Pin 1: 3V3 (T_SCLK)
- Pin 2: T_SCLK
- Pin 3: 3
- Pin 4: T_SDATO
- Pin 5: T_SDATI
- Pin 6: T_SCS
- Pin 7: T_nFAULT
- Pin 8: T_nSTALL
- Pin 9: T_RESET
- Pin 10: T_nSLEEP
- Pin 11: T_DIR/AIN2
- Pin 12: T_EN
- Pin 13: 13
- Pin 14: T_STEP/AIN1

The connector is connected to a microcontroller (MP1, MP2) and a ground symbol.

