

MOTOR PSU

The diagram illustrates a MOTOR PSU circuit. It features a MOTOR SUPPLY input connected to a network of six 100uF/63V capacitors (C18, C22, C23, C24, C25, C26) and a 0E resistor (R9). The output is connected to a VM terminal. The circuit is powered by a UC5082-V2 IC, which is connected to a 0E resistor (R9) and a 0E resistor (R9). The circuit is also connected to a 0E resistor (R9) and a 0E resistor (R9).

MOTOR DRIVER INTERFACE

The diagram illustrates the motor driver interface for three 74X595 shift registers (U5, U6, U7) connected to a microcontroller. The microcontroller pins are 595-DIND, 595-SCK, 595-CS, and 595-END. Each shift register is powered by +3.3V and GND, with a 100nF decoupling capacitor (C19, C20, C21). The outputs of the shift registers are connected to motor driver pins: M2-1, M1-1, M0-1, EN-1, DIR-1, DCY-1, SLP-1, RST-1 for U5; M2-2, M1-2, M0-2, EN-2, DIR-2, DCY-2, SLP-2, RST-2 for U6; and M2-3, M1-3, M0-3, EN-3, DIR-3, DCY-3, SLP-3, RST-3 for U7. The 595-Q-EX pin is also shown.

74X595 U5

Pin	Signal
16	VCC
8	GND
14	SER
11	SCK
10	SCL
12	RCK
13	G
15	QA
1	QB
2	QC
3	QD
4	QE
5	QF
6	QG
7	QH
9	QH

74X595 U6

Pin	Signal
16	VCC
8	GND
14	SER
11	SCK
10	SCL
12	RCK
13	G
15	QA
1	QB
2	QC
3	QD
4	QE
5	QF
6	QG
7	QH
9	QH

74X595 U7

Pin	Signal
16	VCC
8	GND
14	SER
11	SCK
10	SCL
12	RCK
13	G
15	QA
1	QB
2	QC
3	QD
4	QE
5	QF
6	QG
7	QH
9	QH

595-Q-EX

595-DIND
595-SCK
595-CS
595-END

100nF C19
100nF C20
100nF C21

3.3V
GND

M2-1
M1-1
M0-1
EN-1
DIR-1
DCY-1
SLP-1
RST-1

M2-2
M1-2
M0-2
EN-2
DIR-2
DCY-2
SLP-2
RST-2

M2-3
M1-3
M0-3
EN-3
DIR-3
DCY-3
SLP-3
RST-3

[illegible]

1. DRV8825 NEEDS PCB HEATSINK.
 2. BOTH PHASE HAVE SAME VREF FOR I-SENSE.
 3. V3P0UT NOT USED.
 4. DVDD CAN BE 5V OR 3.3V.

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 3) V3P3OUT NOT USED.
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