

# MOSFET CONTROLLER FOR X-AXIS H-BRIDGE

**CAD Note:**  
Minimize the loop length for the high-side and low-side gate drivers.

**CAD Note:**  
Do not connect the SLx pins directly to GND. Instead, use dedicated traces to connect these pins to the sources of the low-side external MOSFETs. These recommendations allow for more accurate VDS sensing of the external MOSFETs for overcurrent detection

**CAD Note:**  
Place this capacitor as near to the VM pin as possible with a thick trace or ground plane connected to the GND pin.

**CAD Note:**  
Place this capacitor DVDD as near to the pin as possible and minimize the path from the capacitor to the GND

