

Figure 1 Voltage waveform on the one output of the DRV8432 bridge after LC filter (upper trace), 4 Ω load (connected all the time), 40Vpp. ~FAULT pin (bottom trace) is high.

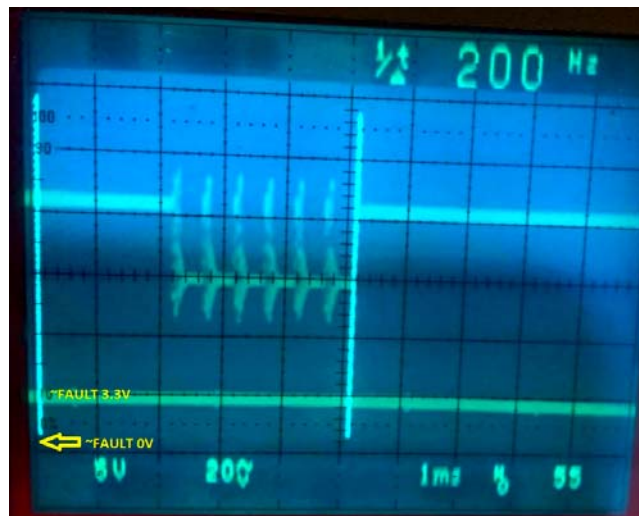


Figure 2 Voltage waveform on the output of the DRV8432 bridge after LC filter (upper trace), 4 Ω load, additional 0.1 Ω load is connected. ~FAULT (bottom trace) pin is high.



Figure 3 Screen shot Figure 2 with 1/10 time base.

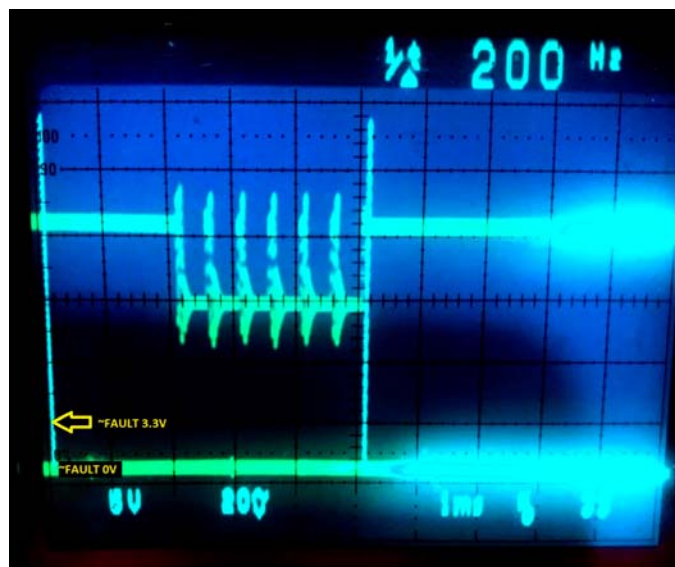


Figure 4 Voltage waveform on the one output of the DRV8432 bridge after LC filter (upper trace), 4 Ω load, additional 0.1 Ω load is connected for a longer time. ~FAULT (bottom trace) pin becomes low.

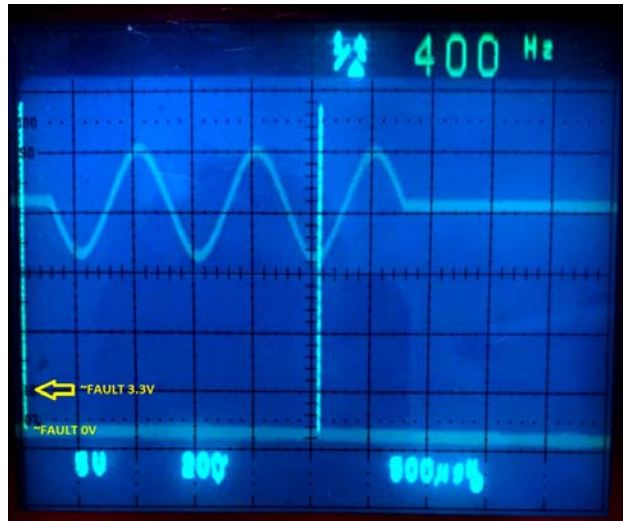


Figure 5 Voltage waveform on the one output of the DRV8432 bridge after LC filter (upper trace), 4 Ω load, 0.1 Ω load is removed. DRV8432 resumes normal operation, \sim FAULT pin (bottom trace) remains low.

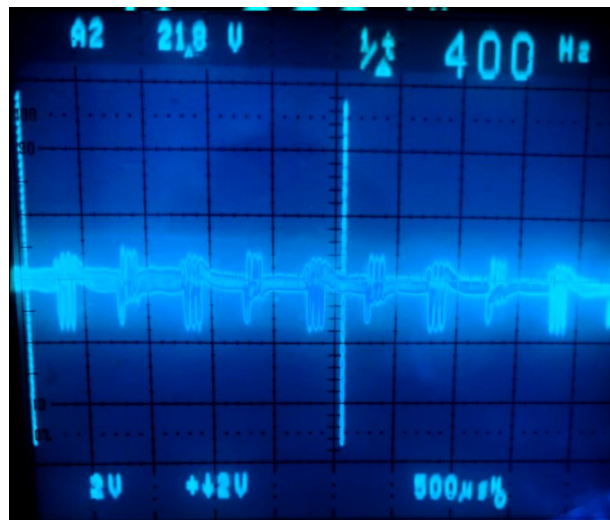


Figure 6 Current limiting at work. Voltage waveform across 0.1 Ω load, about 2V peak (20A peak)