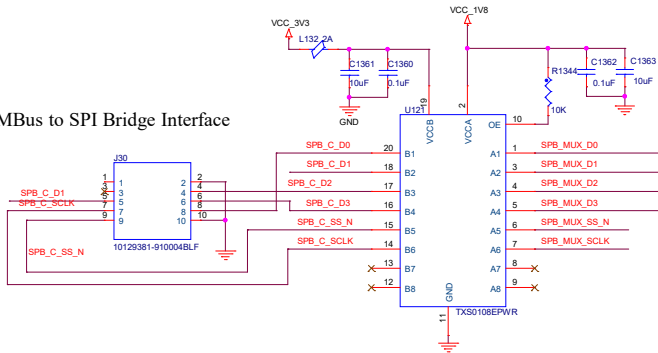
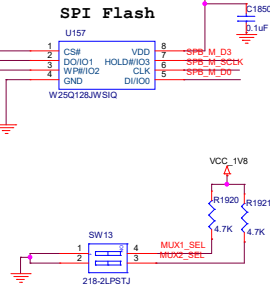
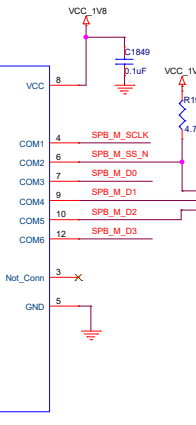
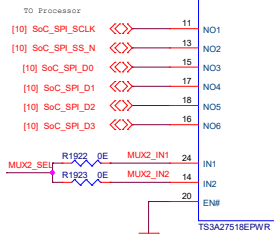
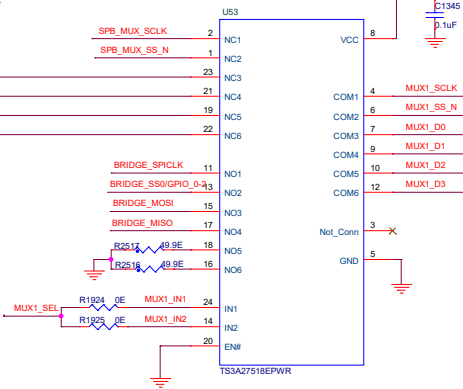
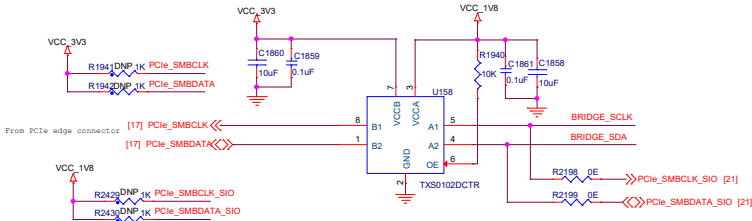
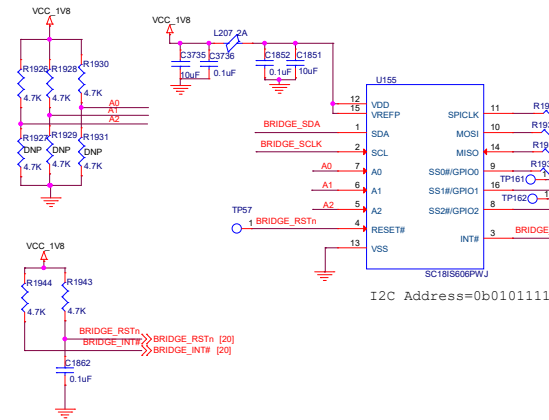


PCIe SMBus to SPI Bridge Interface



PCIe I2C to SPI Bridge Interface



SPI Selection Switch		Selected SPI Option
MUX1_SEL SW.1	MUX2_SEL SW.2	
1	1	Standard Header to QSPI Flash Memory Programming
0	1	I2C-SPI Bridge to QSPI Flash Memory Programming
IDC	0	SoC Boot from QSPI Flash Memory

Switch ON :1
Switch OFF :0
Note: 150 I don't care

A