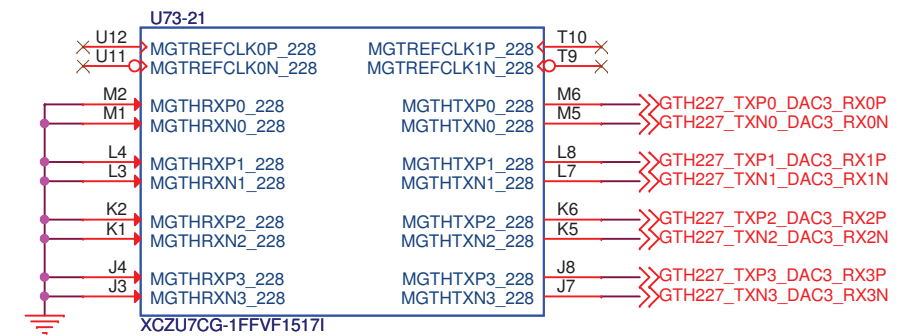
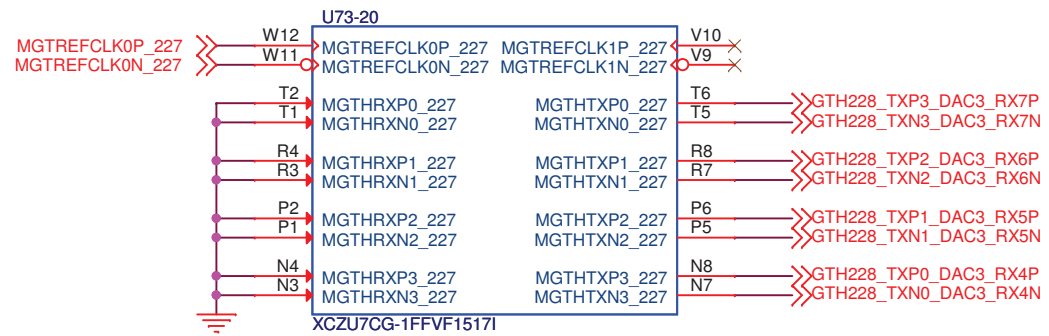
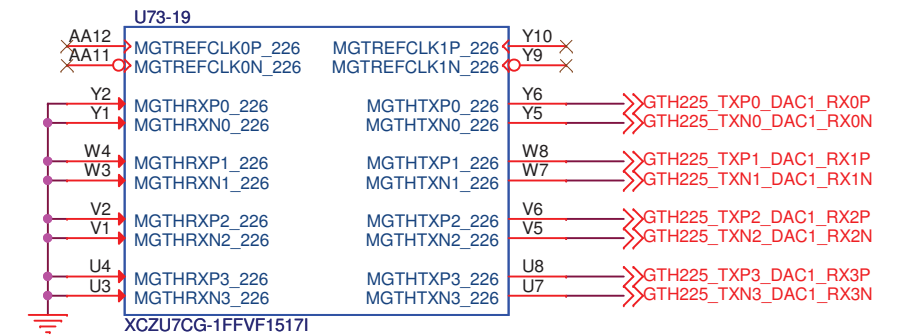
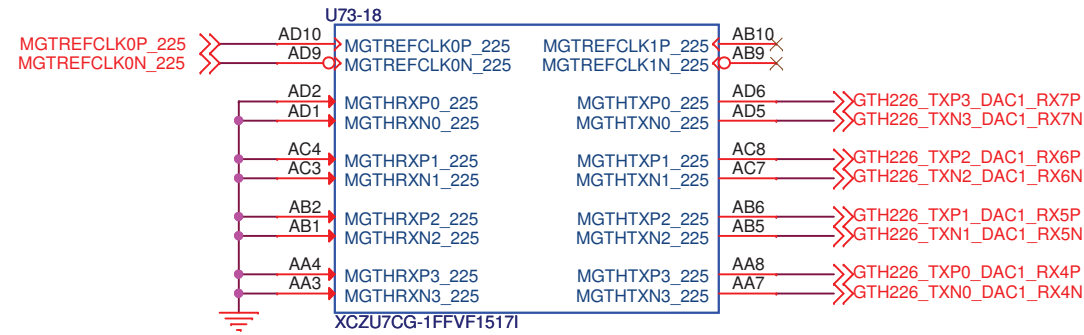
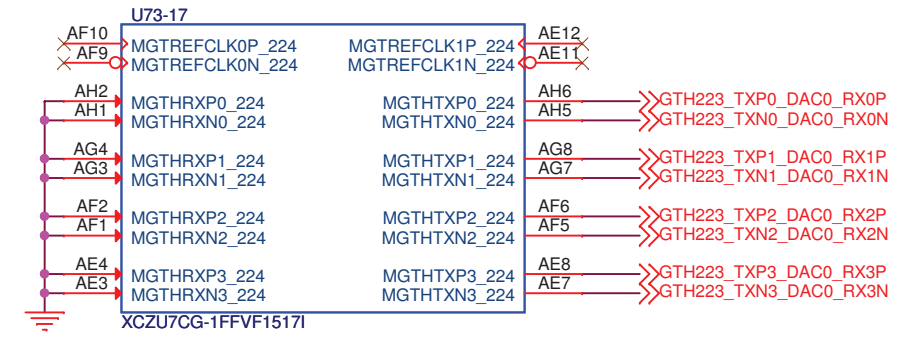
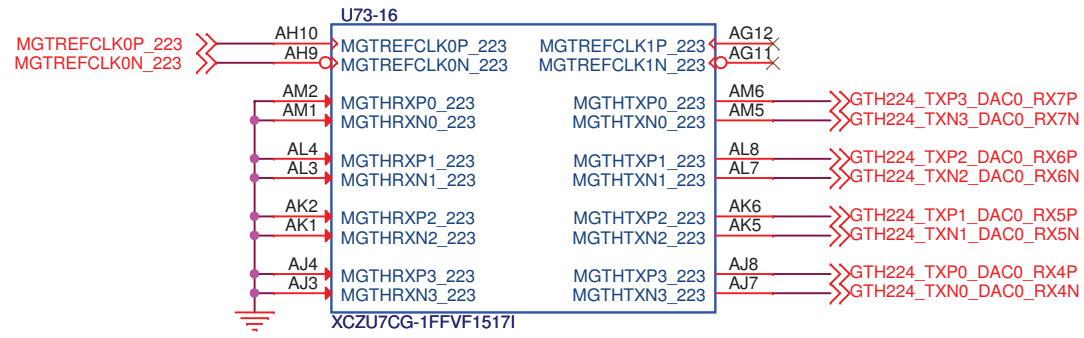
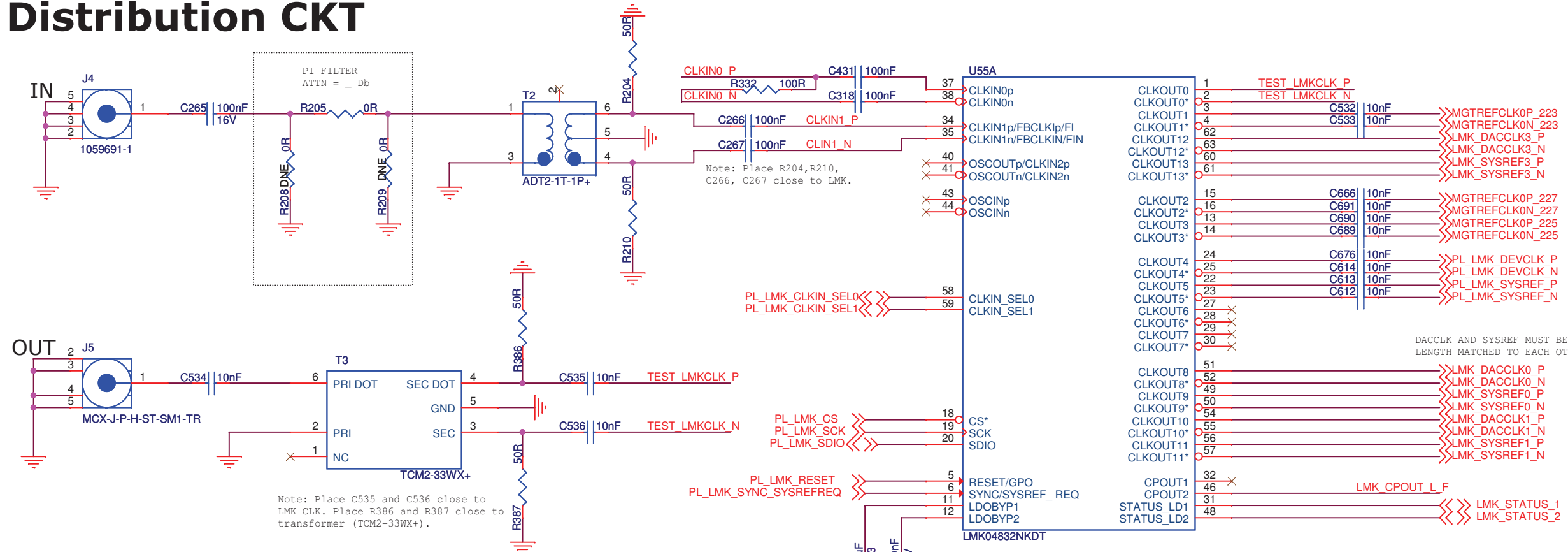


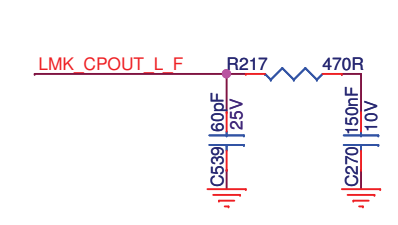
PL MGT BANKS



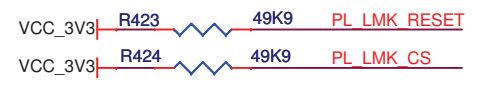
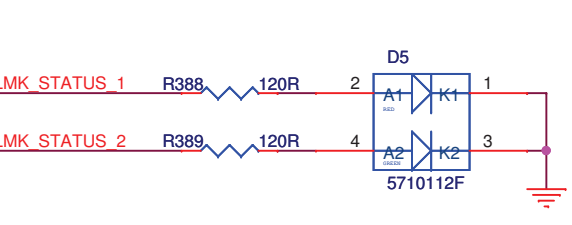
Clock Distribution CKT



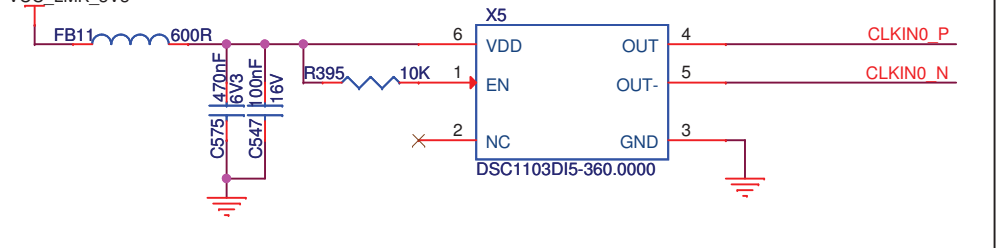
LOOP FILTER



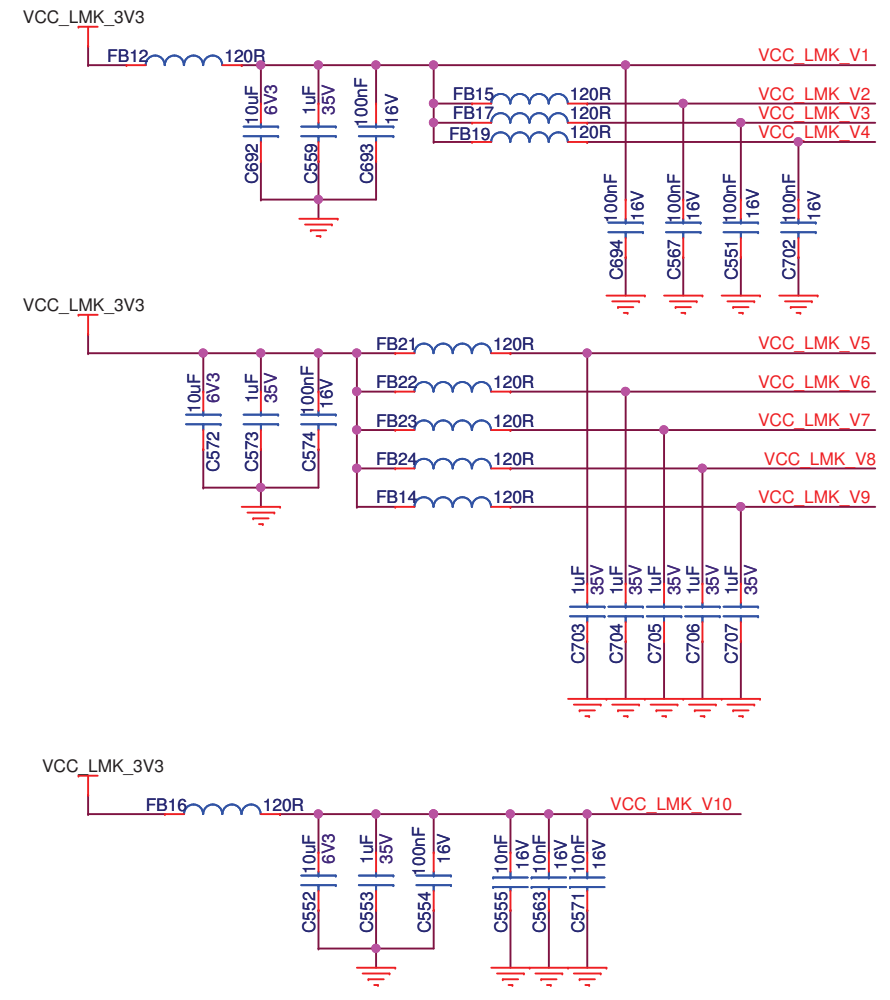
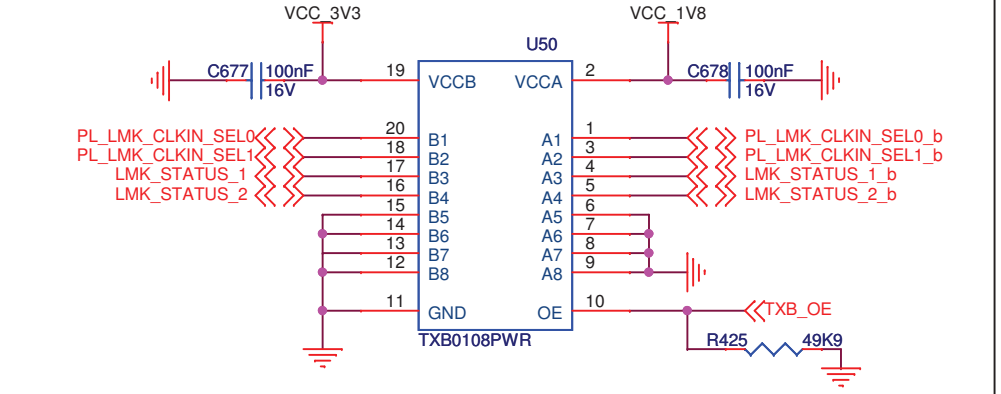
STATUS LED



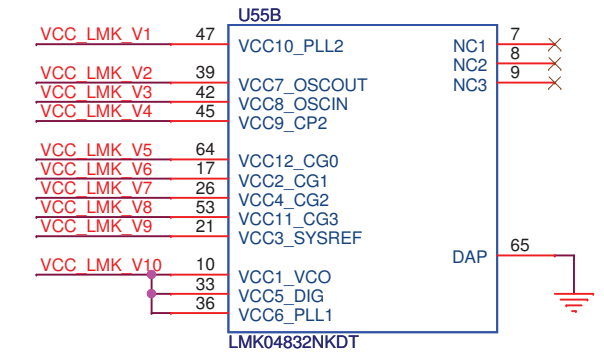
360 Mhz LVDS Clock



BIDIRECTIONAL BUFFER

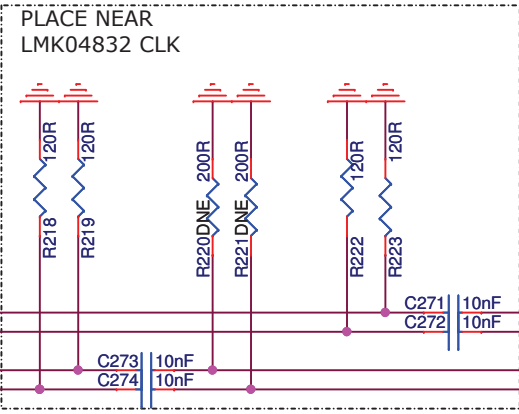


Power

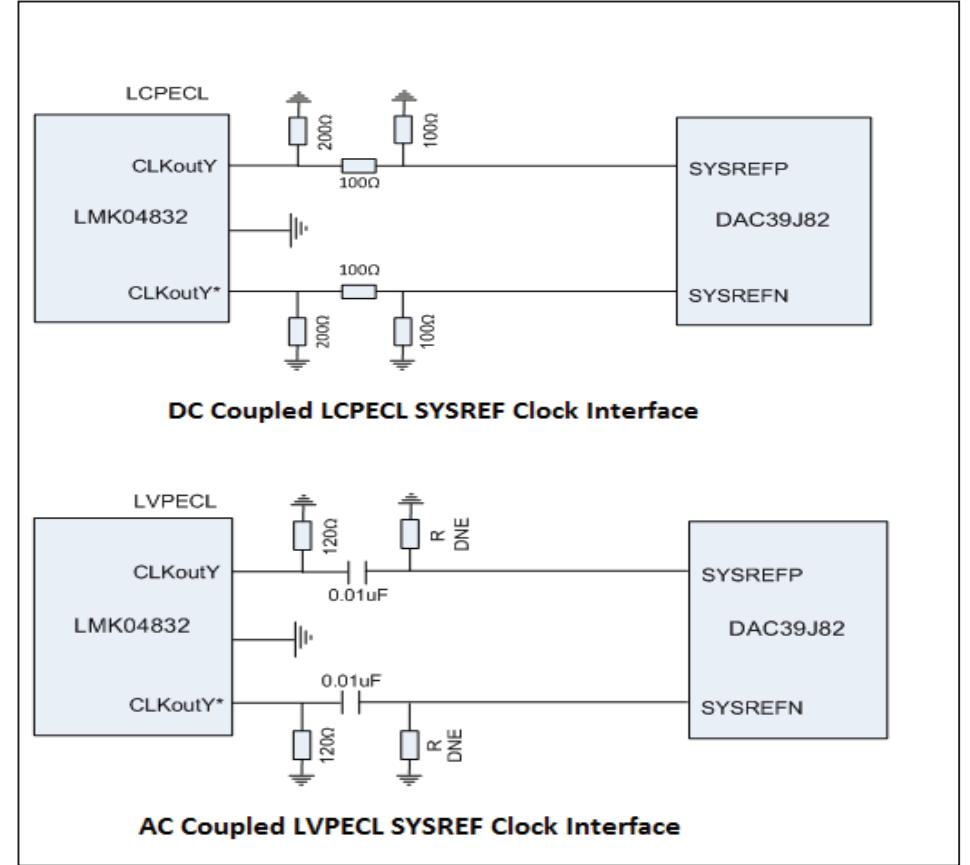
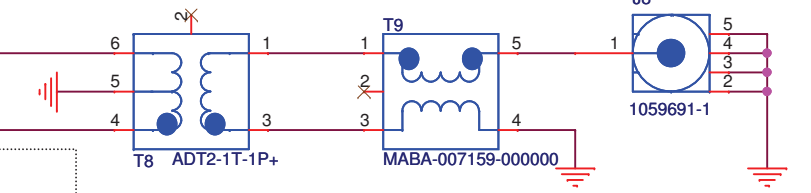
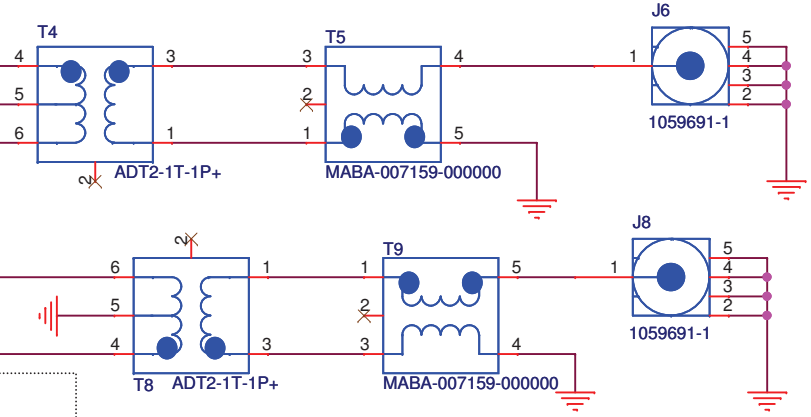
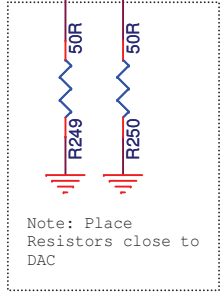
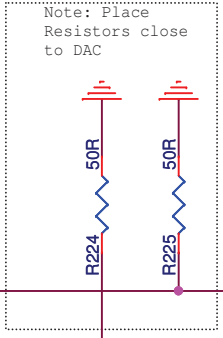
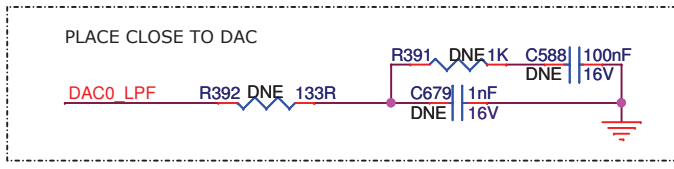
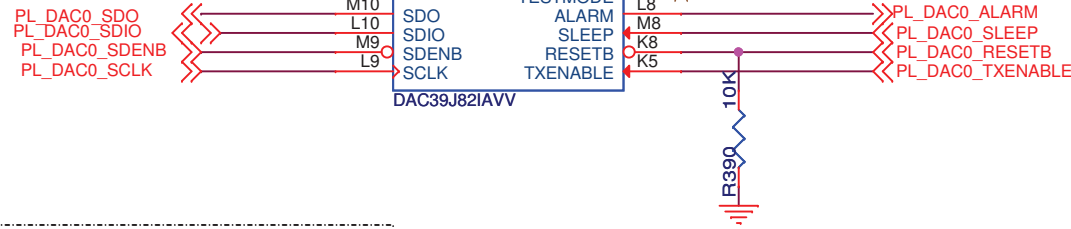


DAC0 WB, NB

DAC WB, IOUTAP/N: 407.5 ± 30 MHz, NB
 DAC IOUTDP/N: 70MHz ± 2.5 MHz.

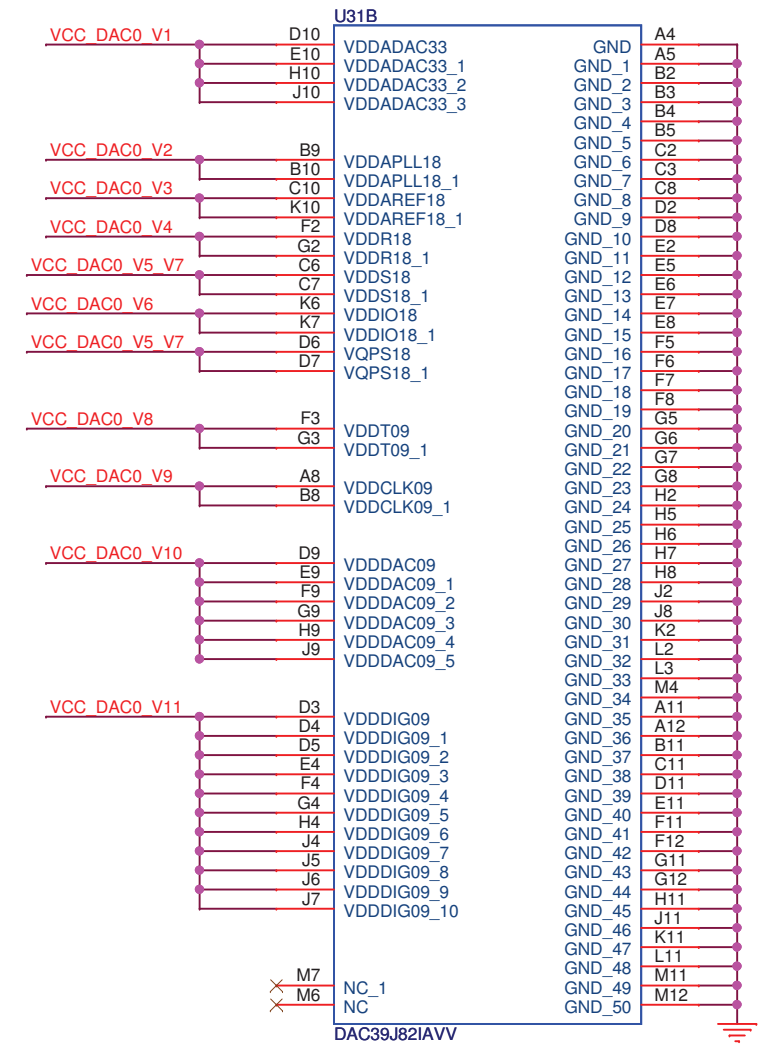
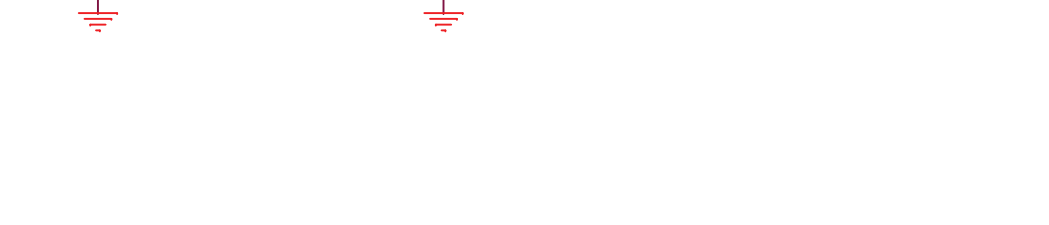
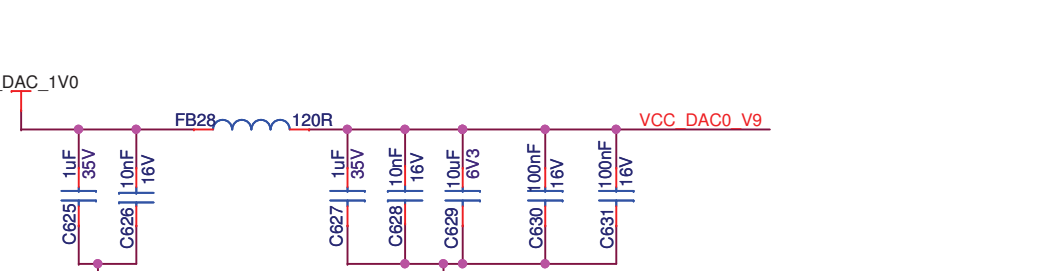
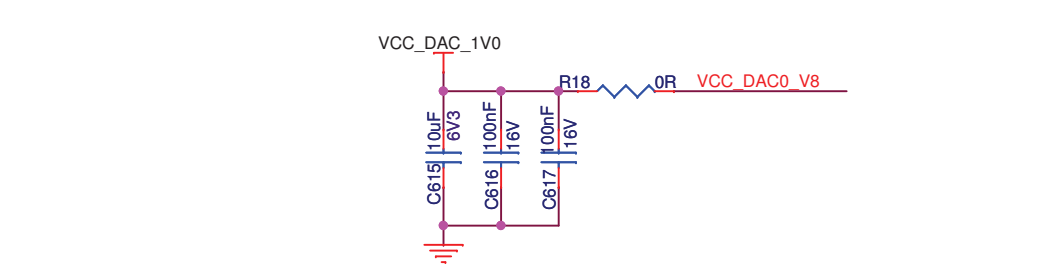
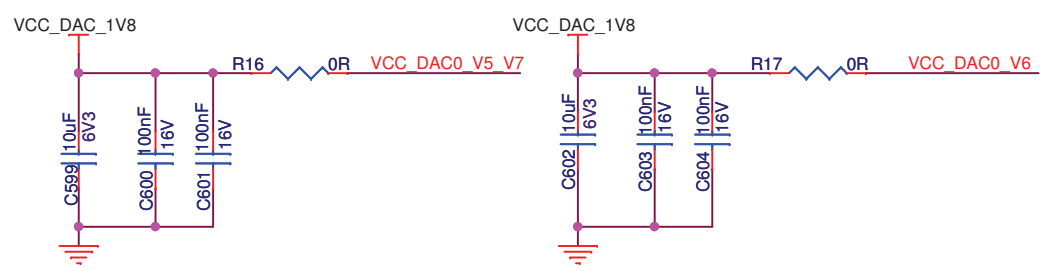
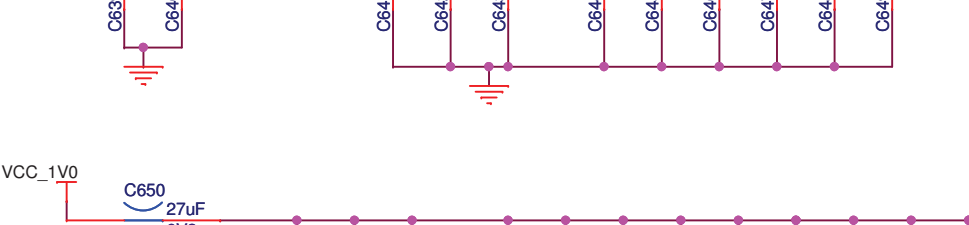
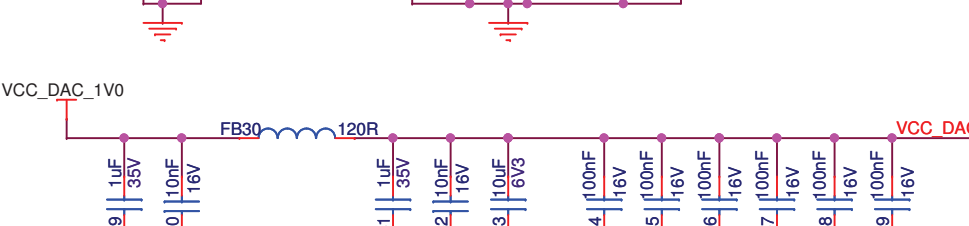
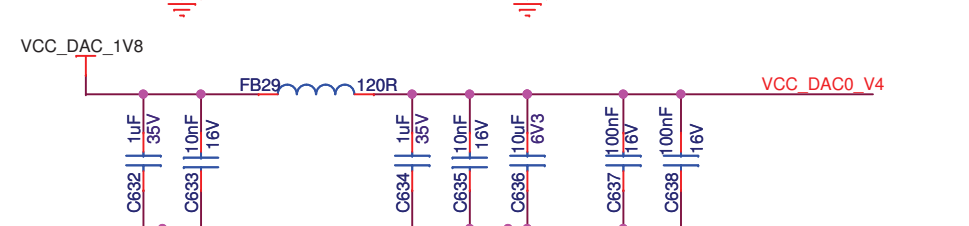
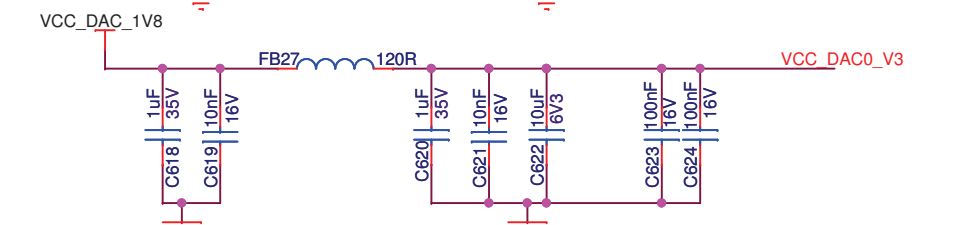
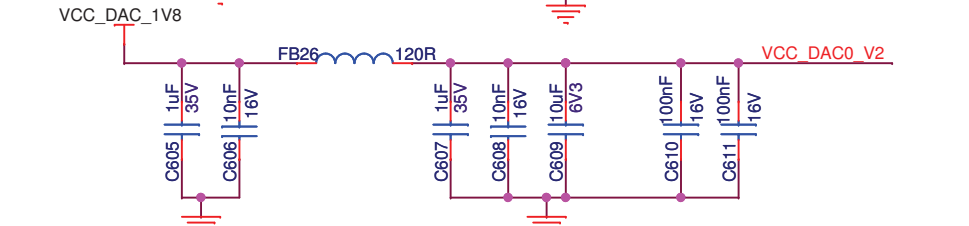
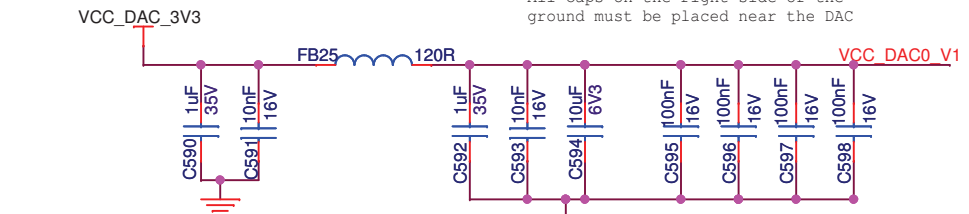


GTH223_TXP0_DAC0_RX0P	C275	100nF	G1	RX0P
GTH223_TXN0_DAC0_RX0N	C276	100nF	H1	RX0N
GTH223_TXP1_DAC0_RX1P	C277	100nF	K1	RX1P
GTH223_TXN1_DAC0_RX1N	C278	100nF	J1	RX1N
GTH223_TXP2_DAC0_RX2P	C279	100nF	M1	RX2P
GTH223_TXN2_DAC0_RX2N	C280	100nF	M2	RX2N
GTH223_TXP3_DAC0_RX3P	C281	100nF	M3	RX3P
GTH223_TXN3_DAC0_RX3N	C282	100nF	M2	RX3N
GTH224_TXP0_DAC0_RX4P	C284	100nF	F1	RX4P
GTH224_TXN0_DAC0_RX4N	C285	100nF	E1	RX4N
GTH224_TXP1_DAC0_RX5P	C286	100nF	C1	RX5P
GTH224_TXN1_DAC0_RX5N	C287	100nF	D1	RX5N
GTH224_TXP2_DAC0_RX6P	C288	100nF	B1	RX6P
GTH224_TXN2_DAC0_RX6N	C289	100nF	A1	RX6N
GTH224_TXP3_DAC0_RX7P	C290	100nF	A3	RX7P
GTH224_TXN3_DAC0_RX7N	C291	100nF	A2	RX7N



All Caps on the right side of the ground must be placed near the DAC

WB/NB DAC0 POWER



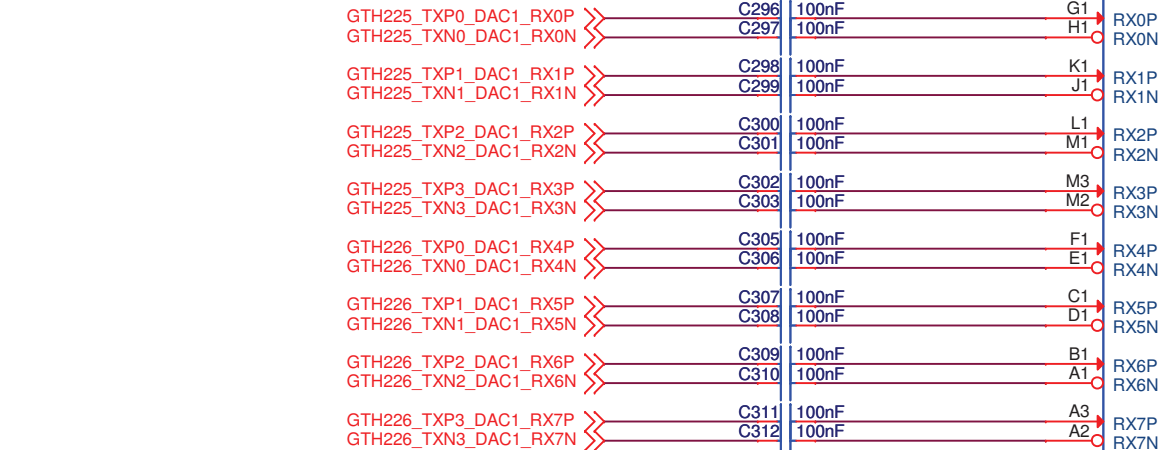
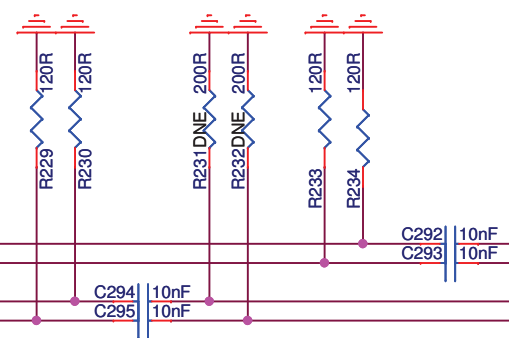
DAC LO 1

DAC LO 1 analog out -
OUTAP/N: 223.5 - 311.4 MHz

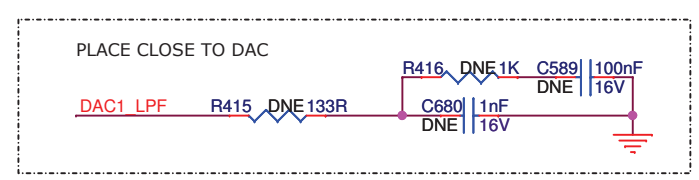
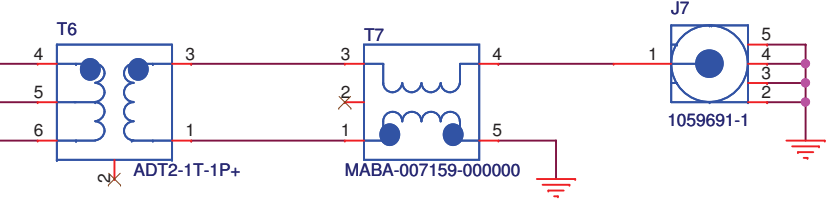
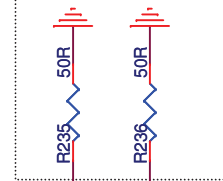
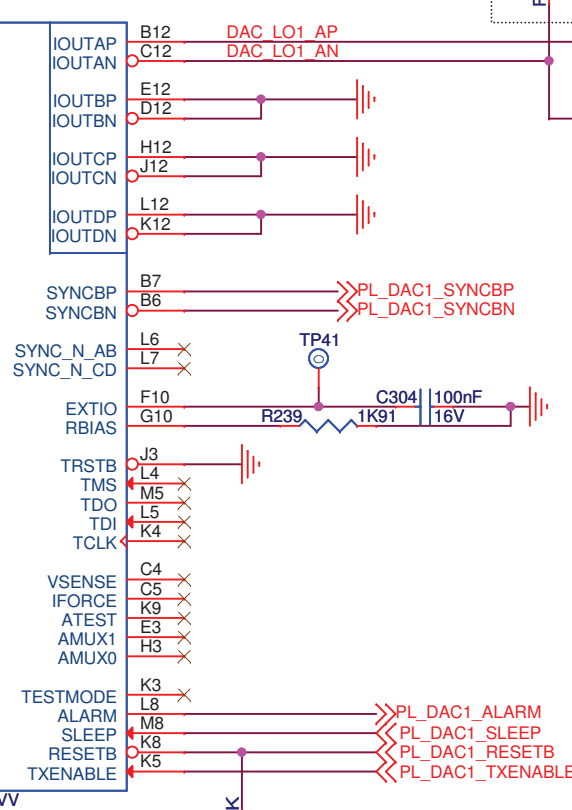
Note: Place
Resistors close to
DAC

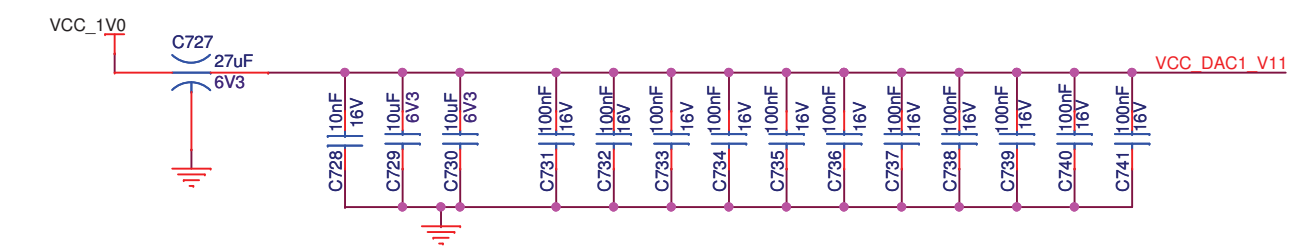
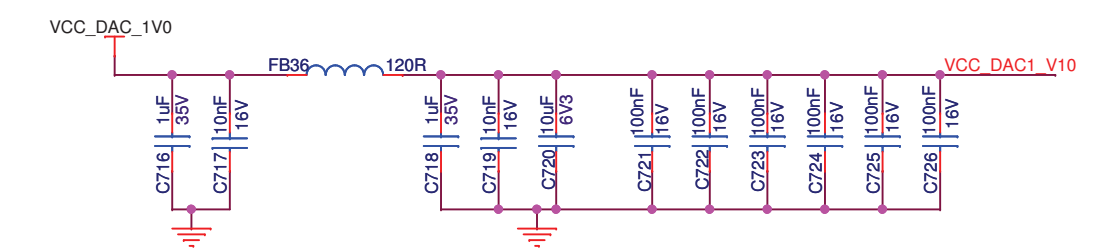
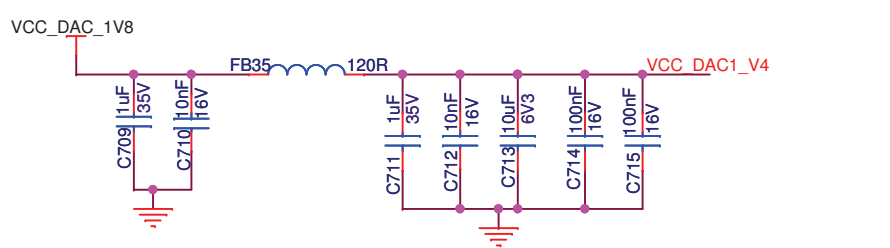
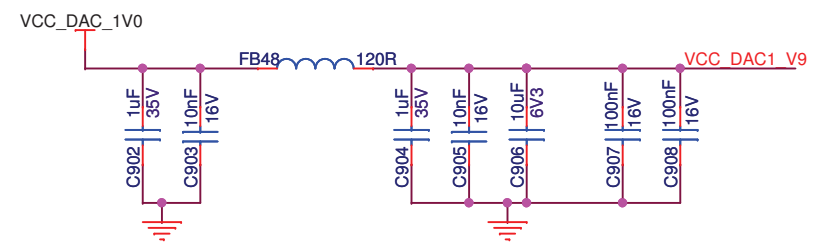
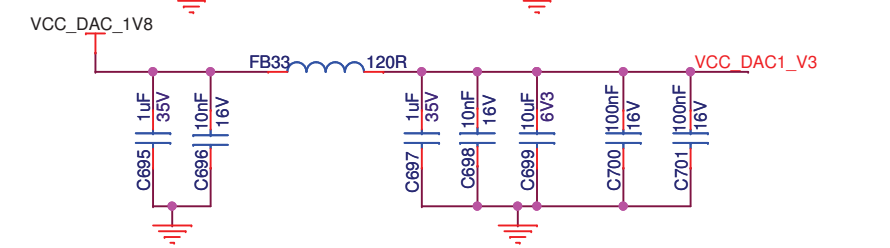
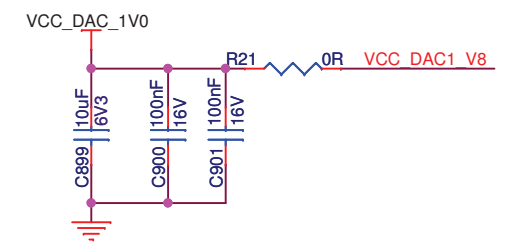
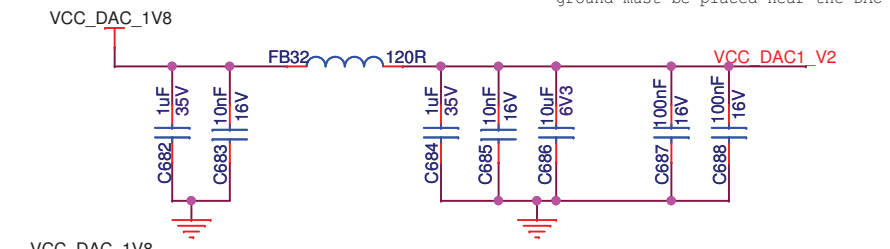
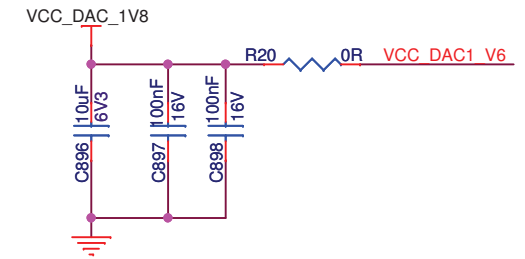
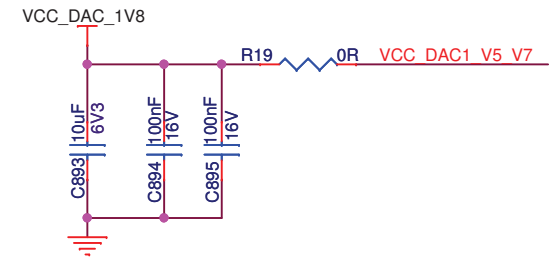
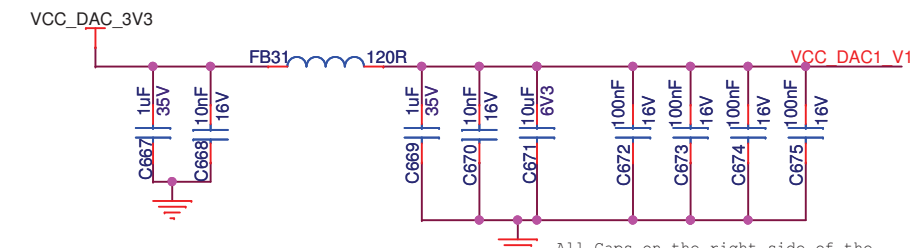
PLACE NEAR
LMK04832 CLK

LMK_DACCLK1_N
LMK_DACCLK1_P
LMK_SYSREF1_P
LMK_SYSREF1_N



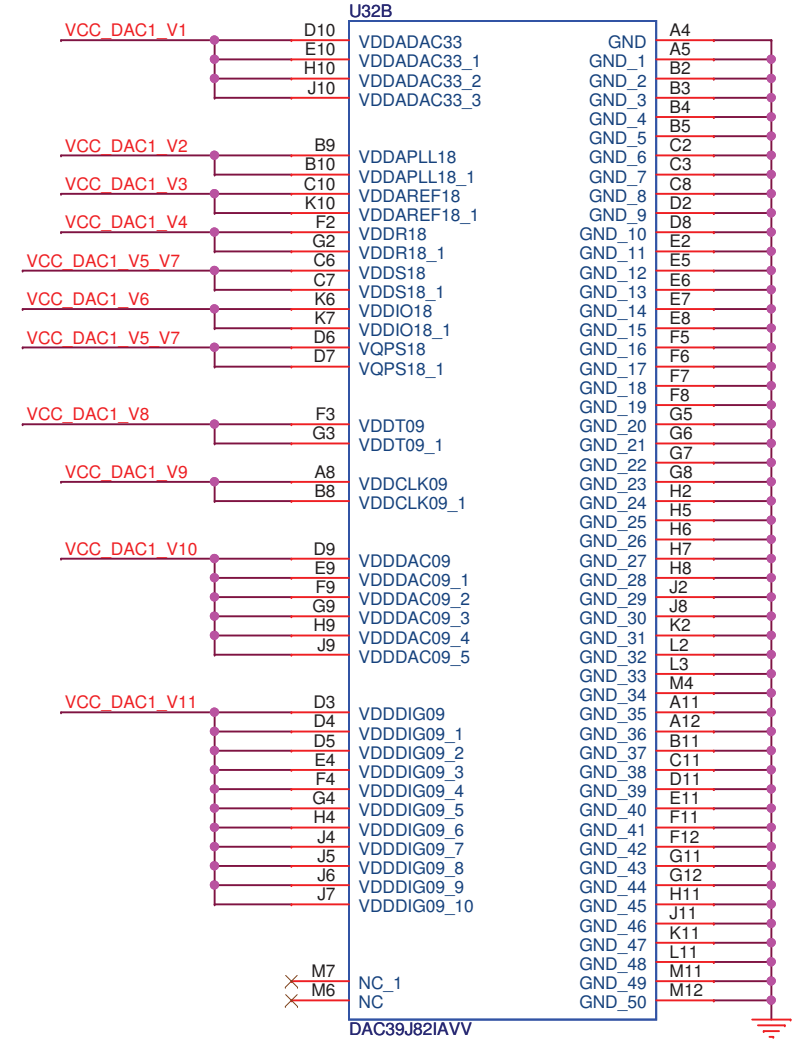
PL_DAC1_SDO
PL_DAC1_SDIO
PL_DAC1_SDENB
PL_DAC1_SCLK





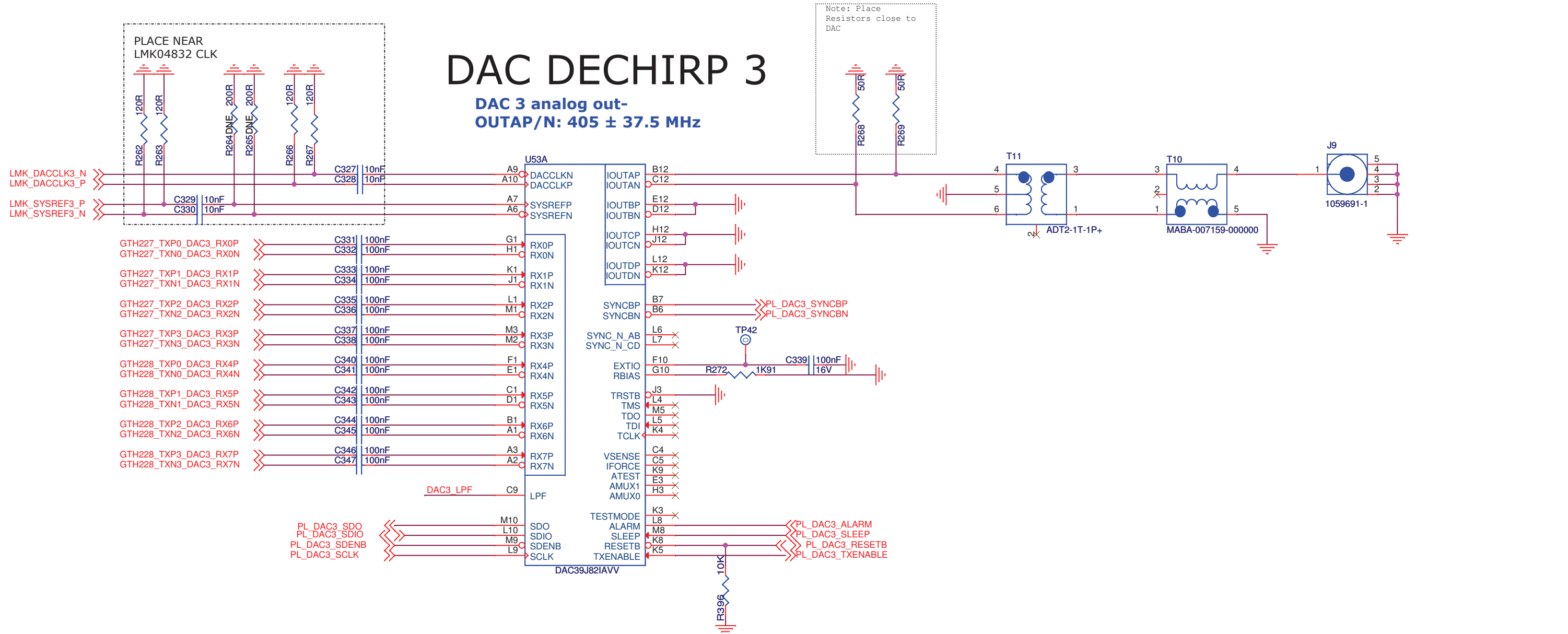
All Caps on the right side of the ground must be placed near the DAC

LO DAC1 POWER

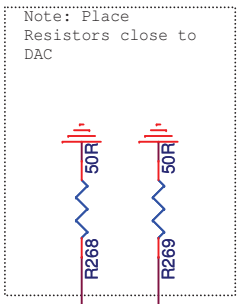
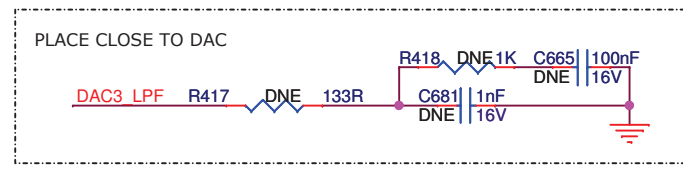


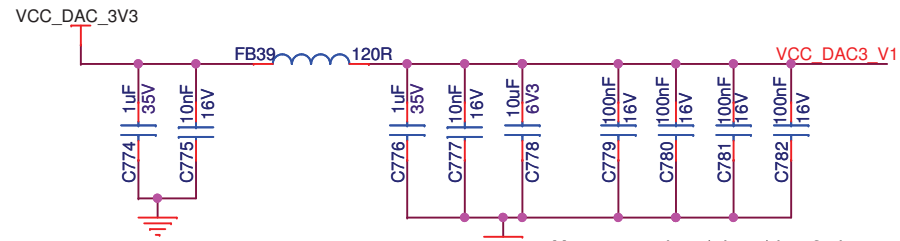
DAC DECHIRP 3

DAC 3 analog out-
OUTAP/N: 405 ± 37.5 MHz

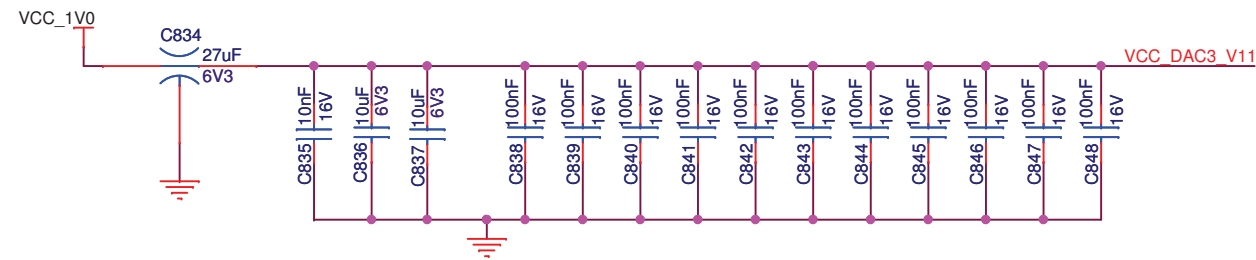
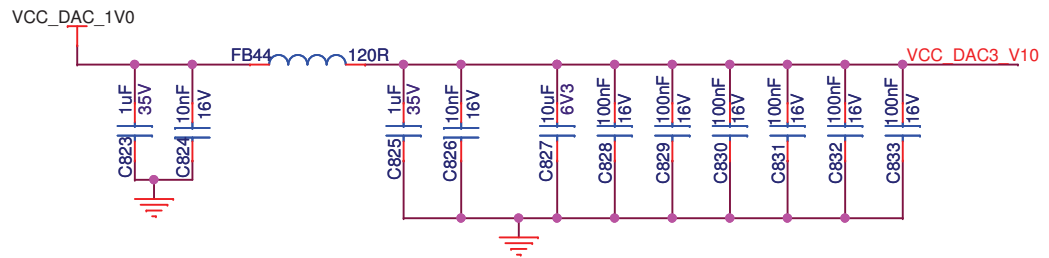
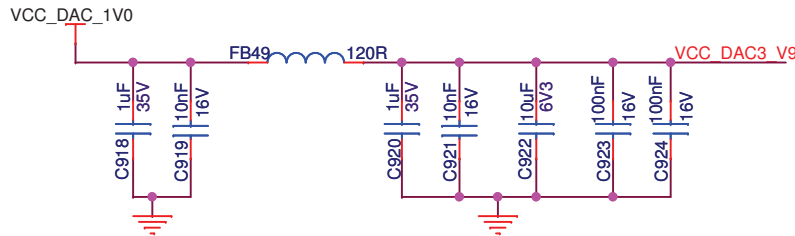
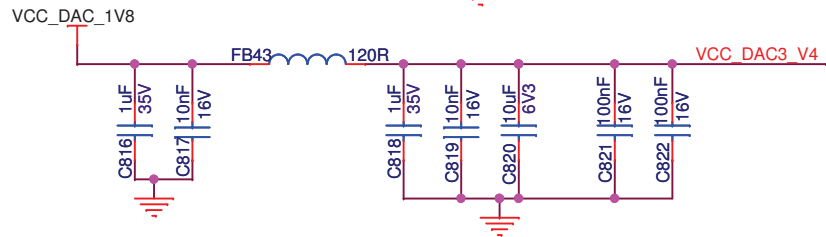
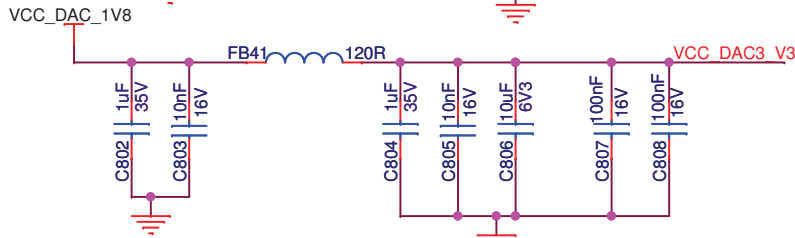
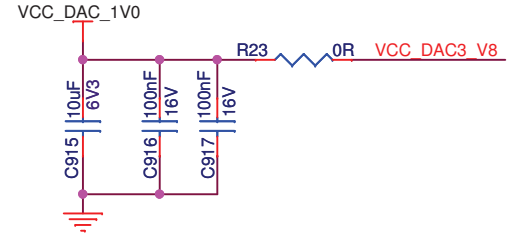
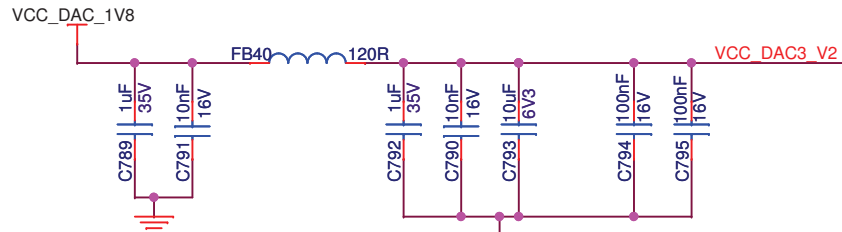
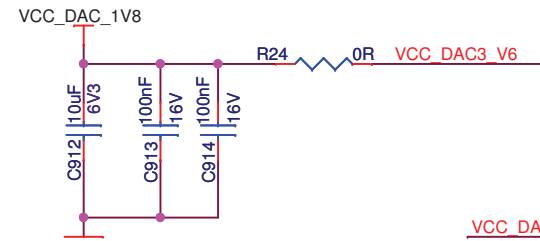
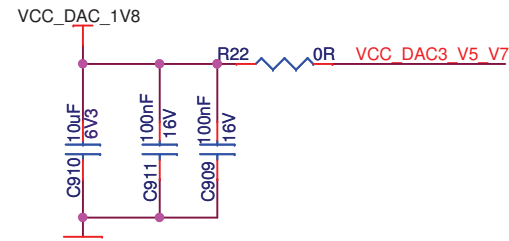


GTH227_TXP0_DAC3_RX0P	C331	100nF	G1	RX0P
GTH227_TXN0_DAC3_RX0N	C332	100nF	H1	RX0N
GTH227_TXP1_DAC3_RX1P	C333	100nF	K1	RX1P
GTH227_TXN1_DAC3_RX1N	C334	100nF	J1	RX1N
GTH227_TXP2_DAC3_RX2P	C335	100nF	L1	RX2P
GTH227_TXN2_DAC3_RX2N	C336	100nF	M1	RX2N
GTH227_TXP3_DAC3_RX3P	C337	100nF	M3	RX3P
GTH227_TXN3_DAC3_RX3N	C338	100nF	M2	RX3N
GTH228_TXP0_DAC3_RX4P	C340	100nF	F1	RX4P
GTH228_TXN0_DAC3_RX4N	C341	100nF	E1	RX4N
GTH228_TXP1_DAC3_RX5P	C342	100nF	C1	RX5P
GTH228_TXN1_DAC3_RX5N	C343	100nF	D1	RX5N
GTH228_TXP2_DAC3_RX6P	C344	100nF	B1	RX6P
GTH228_TXN2_DAC3_RX6N	C345	100nF	A1	RX6N
GTH228_TXP3_DAC3_RX7P	C346	100nF	A3	RX7P
GTH228_TXN3_DAC3_RX7N	C347	100nF	A2	RX7N





All Caps on the right side of the ground must be placed near the DAC



DAC3 POWER

