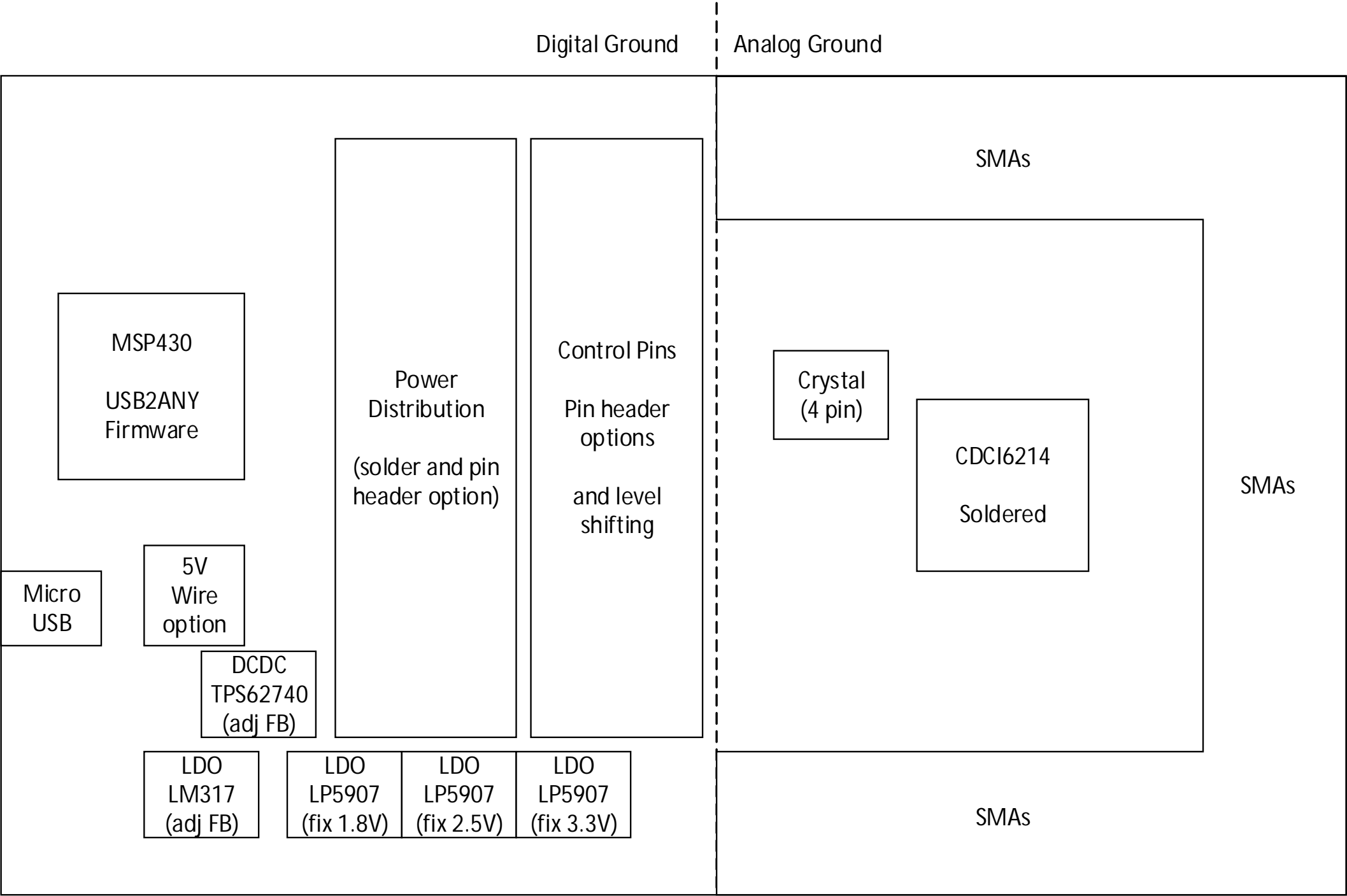


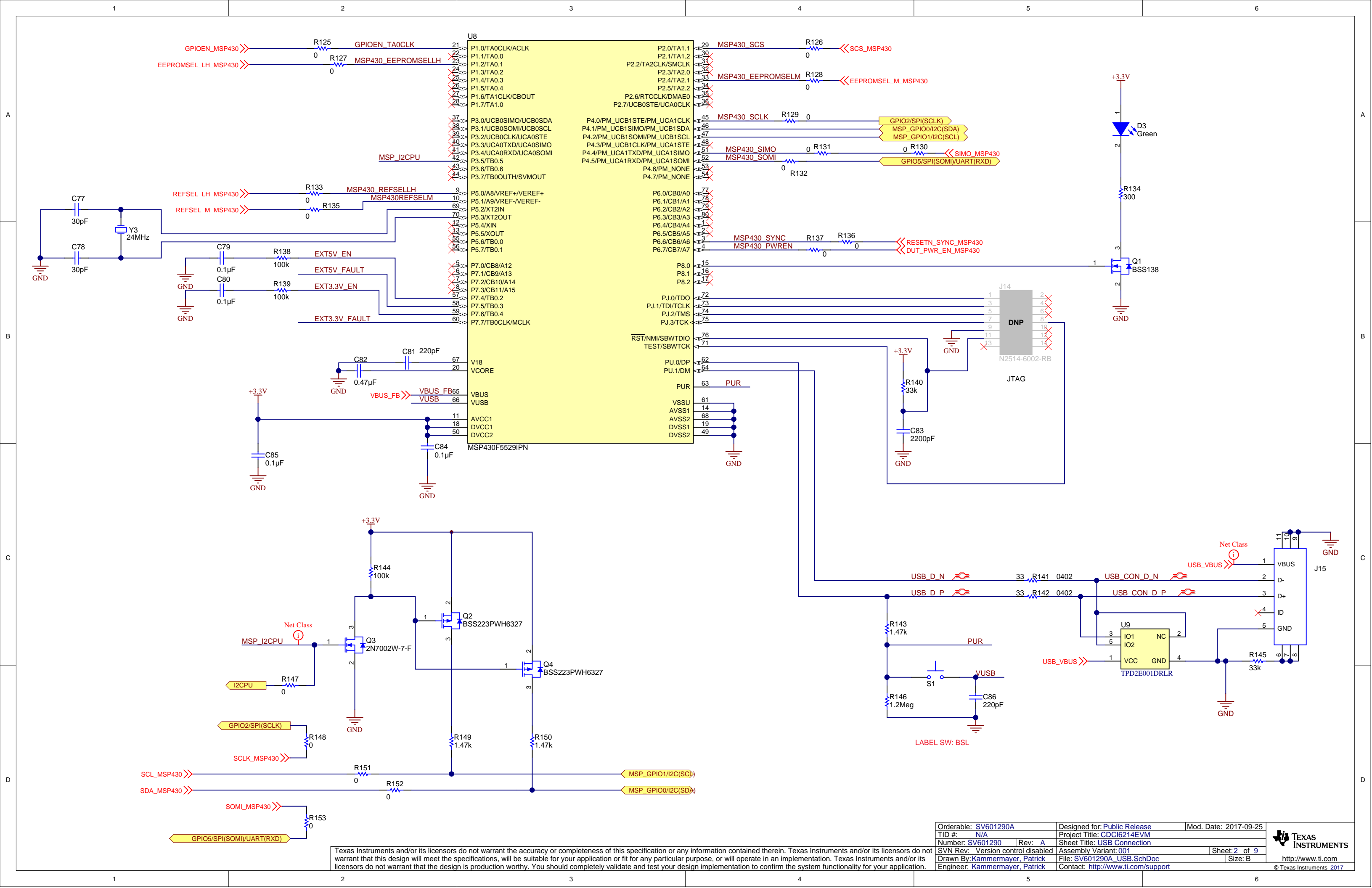
CDCI6214EVM

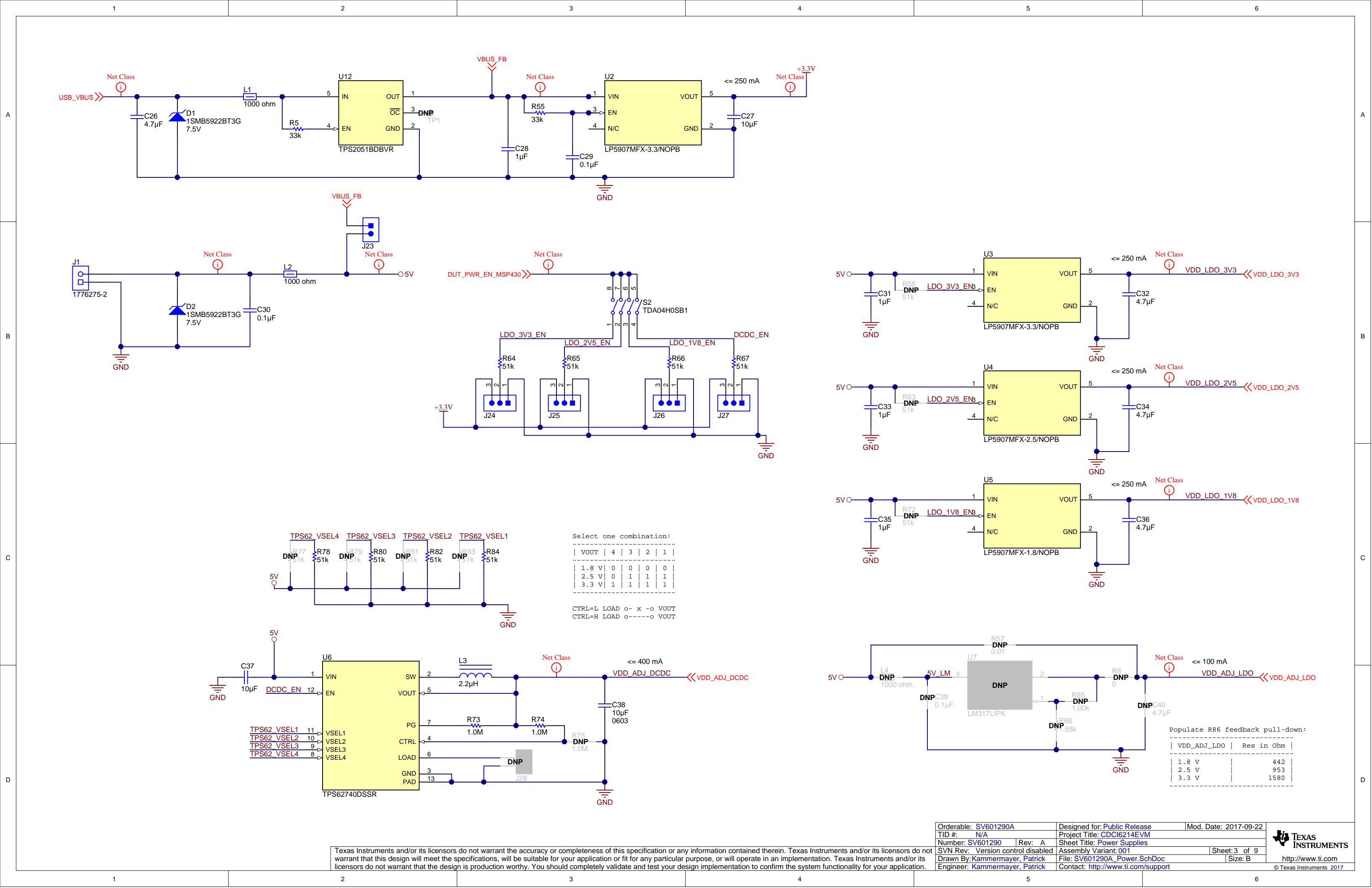
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

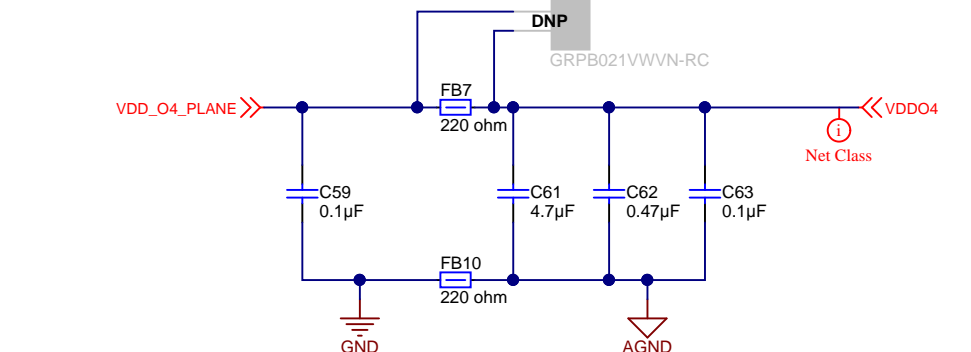
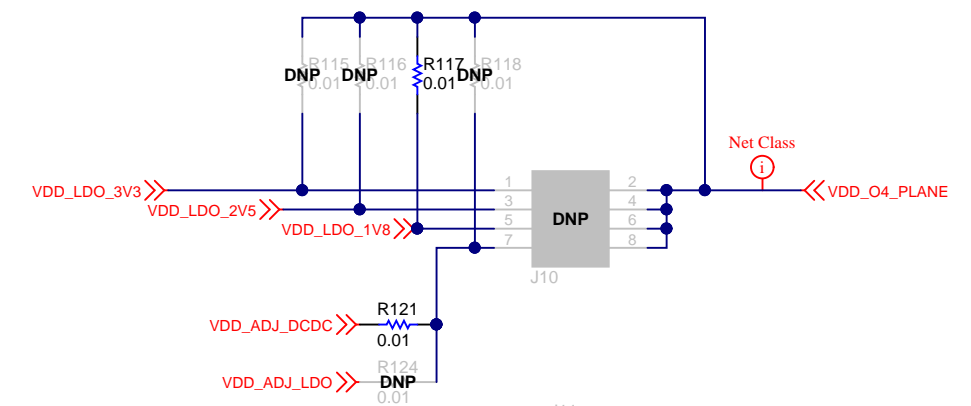
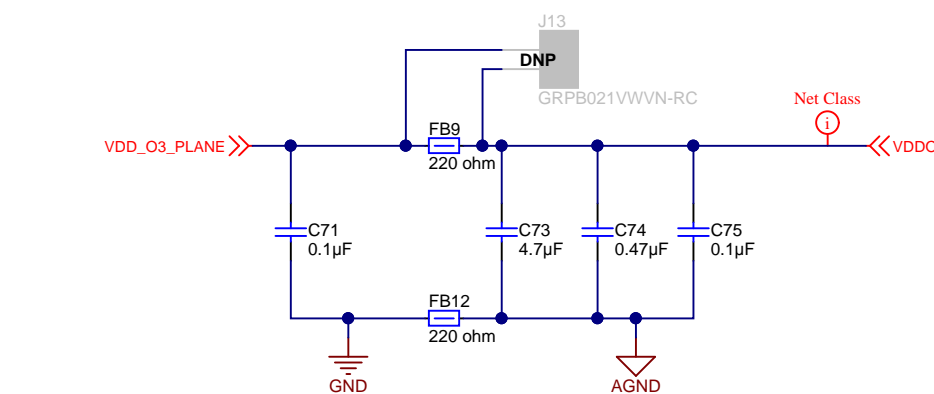
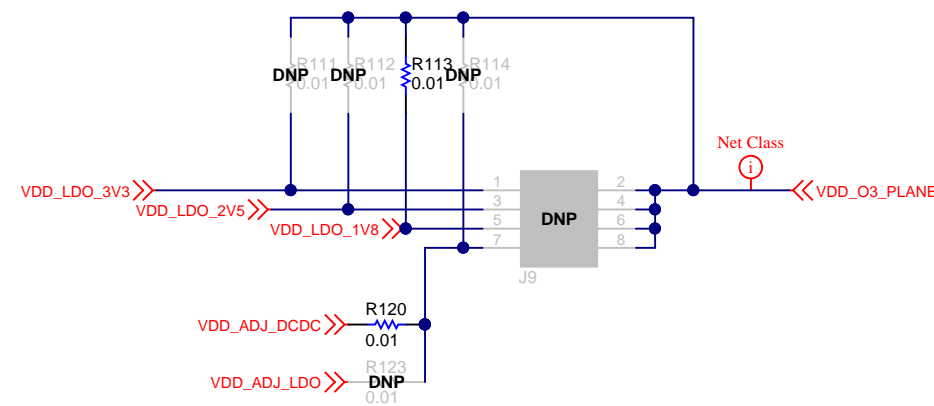
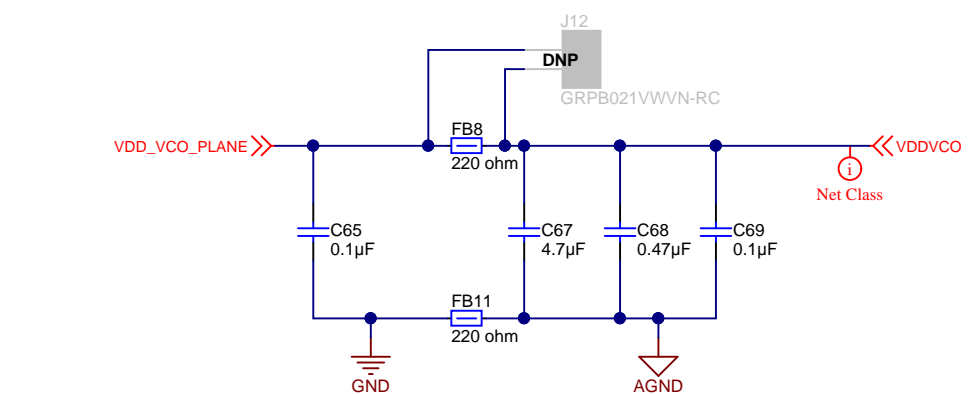
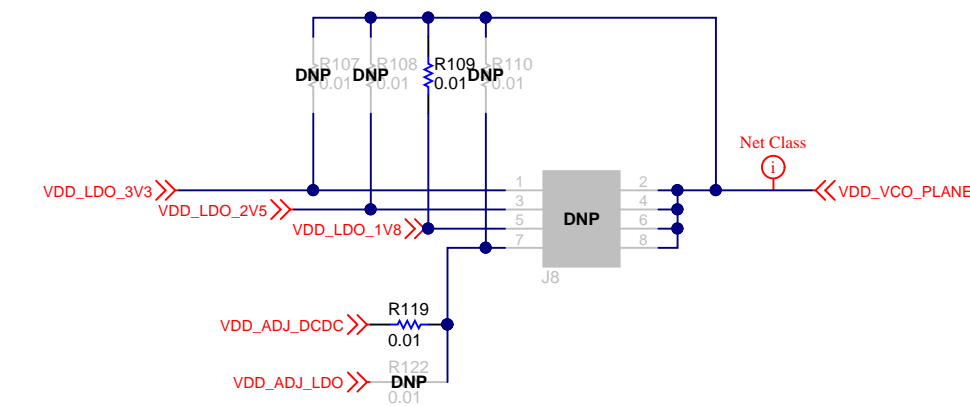
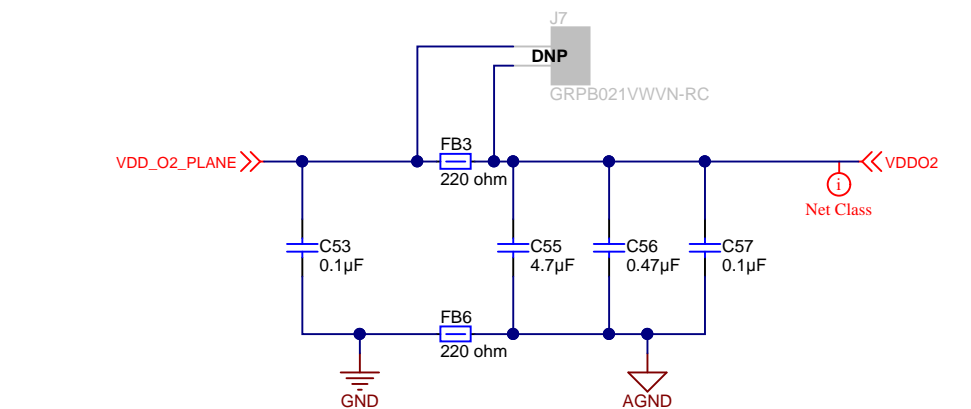
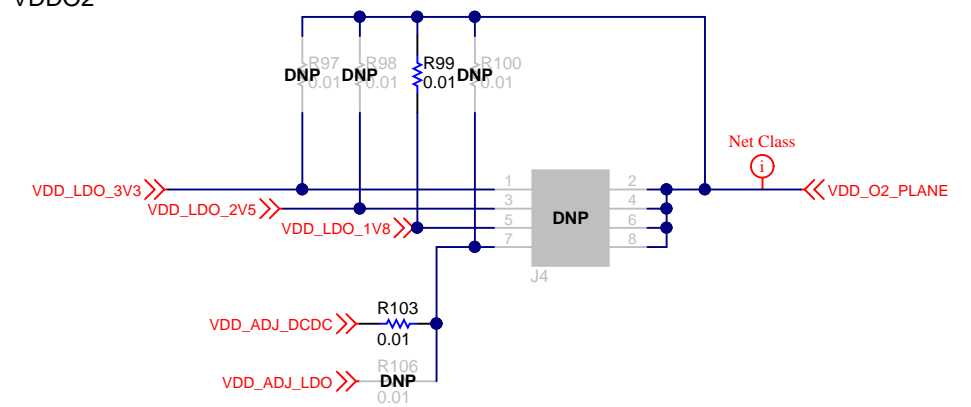
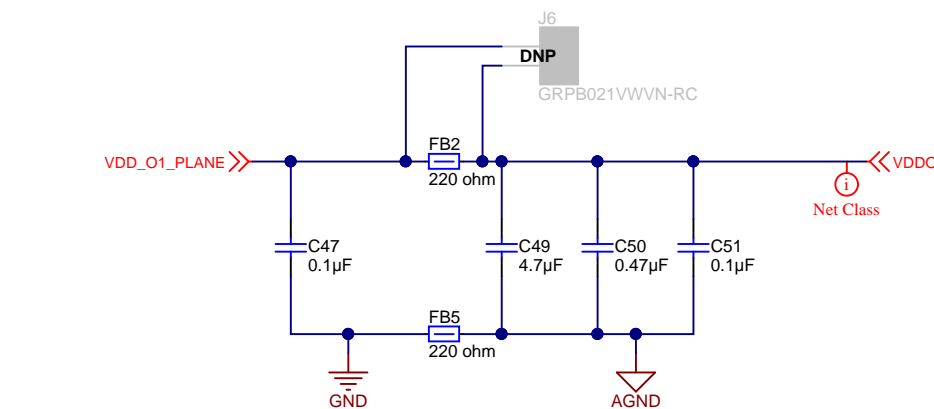
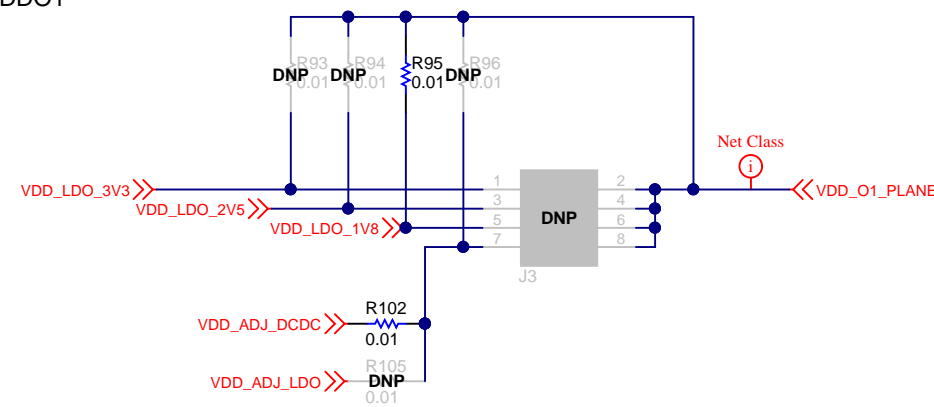
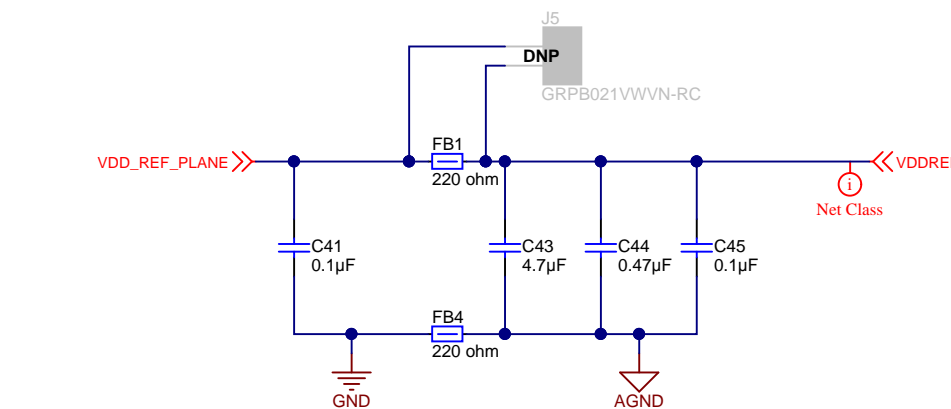
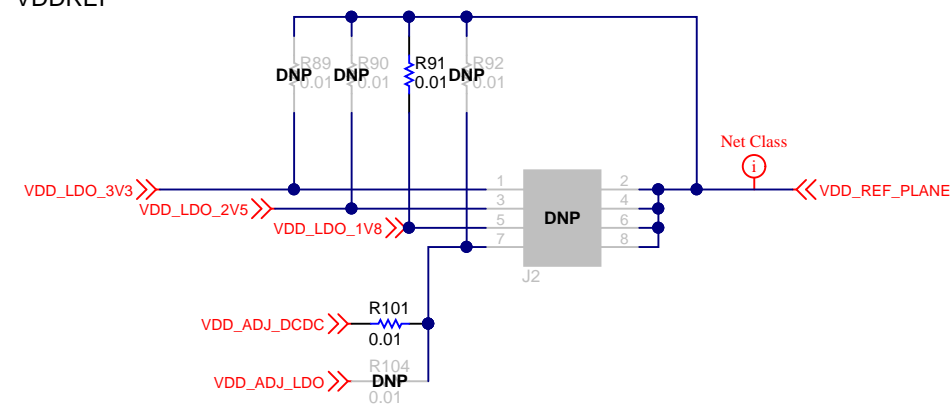


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Orderable: SV601290A		Designed for: Public Release		Mod. Date: 2017-09-15
TID #: N/A		Project Title: CDCI6214EVM		
Number: SV601290	Rev: A	Sheet Title: Block Diagram		
SVN Rev: Version control disabled		Assembly Variant: 001		Sheet: 1 of 9
Drawn By: Kammermayer, Patrick		File: SV601290A_CoverSheet.SchDoc		Size: B
Engineer: Kammermayer, Patrick		Contact: http://www.ti.com/support		



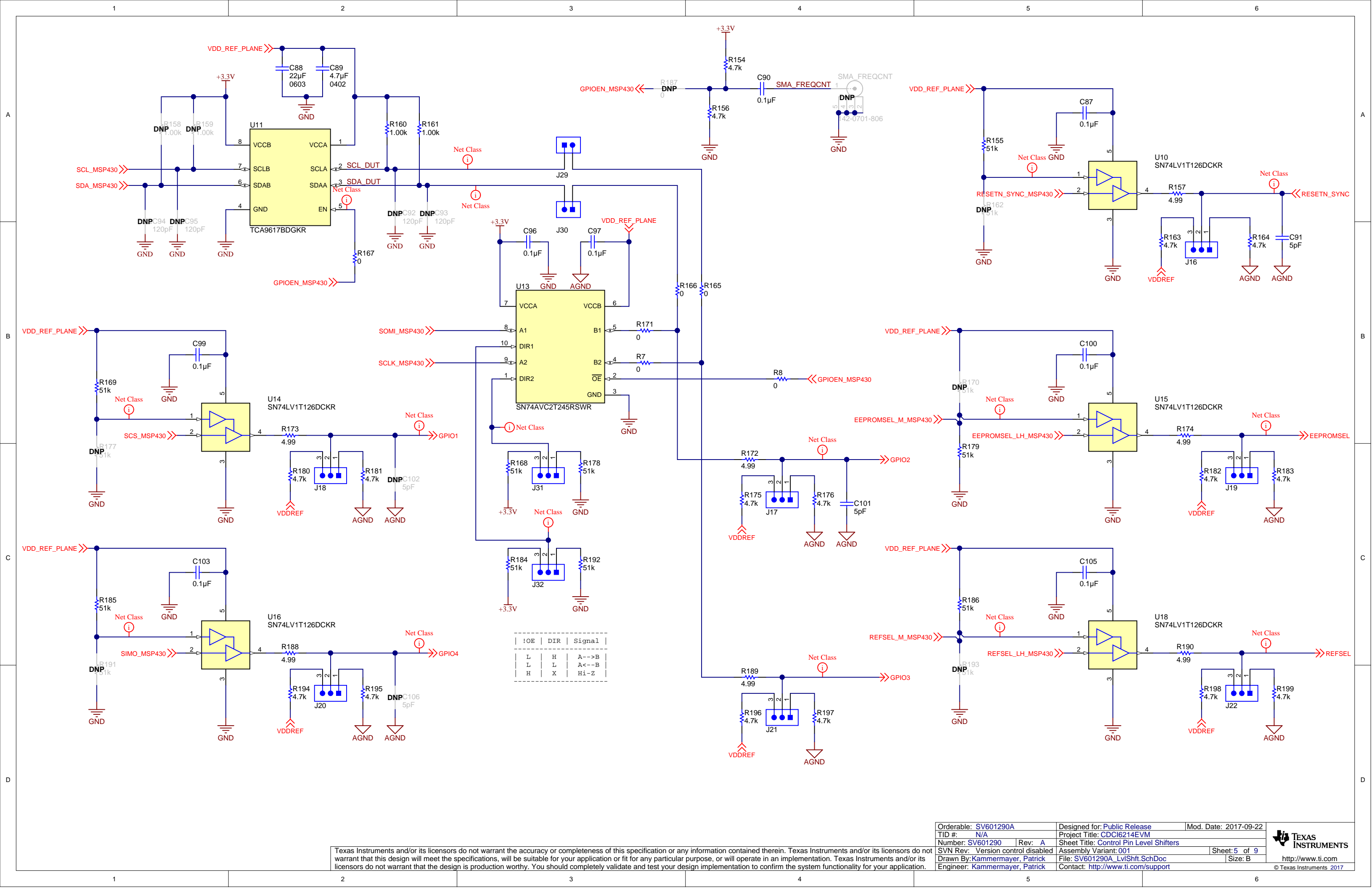




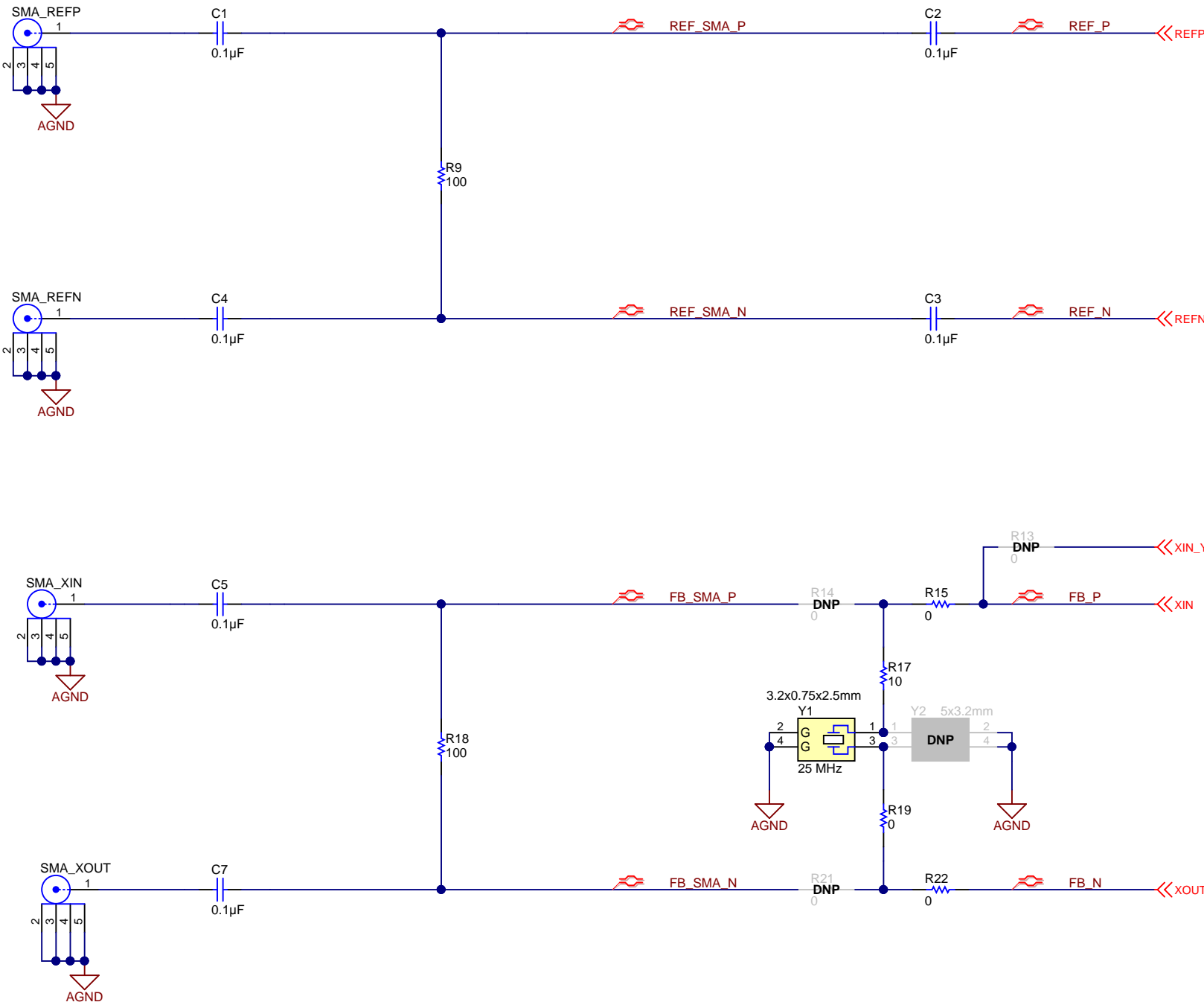
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Orderable: SV601290A	Designed for: Public Release	Mod. Date: 2017-09-22
TID #: N/A	Project Title: CDCI6214EVM	
Number: SV601290	Rev: A	Sheet Title: Power Supply Filters and Distribution
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 4 of 9
Drawn By: Kammermayer, Patrick	File: SV601290A_PowerFilterDistr.SchDoc	Size: B
Engineer: Kammermayer, Patrick	Contact: http://www.ti.com/support	



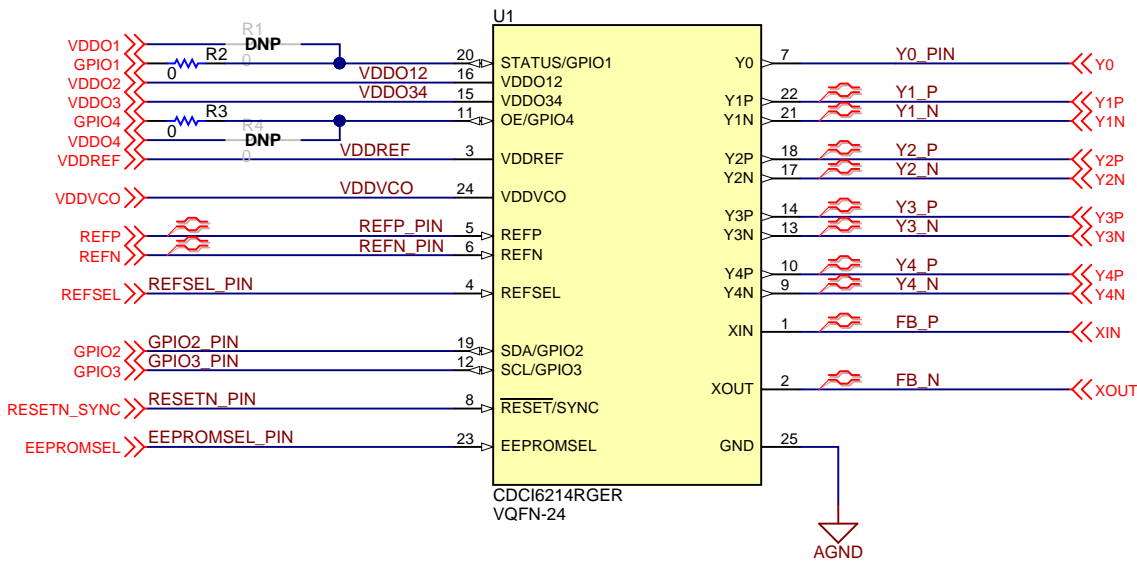


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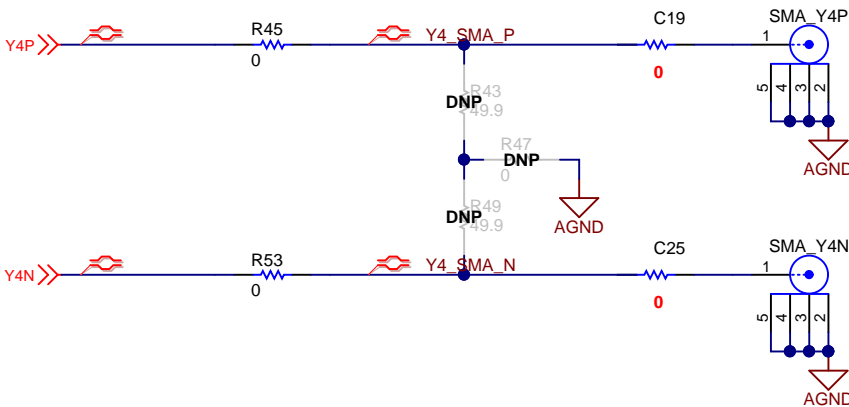
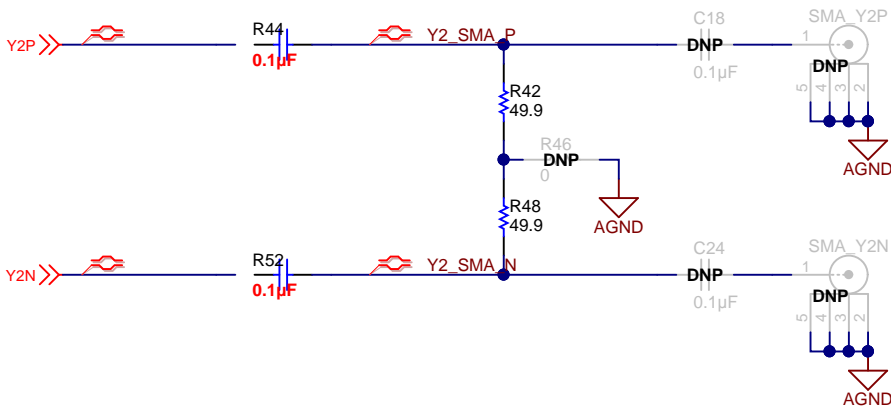
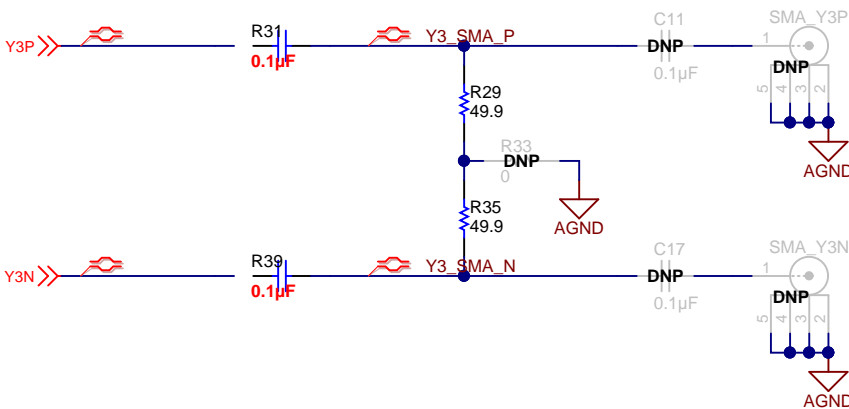
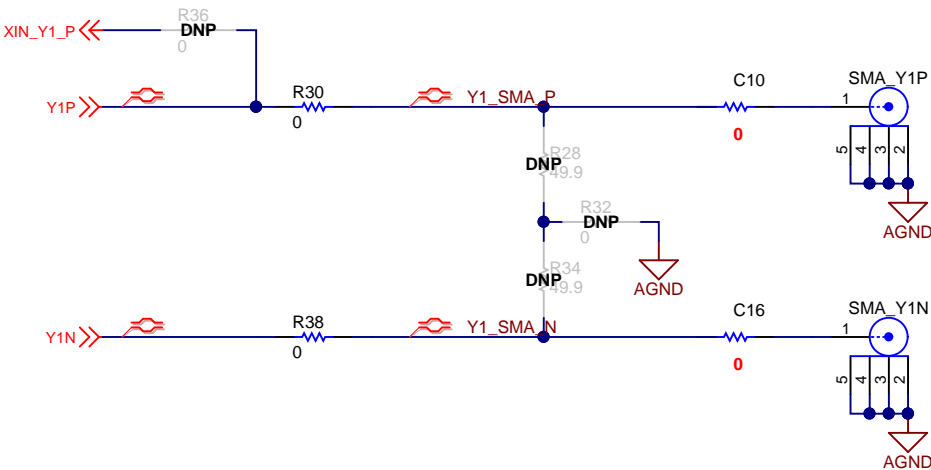
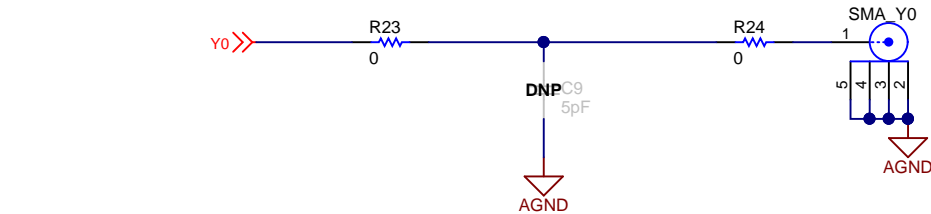
Orderable: SV601290A	Designed for: Public Release	Mod. Date: 2017-09-15
TID #: N/A	Project Title: CDCI6214EVM	
Number: SV601290	Rev: A	Sheet Title: Reference Clock Inputs
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 6 of 9
Drawn By: Kammermayer, Patrick	File: SV601290A_Inputs.SchDoc	Size: B
Engineer: Kammermayer, Patrick	Contact: http://www.ti.com/support	



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Orderable: SV601290A	Designed for: Public Release	Mod. Date: 2017-09-15
TID #: N/A	Project Title: CDCi6214EVM	
Number: SV601290	Rev: A	Sheet Title: Clock Generator
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 7 of 9
Drawn By: Kammermayer, Patrick	File: SV601290A_ClockGen.SchDoc	Size: B
Engineer: Kammermayer, Patrick	Contact: http://www.ti.com/support	

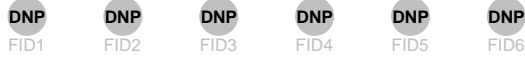
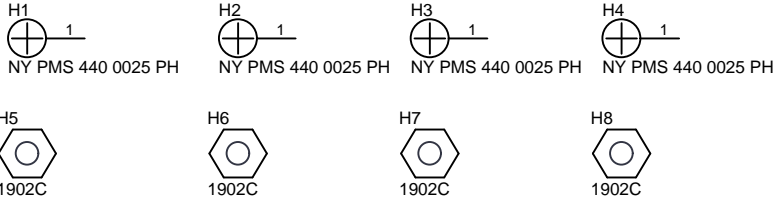
EVM Default			
Y1 HCSL 50 Ohm scope		Y3 AC-LVPECL Hi-Z Probe	
Y2 AC-LVPECL Hi-Z Probe		Y4 HCSL 50 Ohm scope	



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Orderable: SV601290A	Designed for: Public Release	Mod. Date: 2017-09-15
TID #: N/A	Project Title: CDCI6214EVM	
Number: SV601290	Rev: A	Sheet Title: Clock Generator Outputs
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 8 of 9
Drawn By: Kammermayer, Patrick	File: SV601290A_Outputs.SchDoc	Size: B
Engineer: Kammermayer, Patrick	Contact: http://www.ti.com/support	





PCB Number: SV601290
PCB Rev: A

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer

LBL1

PCB Label

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

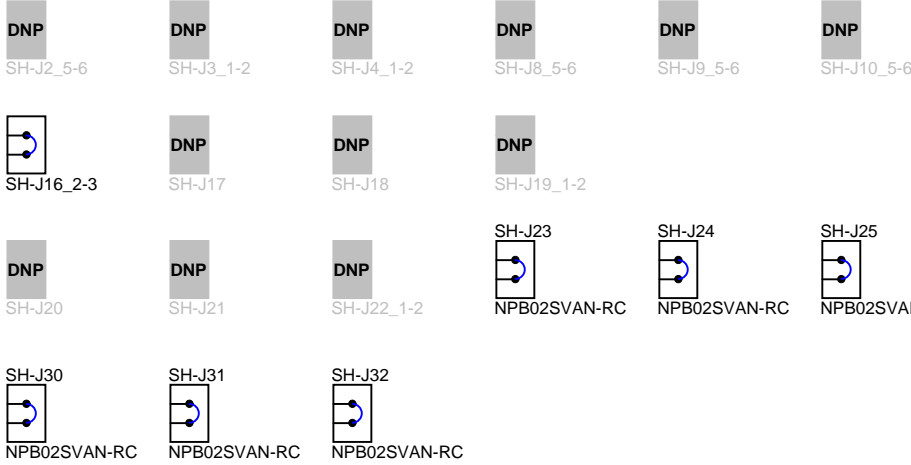
Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



Orderable: SV601290A		Designed for: Public Release		Mod. Date: 2017-09-22	
TID #: N/A		Project Title: CDCI6214EVM			
Number: SV601290		Rev: A		Sheet Title: Mechanical Components	
SVN Rev: Version control disabled		Assembly Variant: 001			Sheet: 9 of 9
Drawn By: Kammermayer, Patrick		File: SV601290A_EVM_Hardware.SchDoc			Size: B
Engineer: Kammermayer, Patrick		Contact: http://www.ti.com/support			