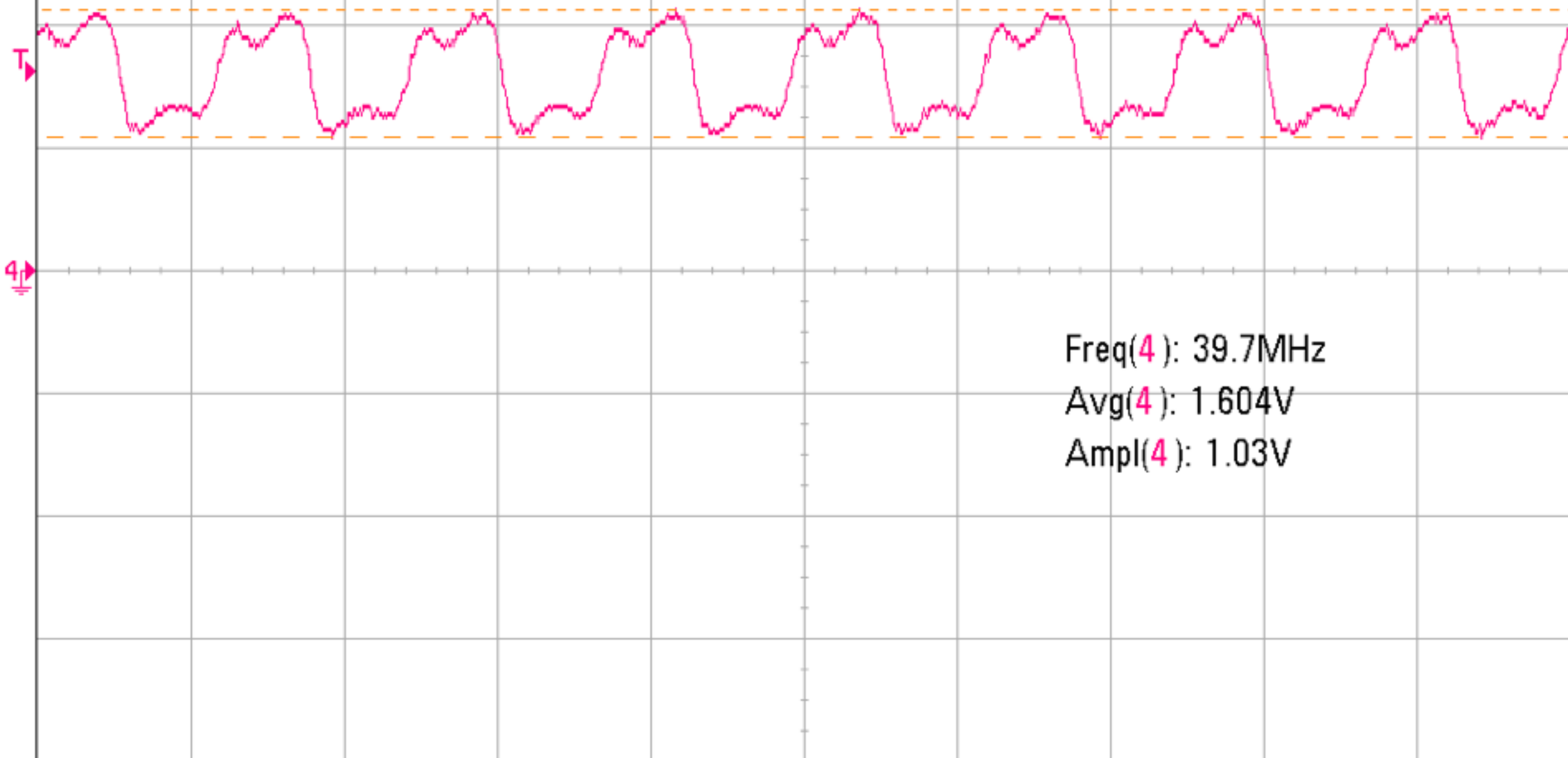




1 2 3 4 1.00V/ 0.0s 20.00ns/ Stop f 4 1.63V

Custom Board CLKin0+ (single-ended)



Channel 4 Menu

Coupling
DC

BW Limit
☐

Fine
☐

Invert
☐

Probe
↓



1 2 3 4 1.00V/ 0.0s 20.00ns/ Stop f 4 -1.72V

Custom Board
CLKin0- (single-ended)

4

T

Freq(4): 40.0MHz

Avg(4): -1.686V

Ampl(4): 1.19V

Channel 4 Menu

Coupling
DC

BW Limit

Fine

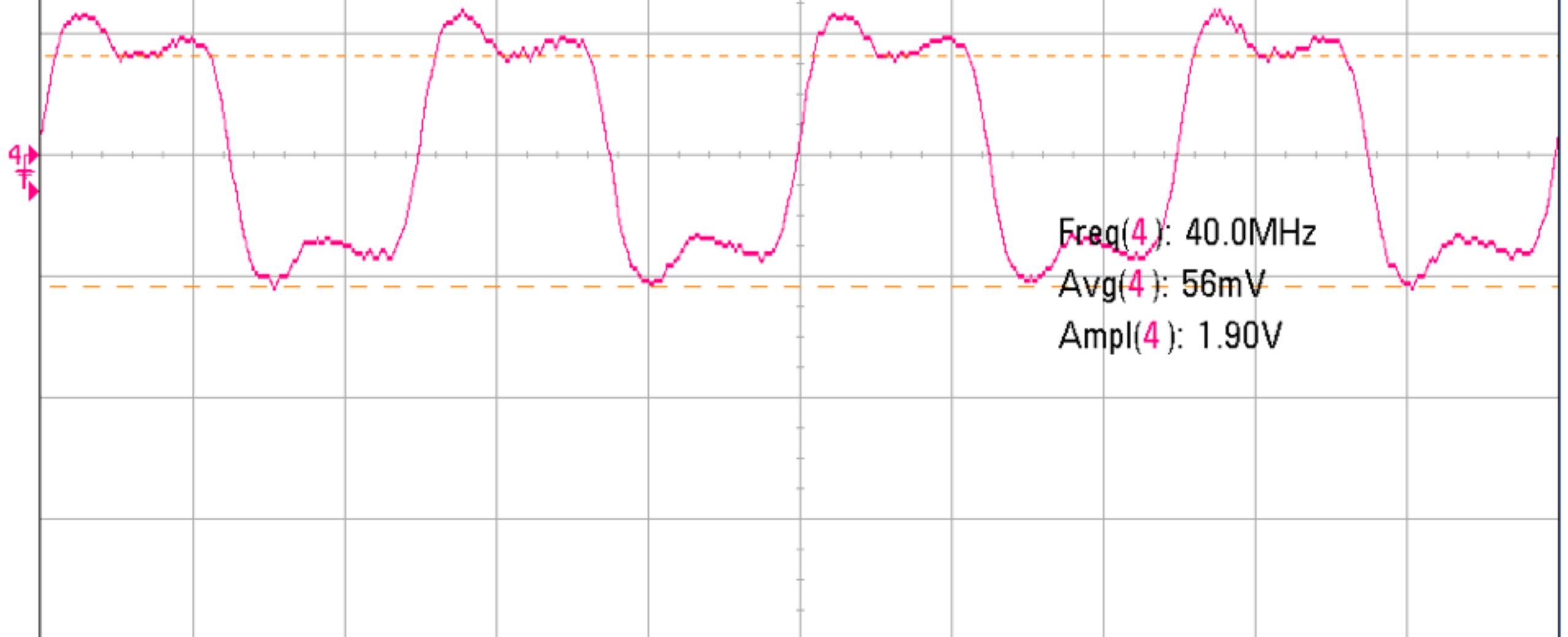
Invert

Probe



1 2 3 4 1.00V/ 0.0s 10.00ns/ Stop f 4 -300V

Custom Board CLKin0+/- (differential)



Channel 4 Menu

Coupling
DC

BW Limit

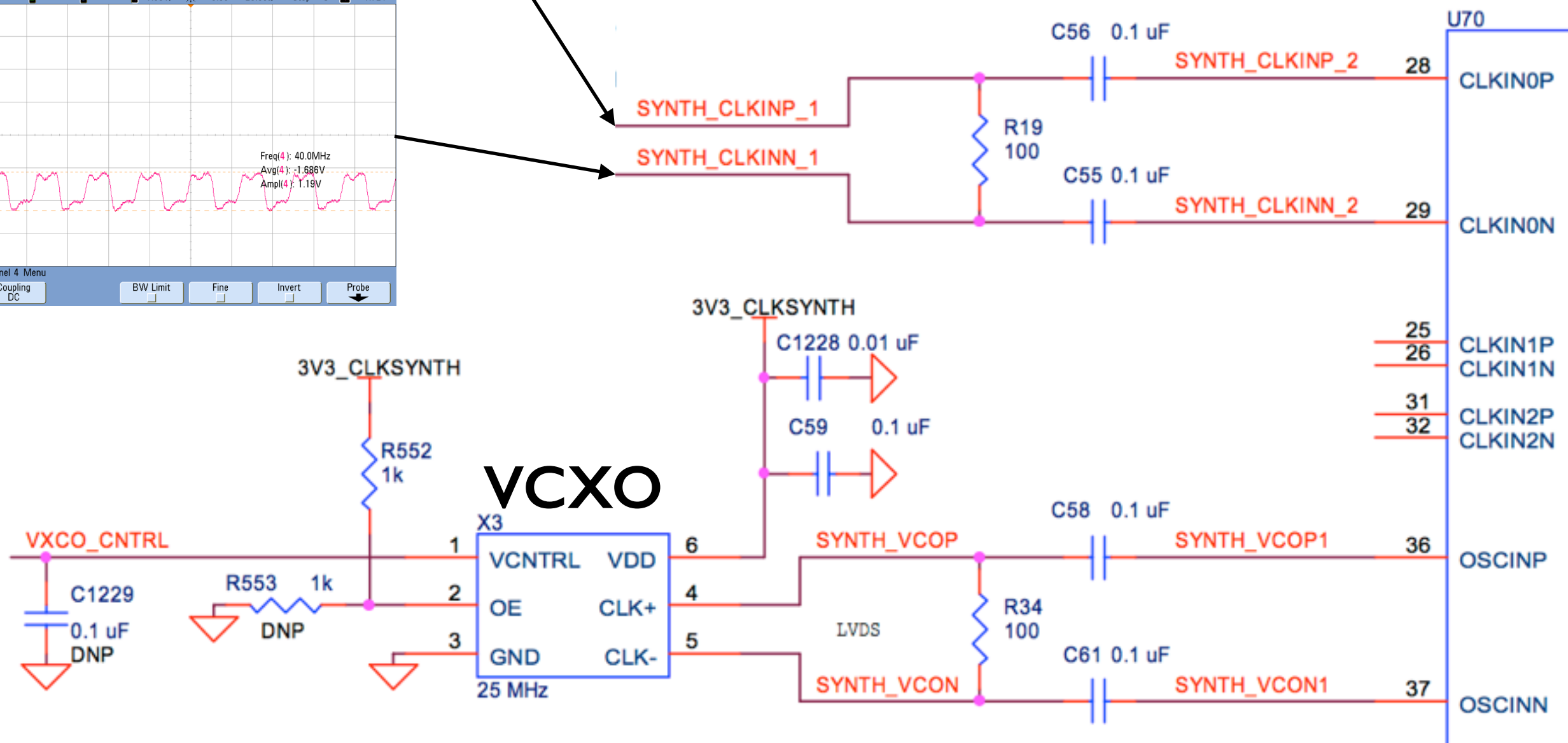
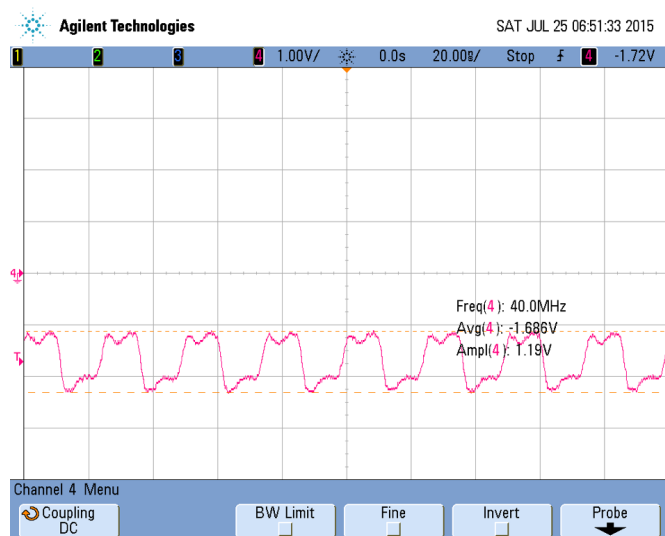
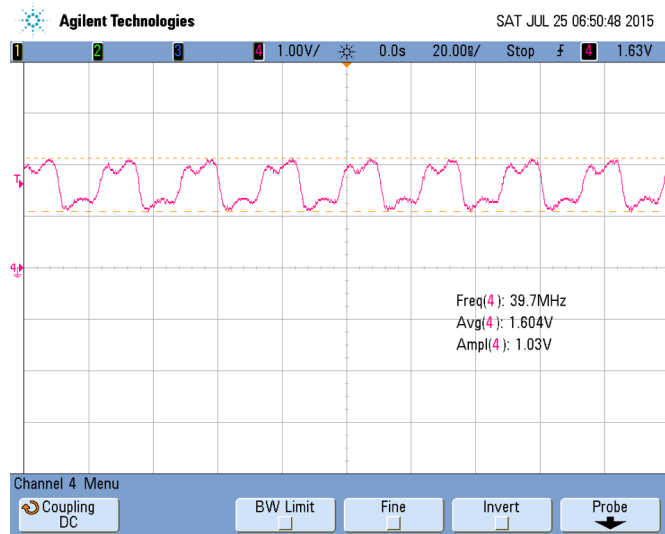
Fine

Invert

Probe

Input Clock Schematic for Custom Board

(see other attachment for full schematic)

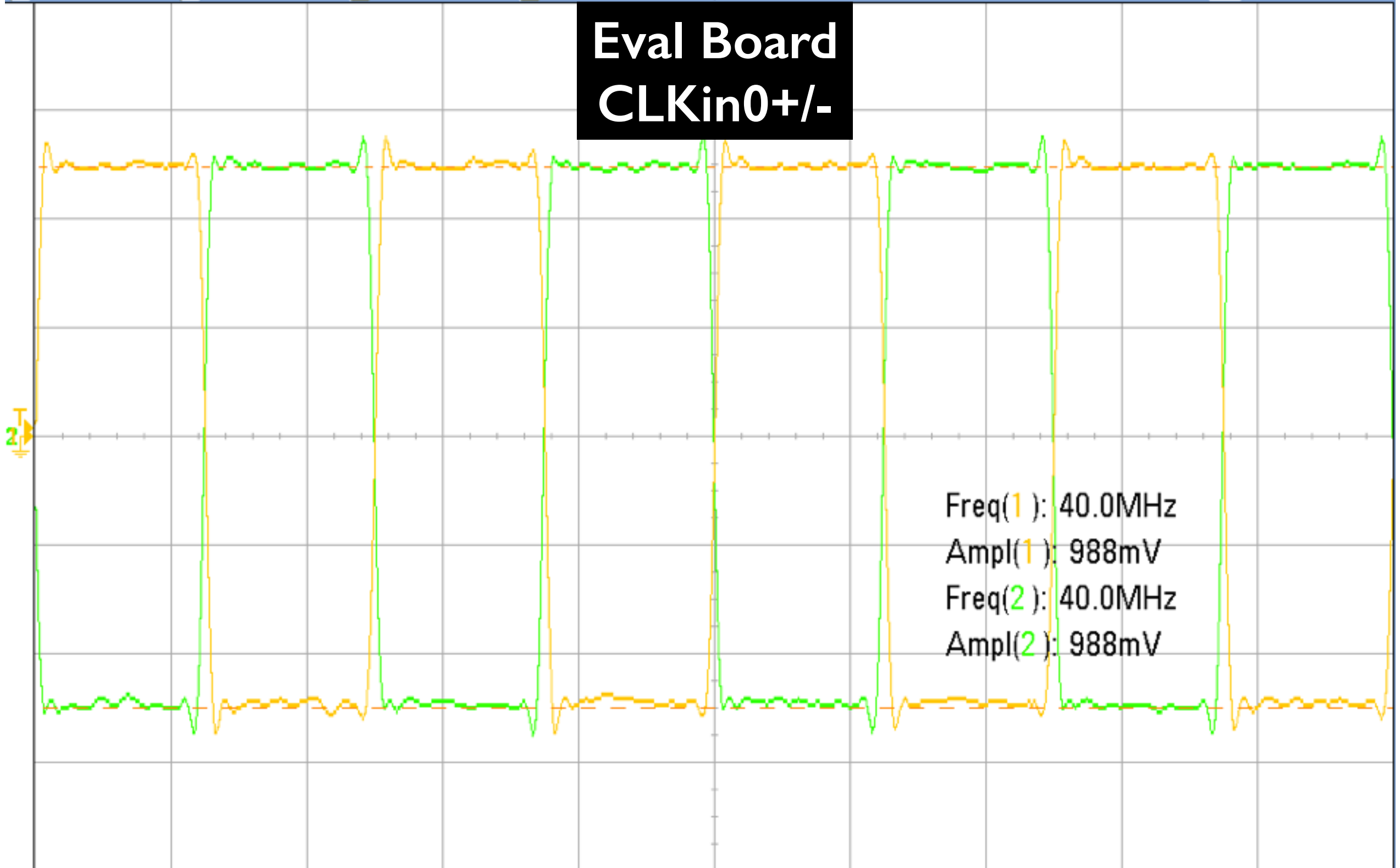


LMK04906



1 200mV/ 2 200mV/ 3 4 0.0s 10.00ns/ Auto f 1 15.8V

Eval Board
CLKin0+/-



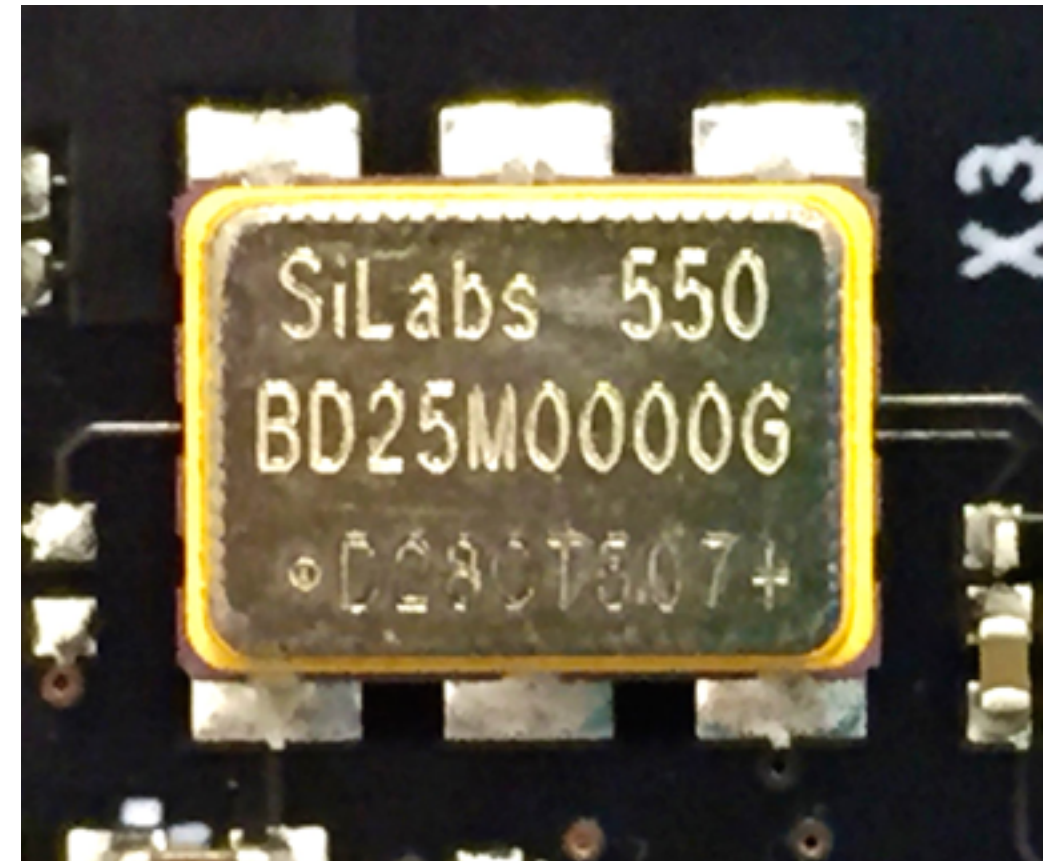
Channel 1 Menu

Coupling DC Imped 50 Ohm BW Limit Fine Invert Probe

Eval Board

Custom Board

VCXO



LMK04906

