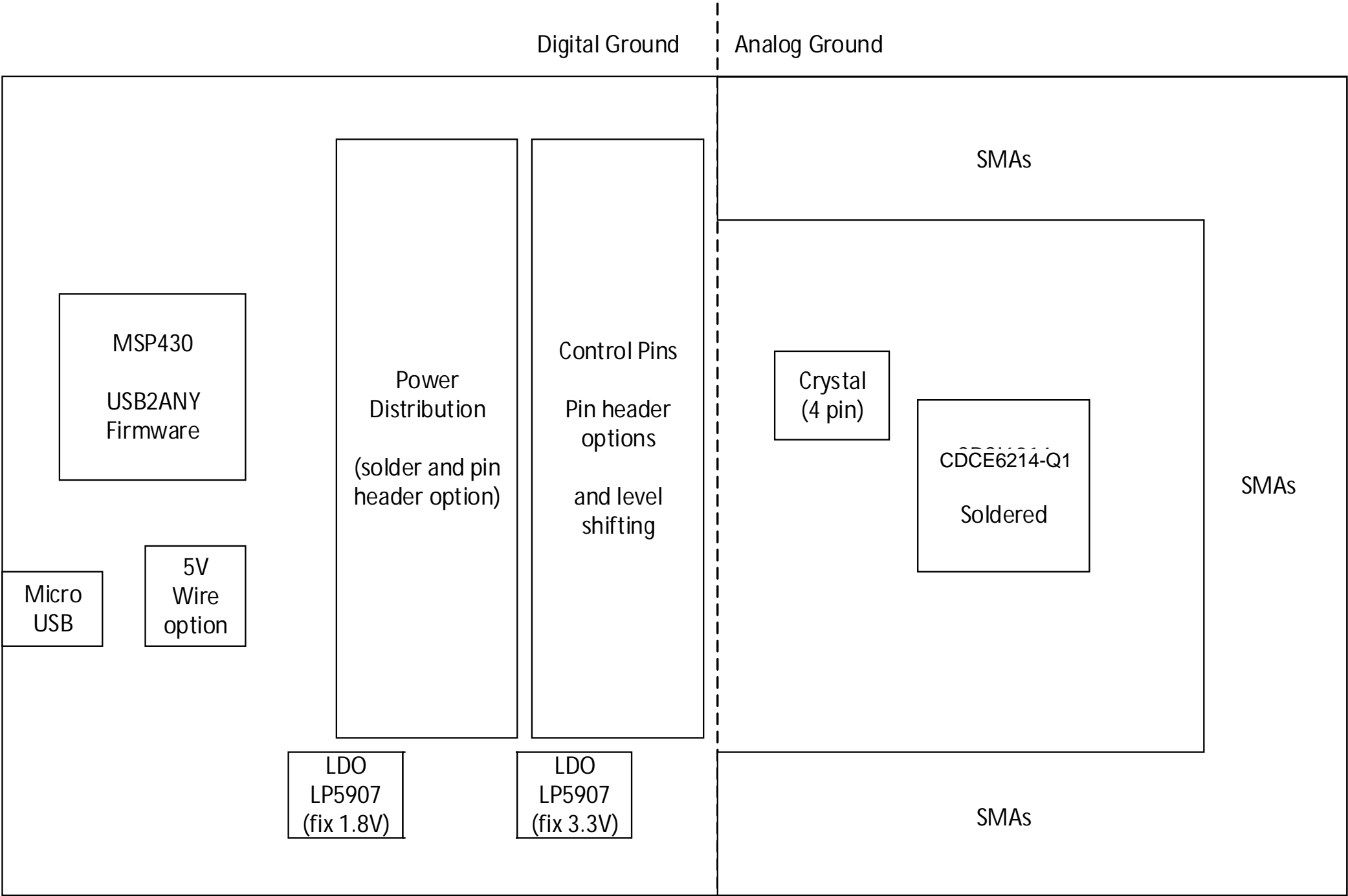


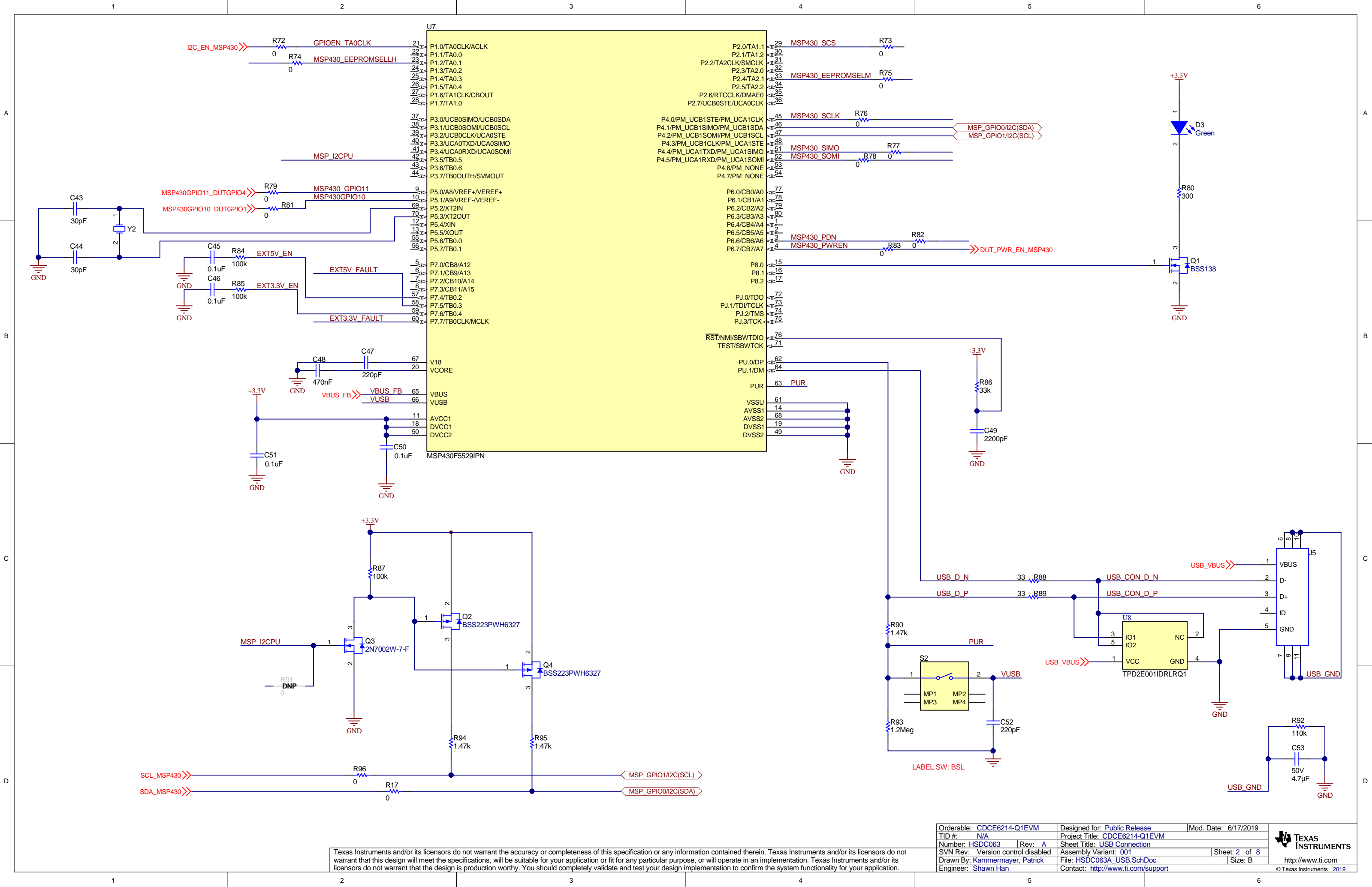
CDCE6214-Q1EVM

Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



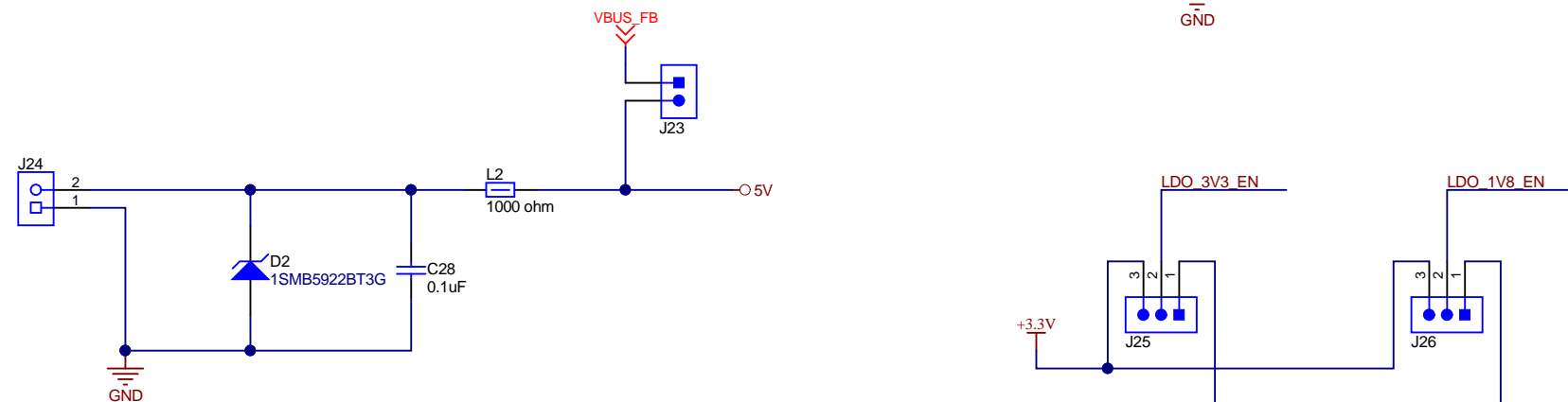
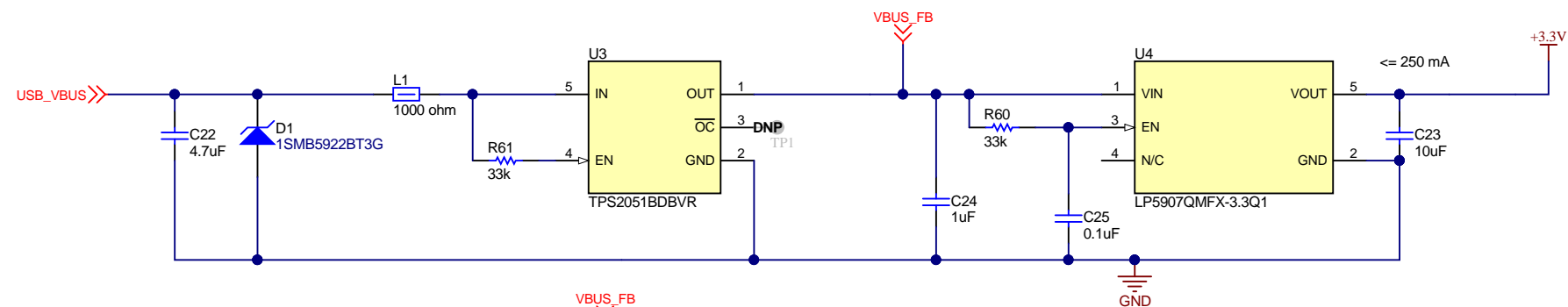
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Orderable: CDCE6214-Q1EVM		Designed for: Public Release	Mod. Date: 6/10/2019
TID #: N/A		Project Title: CDCE6214-Q1EVM	
Number: HSDC063	Rev: A	Sheet Title: Block Diagram	
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet: 1 of 8
Drawn By: Shawn Han		File: HSDC063A_CoverSheet.SchDoc	Size: B
Engineer: Shawn Han		Contact: http://www.ti.com/support	



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Orderable: CDCE6214-Q1EVM	Designed for: Public Release	Mod. Date: 6/17/2019
TID #: N/A	Project Title: CDCE6214-Q1EVM	
Number: HSDC063	Rev: A	Sheet Title: USB Connection
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 8
Drawn By: Kammermayer, Patrick	File: HSDC063A_USB.SchDoc	Size: B
Engineer: Shawn Han	Contact: http://www.ti.com/support	



Only J23 is opened, J24 can use an external supply in the range of 3.6V - 5V for LDO U5 and U6.

J23 Short: Select USB power supply (default)
J23 Open: Select external power supply fro J24

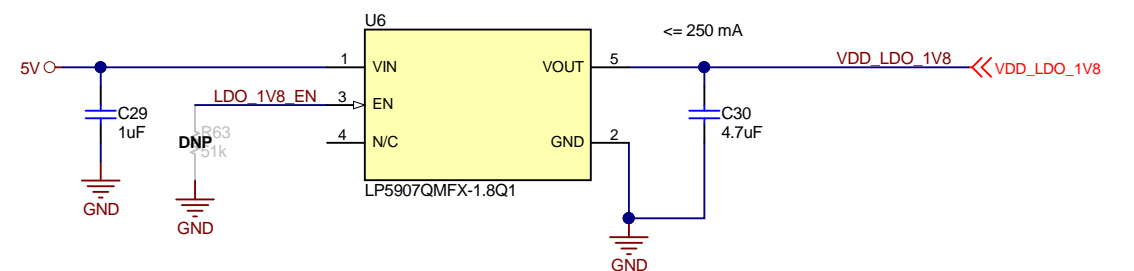
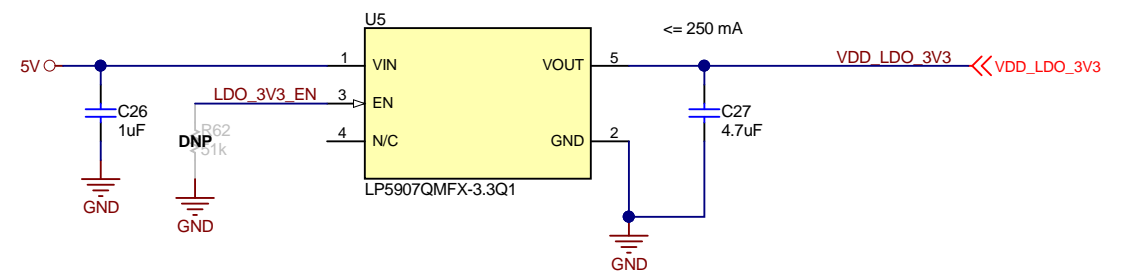
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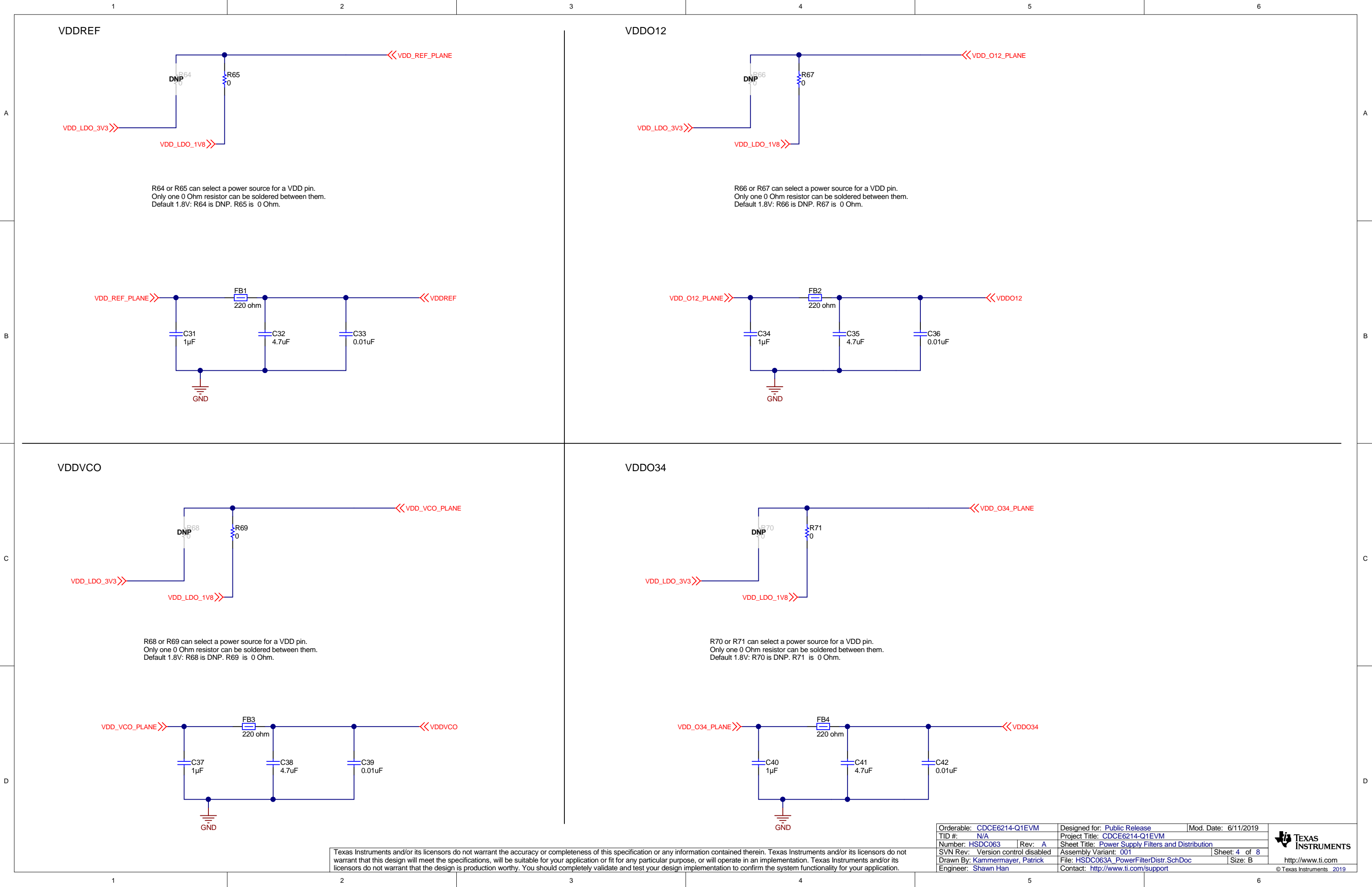
U5, U6 LDO LP5907 EN pin has an internal 1-M Ω pulldown resistor to hold the regulator off by default. For J25, J26, a shunt on pin 2 and pin 3 can enable LDOs, float pin 2 can disable LDOs, a shunt on pin 1 and pin 2 can make MCU control LDOs.

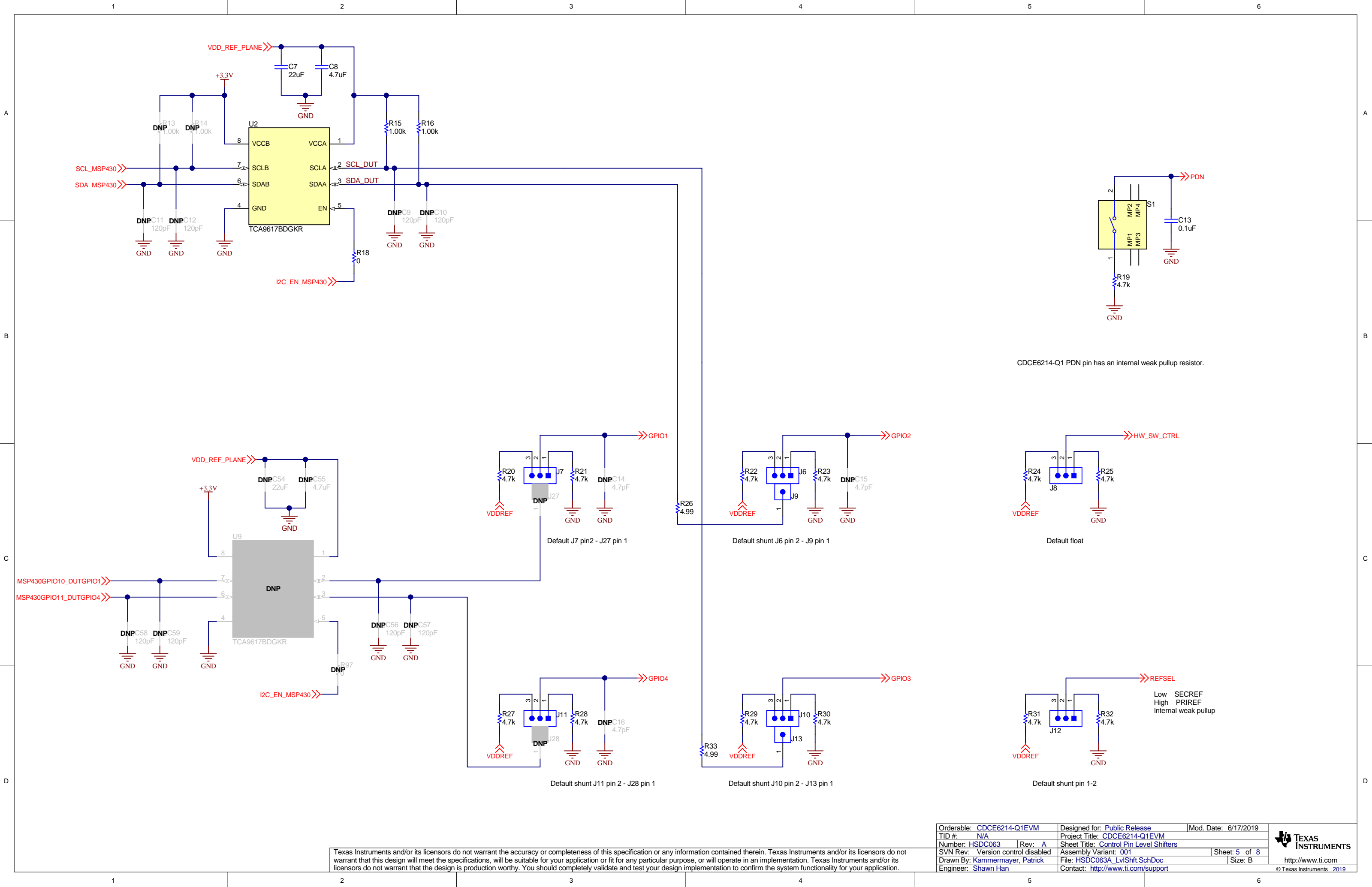
LDO Control
2-3 Enable
Open Disable
1-2 MCU

Default 1.8V LDO enable setting: floating J25, shunt pin 2 and pin 3 for J26.

It is allowed enabled both LDOs. CDCE6214-Q1 supports mixed power supplies. This board can use R64 - R71 to select power source for each CDCE6214-Q1 VDD pin.

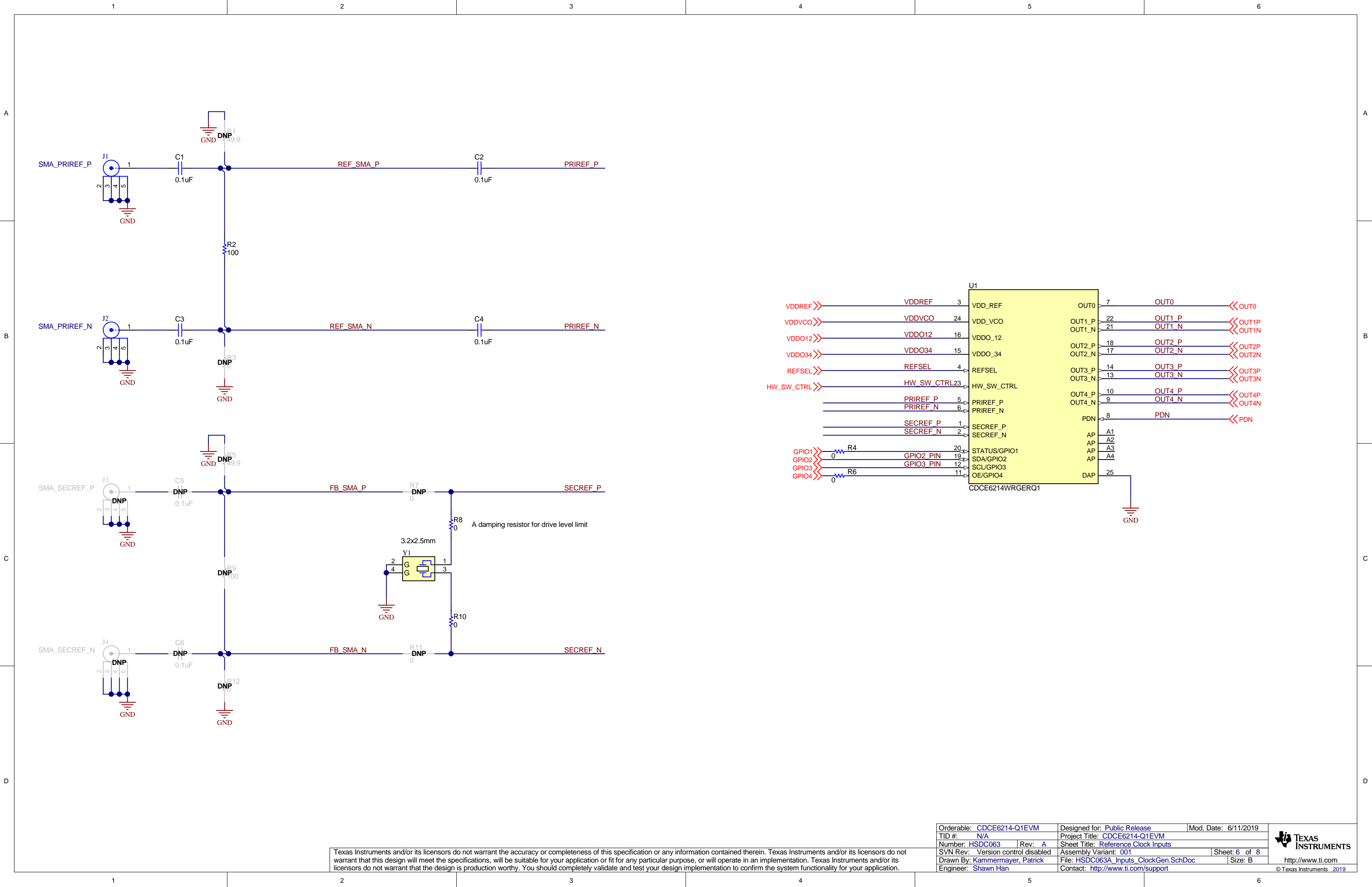






CDCE6214-Q1 PDN pin has an internal weak pullup resistor.

Low SECREP
High PRIREF
Internal weak pullup



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Orderable: CDCE6214-Q1EVM	Designed for: Public Release	Mod. Date: 6/11/2019
TID #: N/A	Project Title: CDCE6214-Q1EVM	
Number: HSDC063	Rev: A	Sheet Title: Reference Clock Inputs
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 6 of 8
Drawn By: Kammermayer, Patrick	File: HSDC063A_Inputs_ClockGen.SchDoc	Size: B
Engineer: Shawn Han	Contact: http://www.ti.com/support	

