



Connection

Live Update
  Limited Ranges Mode

Views

**Tools**

Input

fin [MHz]  Crystal Load   
 Signal Source:

Logic

SPICON:   
 Revison: DieIdent: Vendor:     
 SlaveAddress:   
 Power Down Mode  
 EEPROM Lock

Control Line Selection:

S2 = 0, S1 = 0, S0 = 1

Select Device:

(Local) CDCE913

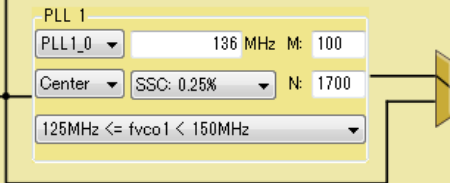
Refresh Devices

Solution Selection:

MySetup01 1.1 (XTAL)

CDCE913

Select



Bits Viewer

Generic Registers	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Reg16 00	SSC1_7,2	SSC1_7,1	SSC1_7,0	SSC1_6,2	SSC1_6,1	SSC1_6,0	SSC1_5,2	SSC1_5,1
Reg17 00	SSC1_5,0	SSC1_4,2	SSC1_4,1	SSC1_4,0	SSC1_3,2	SSC1_3,1	SSC1_3,0	SSC1_2,2
Reg18 08	SSC1_2,1	SSC1_2,0	SSC1_1,2	SSC1_1,1	SSC1_1,0	SSC1_0,2	SSC1_0,1	SSC1_0,0
Reg19 00	FS1_7	FS1_6	FS1_5	FS1_4	FS1_3	FS1_2	FS1_1	FS1_0
Reg20 6D	MUX1	M2	M3,1	M3,0	Y2Y3_ST1,1	Y2Y3_ST1,0	Y2Y3_ST0,1	Y2Y3_ST0,0
Reg21 02	Y2Y3_7	Y2Y3_6	Y2Y3_5	Y2Y3_4	Y2Y3_3	Y2Y3_2	Y2Y3_1	Y2Y3_0
Reg22 80	SSC1DC	Pdiv2,6	Pdiv2,5	Pdiv2,4	Pdiv2,3	Pdiv2,2	Pdiv2,1	Pdiv2,0
Reg23 00	Reserved	Pdiv3,6	Pdiv3,5	Pdiv3,4	Pdiv3,3	Pdiv3,2	Pdiv3,1	Pdiv3,0
Reg24 6A	PLL1_0N11	PLL1_0N10	PLL1_0N9	PLL1_0N8	PLL1_0N7	PLL1_0N6	PLL1_0N5	PLL1_0N4
Reg25 40	PLL1_0N3	PLL1_0N2	PLL1_0N1	PLL1_0N0	PLL1_0R8	PLL1_0R7	PLL1_0R6	PLL1_0R5
Reg26 02	PLL1_0R4	PLL1_0R3	PLL1_0R2	PLL1_0R1	PLL1_0R0	PLL1_0Q5	PLL1_0Q4	PLL1_0Q3
Reg27 21	PLL1_0Q2	PLL1_0Q1	PLL1_0Q0	PLL1_0P2	PLL1_0P1	PLL1_0P0	VCO1_0RG1	VCO1_0RG0
Reg28 F5	PLL1_1N11	PLL1_1N10	PLL1_1N9	PLL1_1N8	PLL1_1N7	PLL1_1N6	PLL1_1N5	PLL1_1N4
Reg29 70	PLL1_1N3	PLL1_1N2	PLL1_1N1	PLL1_1N0	PLL1_1R8	PLL1_1R7	PLL1_1R6	PLL1_1R5
Reg30 3B	PLL1_1R4	PLL1_1R3	PLL1_1R2	PLL1_1R1	PLL1_1R0	PLL1_1Q5	PLL1_1Q4	PLL1_1Q3