



LMx2581

File Keyboard Controls Select Device Options Mode LPT/USB Help

Port Setup Registers Bits/Pins BurstMode PLL

General Settings

- RESET
- PWDN_MODE: Powered Up
- MODE: Full Synthesizer
- ZERO_DELAY
- uWireLock

Channel Divider

- OUTA_MUX: Channel Divider
- OUTB_MUX: Channel Divider

Charge Pump

- CPG_BLEED: 0

FastLock

- FL_CPG: Tri-State
- FL_TOC: 0
- FL_FRCE

Digital Lock Detect

- DLD_TOL: 18 ns
- DLD_PASS_CNT: 32
- DLD_ERR_CNT: 4

Fractional Settings

- FRAC_DITHER: Strong
- FRAC_ORDER: 2nd Order Modulator
- PFD_DLY: 370 ps

MUX/LD Pins

- LD_PINMODE: Push-Pull
- LD_INV
- LD_SELECT: DLD & Vtune Good
- MUXOUT_PINMODE: Push-Pull
- MUX_INV
- MUXOUT_SELECT: Readback Data
- FL_INV
- FL_SELECT: GND
- FL_PINMODE: Tri-state

Output Buffer

- OUTA_PD
- OUTB_PD
- OUTA_PWR: 15
- OUTB_PWR: 15
- AUTO_MUTE
- BUFEN_DIS

Program Pins

- BUFEN
- CE
- TRIGGER

Diagnostics

- ID
- RDADDR: 6
- RD_DIAGNOSTICS: 0

VCO Frequency Calibration

- NO_FCAL
- VCO_SEL: VCO 3
- OSC_FREQ: <64 MHz
- VCO_SEL_MODE: Start with VCO_SEL Cho
- VCO_CAPCODE: 128
- VCO_CAPCODE_MAN

COMMM Mode: USB

TEXAS INSTRUMENTS