



RESETN_SW
 SYNCN_SW

PLL
 VCO Post-Div Out: 2448 MHz
 PRIREF: 4896 MHz
 SECREF: 24 MHz

Output Muxes	Output Dividers	DIFF / 1.8V LVCMOS Output Drivers*
<input type="checkbox"/> CH01PWDN	34 <input type="checkbox"/> DIV01_DDLY	AC-LVPECL → OUT0 72 MHz
<input checked="" type="checkbox"/> CH23PWDN	20 <input type="checkbox"/> DIV23_DDLY	AC-LVPECL → OUT1 72 MHz
<input type="checkbox"/> CH04PWDN	25 <input type="checkbox"/> DIV4_DDLY	AC-LVPECL → OUT2 MHz
		AC-LVPECL → OUT3 MHz
		AC-LVPECL → OUT4 MHz

Load Custom Phase Noise

Set custom phase noise for: REF

x Data Units: kHz Hz

x (kHz)	y (dBc/Hz)
10	-50
100	-70
1000	-90

Phase Noise and Integrated Phase Noise Metrics as measured at CLKOUT0, LVCMOS : 72.0

Integrated Noise Metrics

Phase Noise Integration Range
 kHz to MHz

Phase Noise Area
 Spur Area
 Both Phase Noise and Spur Area

Noise Metric	Value	Unit
RMS Jitter	18830.0	fs
Equivalent Flat Noise	-117.4	dBc/Hz
SNR	41.4	dB

Show more noise metrics

Phase Noise

Get Chart Data

Click on the legend to turn on/off the series.

Phase Noise Values

Offset	Phase Noise
<input type="text" value="12"/> kHz	-77.9 dBc/Hz
<input type="text" value="100"/> kHz	-137.3 dBc/Hz
<input type="text" value="1000"/> kHz	-159.2 dBc/Hz

Simulation Input

X Min: kHz X Max: MHz

Y min: dBc Y max: dBc

Update Simulation Data

Scaling

X Min: kHz X Max: MHz Y min: dBc Y max: dBc

Update Reset to default

PLL Settings Spur Table

Filter type: Brickwall bandwidth: kHz

Delta Sigma order: VCO Frequency: MHz

Randomization: PLL N: + /

LOOPFILTER LOOPFILTER - Locktime

Loop Filter Preferences and Device Settings

Filter type: Basic **Advanced**

Filter Order:

Charge Pump Gain: mA

VCO Gain: 41.6 MHz/V

VCO Input Capacitance: pF

VCO Frequency: MHz

Phase Detector Frequency: MHz

Bode Plot

Get Chart Data

Click on the legend to turn on/off the series.

Loop Parameters

Design Targets		Actual
Loop Bandwidth	<input type="text" value="0.5"/> kHz <input type="checkbox"/> Auto	0.497 kHz
Phase Margin	<input type="text" value="60"/> deg <input type="checkbox"/> Auto	60.7 deg
T3/T1 Ratio	<input type="text" value="50"/> % <input checked="" type="checkbox"/> Auto	50.44009 %
Gamma	<input type="text" value="0.24"/> <input checked="" type="checkbox"/> Auto	0.22

Choose RC Components for me Help

Loop Filter Components

Capacitor Value Step:

C1: 560 nF Calculate Integrated

C2: 27000 nF Calculate

C3: 68 nF Calculate Integrated

Resistor Value Step:

R2: 0.027 kΩ Calculate Integrated

R3: 0.22 kΩ Calculate Integrated

Update Actual Loop Parameters

Scaling

X Min: kHz X Max: MHz Y min: dBc Y max: dBc

Update Reset to default