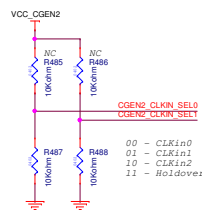
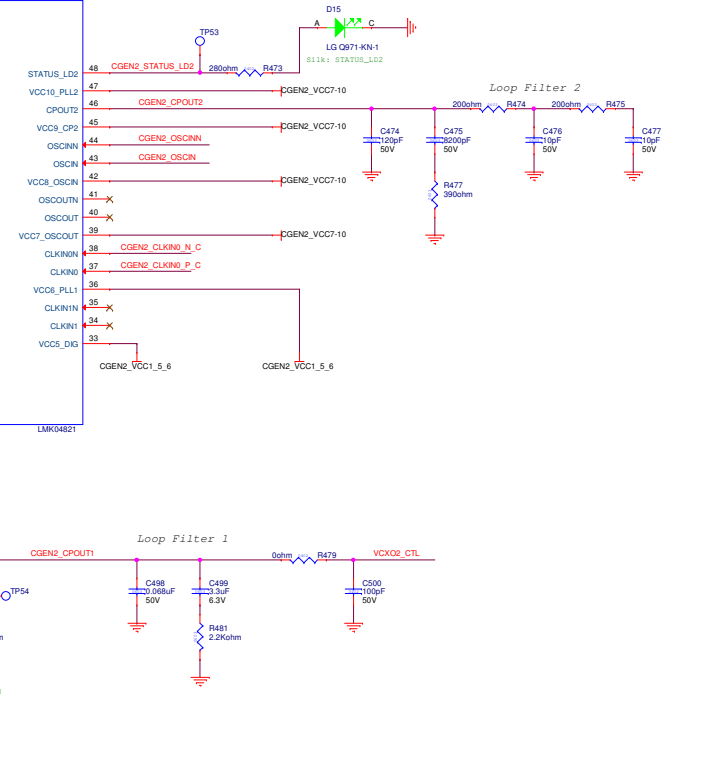
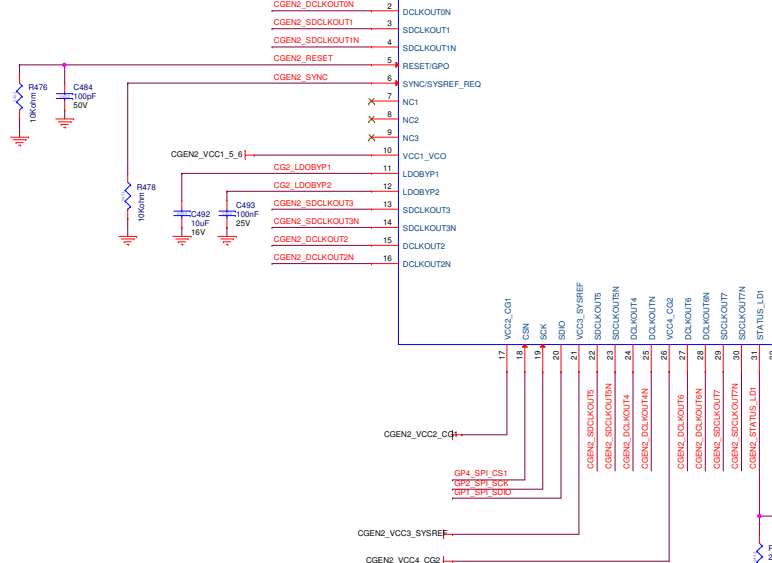
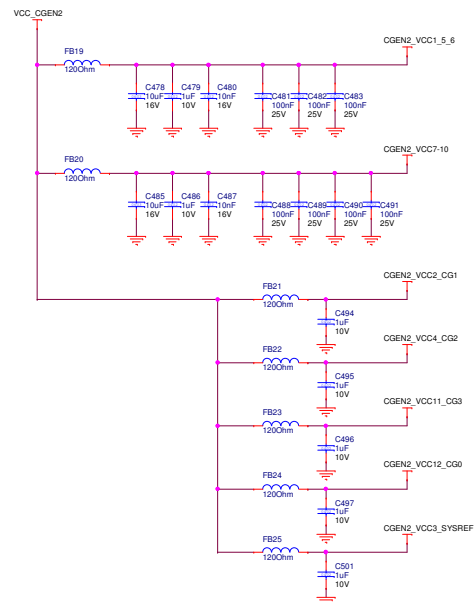
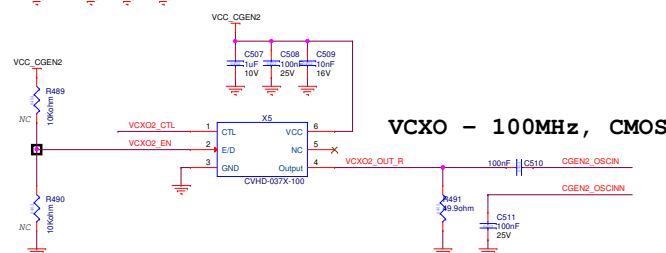
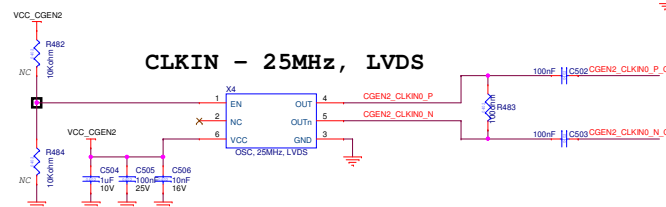


GP1_SPI_SDO GP1_SPI_SDO
GP2_SPI_SCK GP2_SPI_SCK
GP4_SPI_CS1 GP4_SPI_CS1



00 - CLKIn0
01 - CLKIn1
10 - CLKIn2
11 - Holdover



SYSCLK CGEN2_DCLKOUT2 CGEN2_DCLKOUT2
DDRCLK CGEN2_DCLKOUT4 CGEN2_DCLKOUT4
SERDESx CGEN2_DCLKOUT8 CGEN2_DCLKOUT8
SERDESx CGEN2_DCLKOUT10 CGEN2_DCLKOUT10
SERDESx CGEN2_DCLKOUT11 CGEN2_DCLKOUT11
SERDESx CGEN2_DCLKOUT13 CGEN2_DCLKOUT13