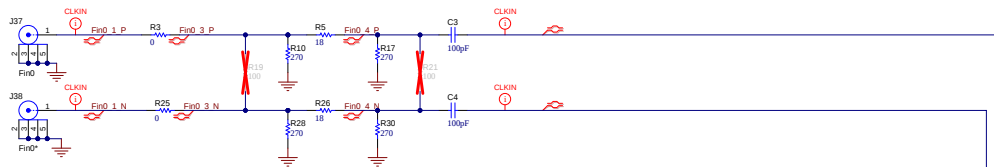
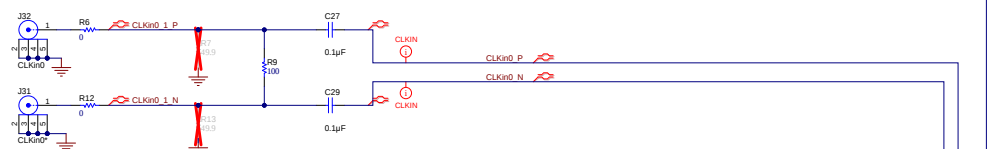


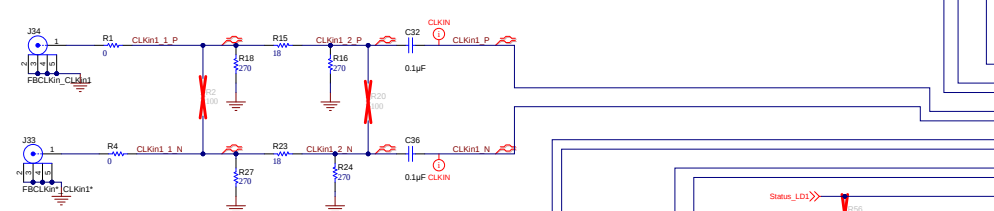
Fin0



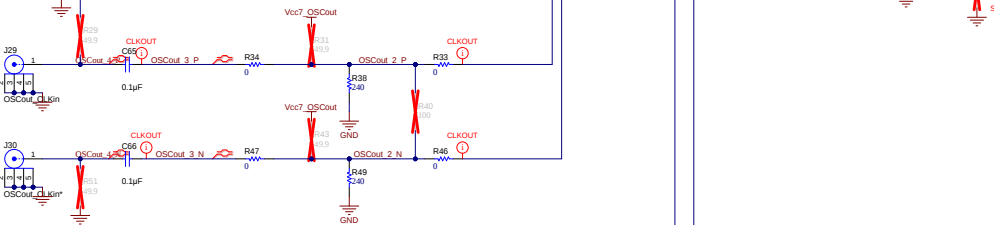
CLKin0



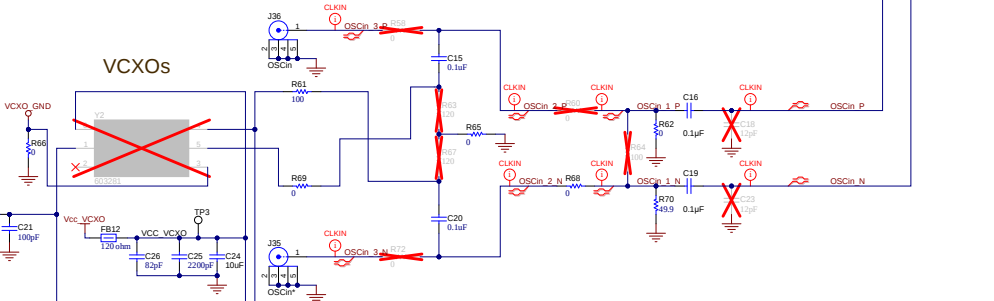
CLKin1/Fin1/FBCLKin



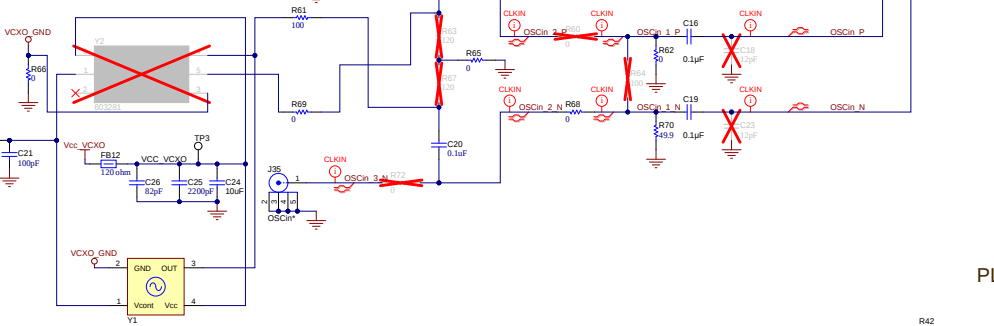
OSCOut/CLKin2



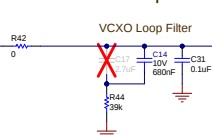
OSCin



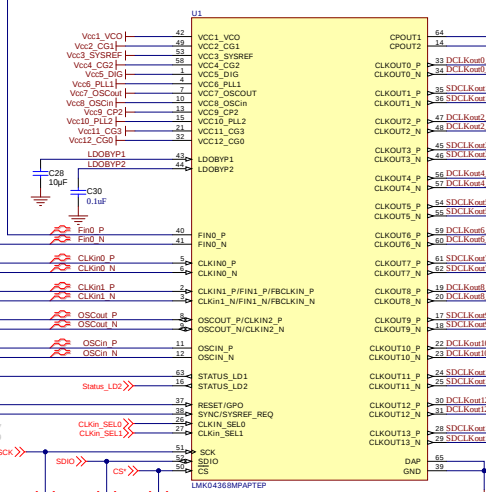
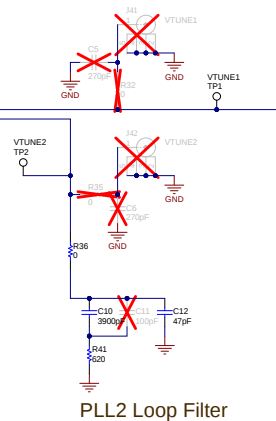
VCXOs



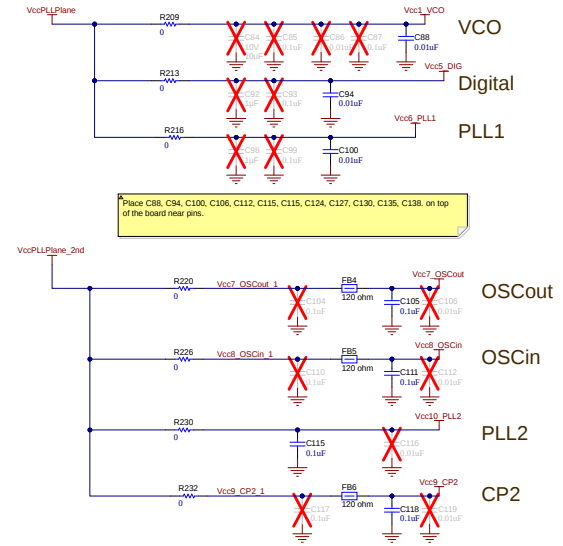
PLL1 Loop Filter



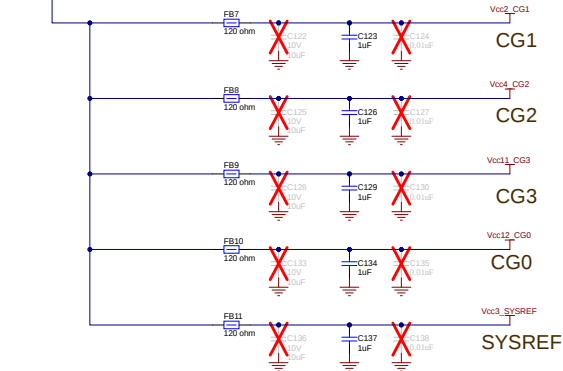
PLL2 Loop Filter



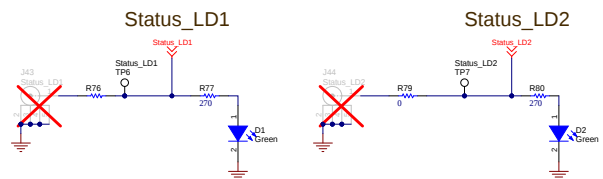
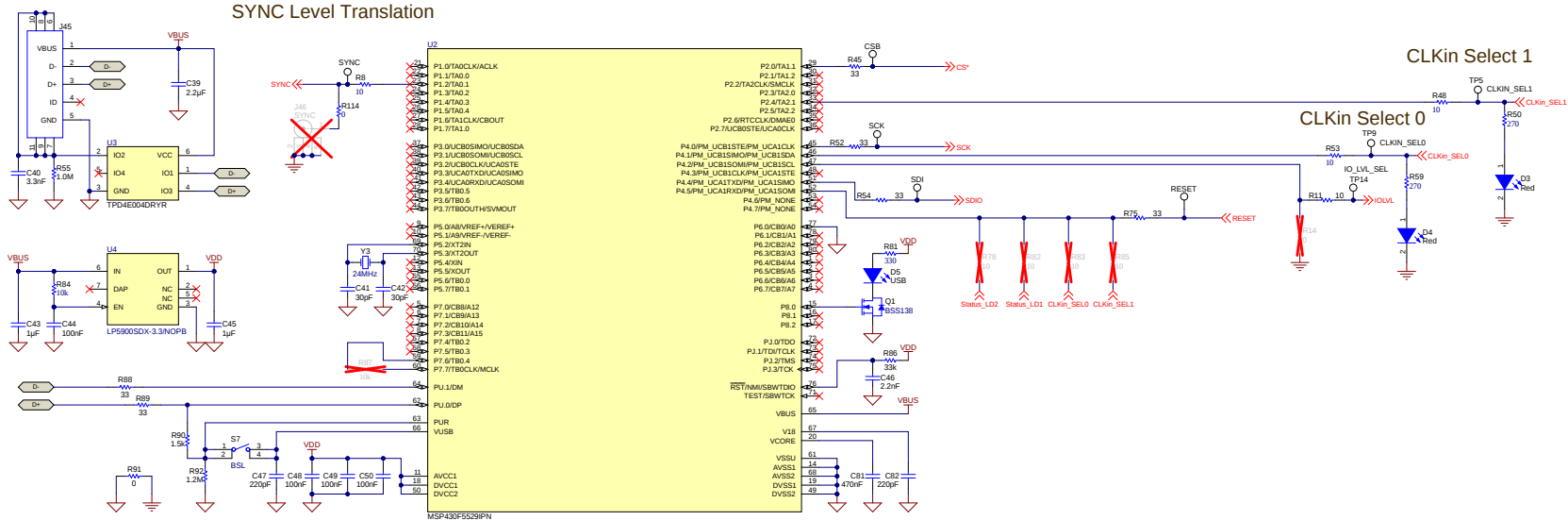
Power Plane for LMK Except Outputs



Power Planes for LMK Outputs

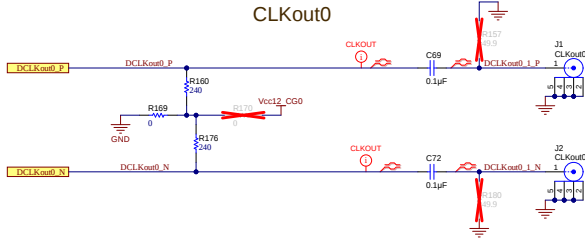


Regulator to power VCXO Separately

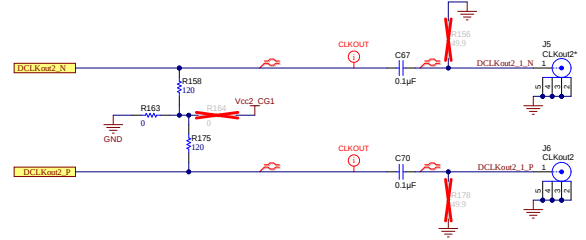


EVEN CLOCK OUTPUTS

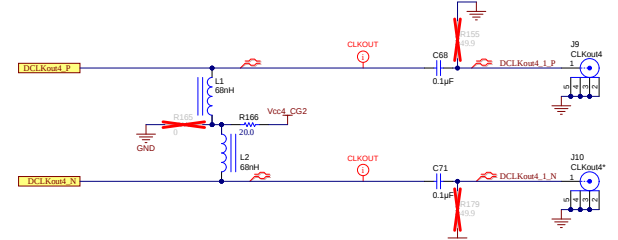
CLKout0



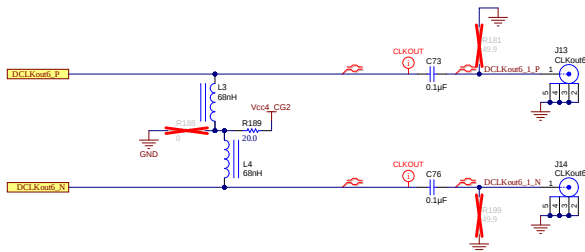
CLKout2



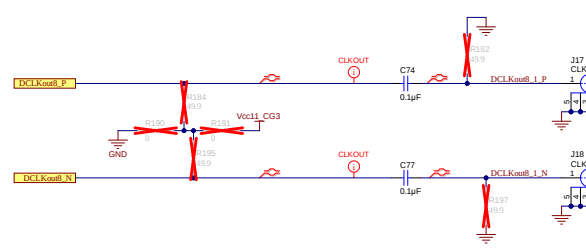
CLKout4



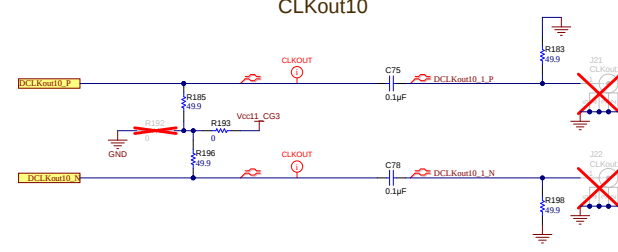
CLKout6



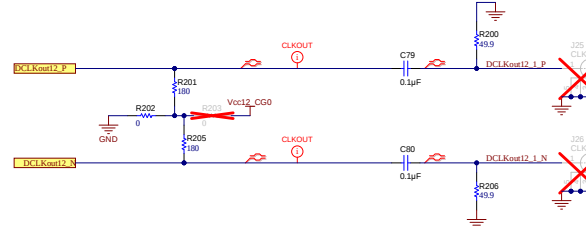
CLKout8



CLKout10

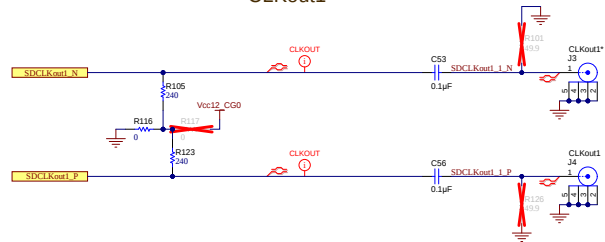


CLKout12

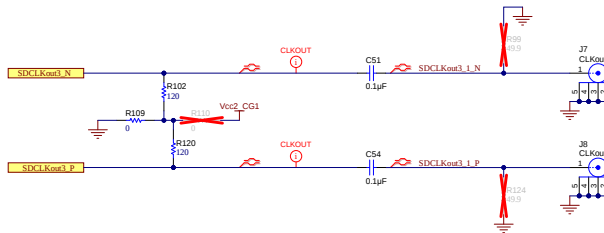


ODD CLOCK OUTPUTS

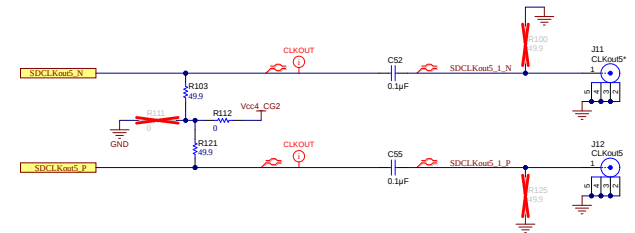
CLKout1



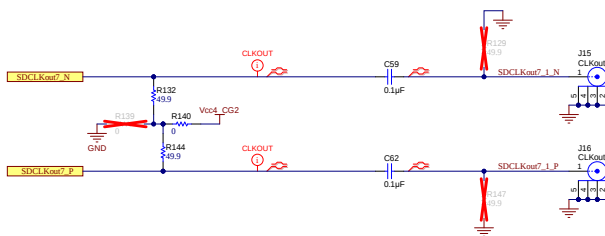
CLKout3



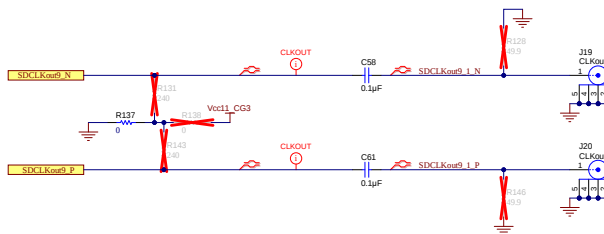
CLKout5



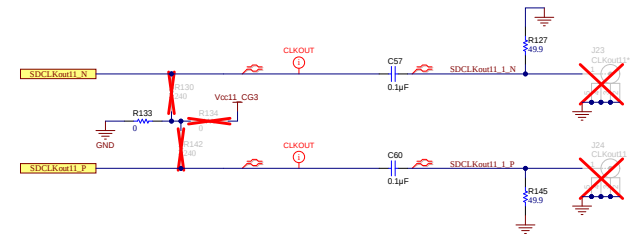
CLKout7



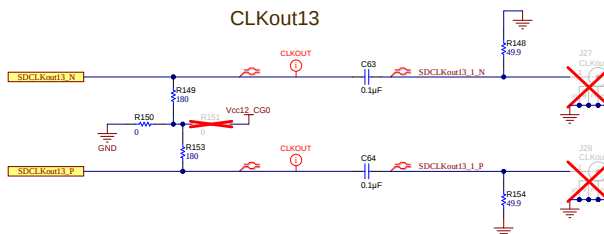
CLKout9

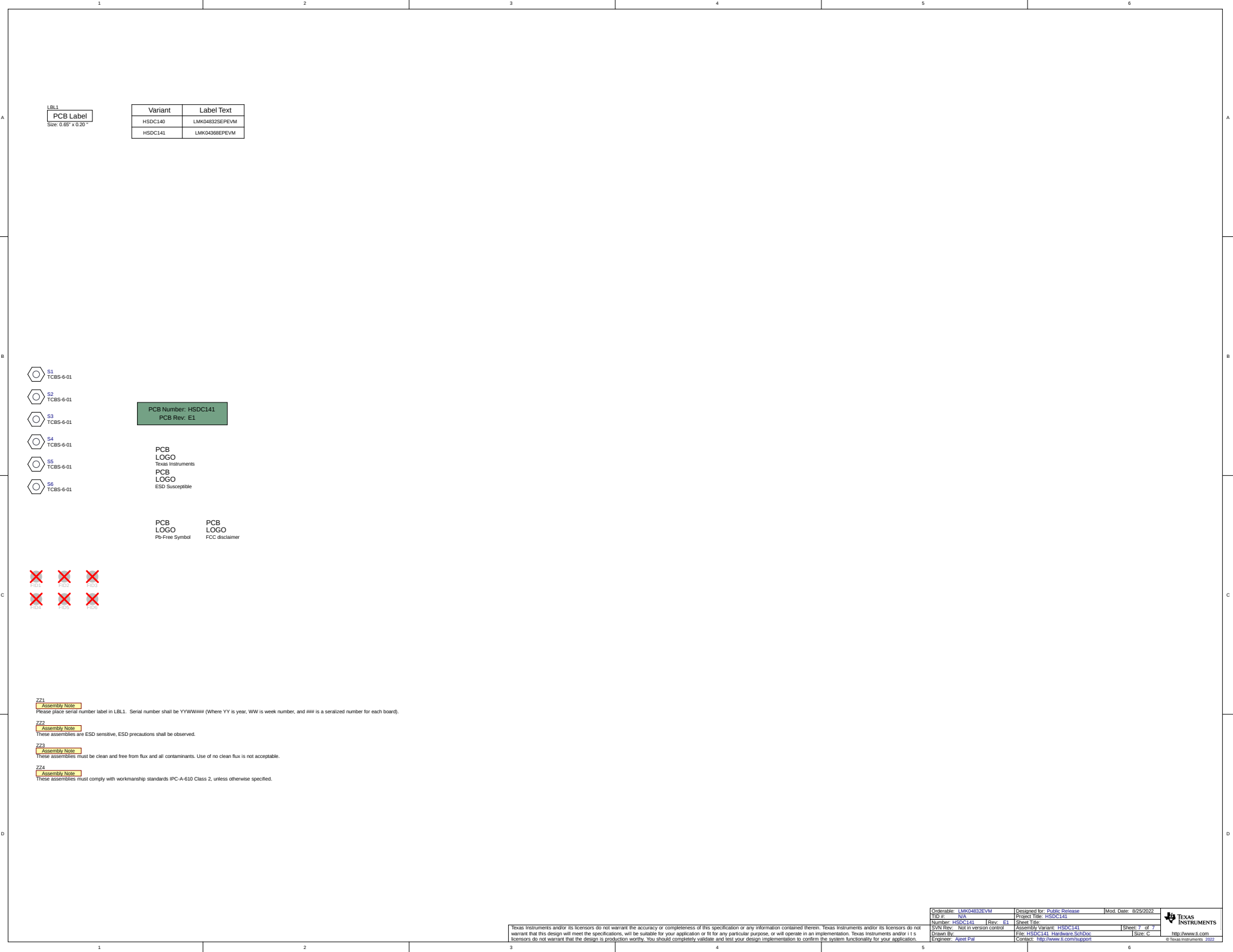


CLKout11



CLKout13





LBL1
PCB Label
Size: 0.65" x 0.20"

Variant	Label Text
HSDC140	LMK04832SEPEVM
HSDC141	LMK04898EPEVM

PCB Number: HSDC141
PCB Riv: E1

PCB
LOGO
Texas Instruments
PCB
LOGO
ESD Susceptible

PCB
LOGO
Pb-Free Symbol
PCB
LOGO
FCC disclaimer



- 721
Assembly Note
Please place serial number label in LBL1. Serial number shall be YYWW### (Where YY is year, WW is week number, and ### is a serialized number for each board).
- 722
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.
- 723
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- 724
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



